

Service Manual

ORDER NO. ARP3519

KRP-M01

THIS MANUAL IS APPLICABLE TO THE FOLLOWING MODEL(S) AND TYPE(S).

Model	Туре	Power Requirement	Remarks
KRP-M01	LFTXJ	AC 110 V to 240 V	
KRP-M01	WAXJ5	AC 220 V to 240 V	

This service manual should be used together with the following manual(s).

Model No.	Order No.	Remarks	
KRP-M01	ARP3508	BLOCK DIAGRAM, ADJUSTMENT, etc.	

For SPECIFICATIONS and PANEL FACILITIES, refer to the operating instructions.

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Ε

1. BASIC ITEMS FOR SERVICE

1.1 QUICK REFERENCE

Quick Reference upon Service Visit 1 Notes, PD/SD diagnosis, and methods for various settings

Notes when visiting for service

1. Notes when disassembling/reassembling

1) Rear case

When reassembling the rear case, the screws must be tightened in a specific order. Be careful not to tighten them in the wrong order forcibly. For details, see "Rear Case" in "5. DISASSEMBLY".

2 Attaching screws for the HDMI and system cable terminals When attaching the HDMI and system cable terminals after replacing the Assembly, secure the terminals manually with a screwdriver, but not with an electric screwdriver.

If you tighten the screws too tightly with an electric screwdriver, the screw heads may be damaged, in which case the screws cannot be untightened/tightened any more.

2. On parts replacement

1) How to discharge before replacing the Assys

A charge of significant voltage remains in the Plasma Panel even after the power is turned off. Safely discharge the panel before replacement of parts, in either manner indicated below:

A: Let the panel sit at least for 3 minutes after the power is turned off. B: Turn the Large Signal System off before the power is turned off then, after 1 minute, turn the power off.

For details, see "5.6 [1] PANEL DRIVE-POWER ON/OFF FUNCTION"

2 On the settings after replacement of the Assys Some boards need settings made after replacement of the Assys. For details, see "8. EACH SETTING AND ADJUSTMENT".

3. On various settings

1) Setting in Factory mode

After a Mask indication into the panel is performed, be sure to set the Mask setting to "OFF" then exit Factory mode.

	PD	SD			
No. of LEDs flashing	MR	Panel	No. of LEDs flashing	MR	
Red 1	MR_POWER	SQ_LSI	Blue 1		
	Panel	Module Device	Blue 2		
Red 2	POWER	communication			
Red 3	SCAN	DIGITAL-RST2	Blue 3		
Red 4	SCN-5V	Panel temperature	Blue 4		
Red 6	Y-DCDC	Audio	Blue 5	Audio (MSP)	
Red 7	Y-SUS	Module microcomputer communication	Blue 6		
Red 8	ADRS		Blue 7	Main 3-wire serial communication	
Red 10	X-DCDC	Panel main IIC	Blue 8	Main IIC communication	
Red 11	X-SUS	communication			
Red 12	DIG-DCDC		Blue 9	Main microcomputer communication	
Red 15	UNKNOWN	FAN	Blue 10	FAN	
		Unit high temperature	Blue 11	Unit high temperature	
		Blue 12		D-TUNER communication	
		DC-IN Blue 13		RST2/RST4	
		Panel main EEPROM Blue 15		Main EEPROM	
	Special LED F	atterns		Subcategory confirmation	
	Panel	MR		procedure	
` - /		PD (1) B R • • • O		If the DISPLAY key is pressed during shutdown, the orange LED flashes. (MR only)	
SD (1-15)		SD (7-15)		SD Subcategory	
System fa	ilure B	Standalone operation B R (MRMS01)		1 Tuner 1	
		(MHMSUT)		2 MSP/MAP	

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PD (2-15)	B R		•••	PD (1)	B R O		•	duri	ng s	ISPLAY key is pressed shutdown, the orange shes. (MR only)
SD (1-15)	R			SD (7-15)	R			SD	S	D Subcategory
System failure	B R	_		Standalone operation	B =				1	Tuner 1
L				(MRMS01)	ö				2	MSP/MAP
MR on standby	(R	ed LE	D lit)		B				3	AV Switch
Rewriting of	В			software (PC)	Ö				4	RGB Switch
software (PC)	R	***	••••	Rewriting of	В		•••	8	5	Main VDEC
NO	В	••••	••••	software (USB)	Ö •				6	VDEC-SDRAM
BACKUP				After rewriting is comp	oleter	d suc	cess-		7	AD/PLL
			fully, the orange LED goes dark.			8	HDMI			
For enecial natterns	oth	or tha	n	Rewriting of software B			9	Display Port Tx		
For special patterns other than described here, see 3.1[1].			failed (USB)	R			13		RST2	
, 11			` ′	Ĕ				2	RST4	
Commands for shifting between standalone and system operations					ions			D main categories		
Panel			MR		have subcategories. For details, see 3.3 [2].					
To Standalone operation: SYSS00 To Standalone operation: N		: MRI	VIS01	1.01	GOIC	2110, 000 0.0 [2].				
To System operation: SYSS01		To System operation: MRMS00								
Note: After issuing a command, unplug then again plug										

How to locate several items on the Factory menu

Item on the Factory menu : Key on the remote control unit Screen indication

1. Confirmation of accumulated power-on time and power-on count Select {INFORMATION} then {HOUR METER}.

(After entering Factory mode, press [♣] four times.)

2. Confirmation of the Power-down and Shutdown histories

① Panel system

PD: Select {PANEL FACTORY} then {POWER DOWN}. (After entering Factory mode, press [MUTING] once, press [ENTER/SET], then press [] two times.)
Select {PANEL FACTORY} then {SHUT DOWN}.

(After entering Factory mode, press [MUTING] once, press [ENTER/SET], then press [♣] three times.)

② MR section

Select (INFORMATION) then (MAIN NG) (After entering Factory mode, press [] three times.)

3 Panel main section

Select {PANEL MAIN FACTORY} then {PM NG INFO}. After entering Factory mode, press [MUTING] twice, then press [ENTER/SET].

3. How to display the Mask indication

Mask indication in the panel side

 Select {PANEL FACTORY} then {RASTER MASK SETUP}. (After entering Factory mode, press [MUTING] once, press [ENTER/SET], then press [] 8 times.)

Press [ENTER/SET], then select a Mask indication, using [♠] or [♣].

Adjustments and Settings after replacement of the Assys (Procedures in Factory mode)

in the AC power cord

- DIGITAL Assy (Panel): Transfer of backup data
 Select {PANEL FACTORY}, {ETC}, then {BACKUP DATA}. (After entering Factory mode, press [MUTING] once, press [ENTER/SET], press [♣] seven times, then press [ENTER/SET].)
 - ② Select {TRANSFER}, using [→], then hold [ENTER/SET] pressed for at least 5
 - 3 After transfer of backup data is completed, {ETC} is automatically selected, and the LED on the front panel returns to normal lighting.

2. MAIN BLOCK Assy (MR), MAIN Assy (Panel): Execution of FINAL SETUP.

- ① Select {INITIALIZE} then {FINAL SETUP}, then press [ENTER/SET]. (After entering
- Factory mode, press [MUTING] four times, then press [♣] once.)
 ② Select "YES", using [♣]. Then hold [ENTER/SET] pressed for at least 5 seconds.
 ③ After "FINAL SETUP IS COMPLETE" is displayed on the screen, turn the POWER switch of the main unit off.

3. POWER SUPPLY Unit (Panel): Clearance of the accumulated power-on

- count and maximum temperature value

 ① Select {PANEL FACTORY}, {ETC}, then {P COUNT INFO}. (After entering Factory mode, press [MUTING] once, press [ENTER/SET], press [♣] seven times, press
- [ENTER/SET], then press [♣] six times.)

 ② Press [♣] to select "CLEAR". Hold [ENTER/SET] pressed for at least 5 seconds.

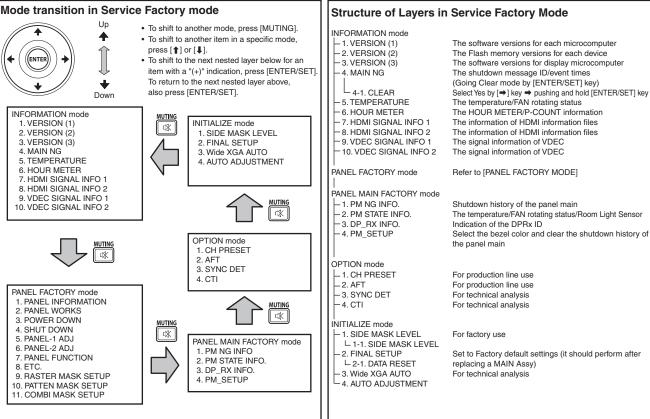
 After clearance is completed, "ETC" is automatically selected. Clear the maximum temperature value (MAX TEMP) in the same manner.

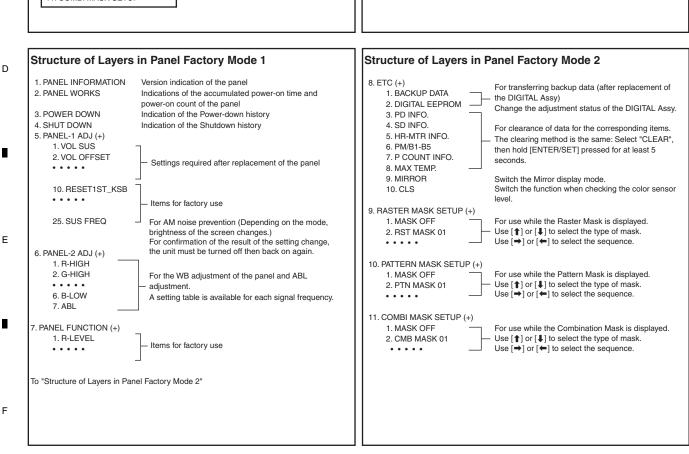
4. Other Assys (Panel): Clearance of the maximum temperature value

- ① Select {PANEL FACTORY}, {ETC}, then {MAX TEMP}. (After entering Factory mode, press [MUTING] once, press [ENTER], press [♣] seven times, press [ENTER/SET], then press [1] seven times.)
- ② Press [→] to select "CLEAR". Hold [ENTER/SET] pressed for at least 5 seconds After clearance is completed, "ETC" is automatically selected.

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Quick Reference upon Service Visit ② Mode transition and structure of layers in Service Factory mode





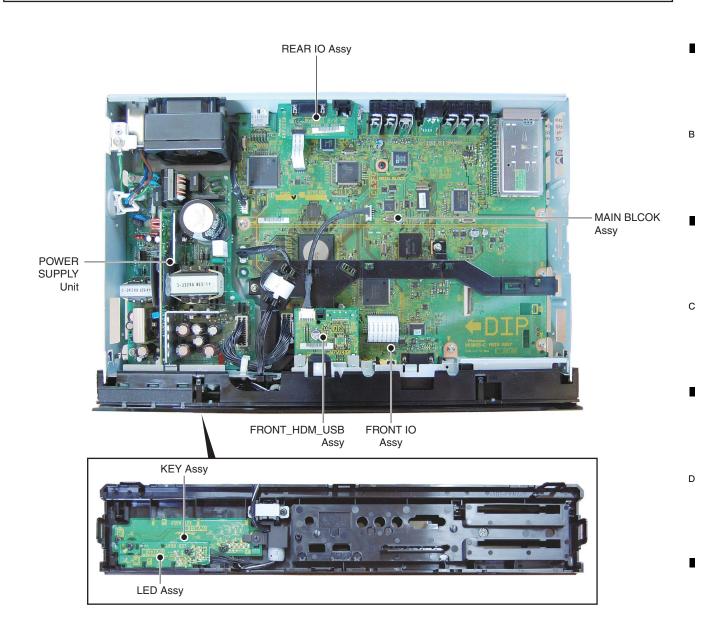
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Note: The wiring shown in the photo is different from the actual wiring, because the product in the photo is a prototype. Upon servicing, be sure to restore the original wiring of the unit after repair work.



 $NOTES: \bullet \textit{Parts marked by "NSP" are generally unavailable because they are not in our \textit{Master Spare Parts List}.$

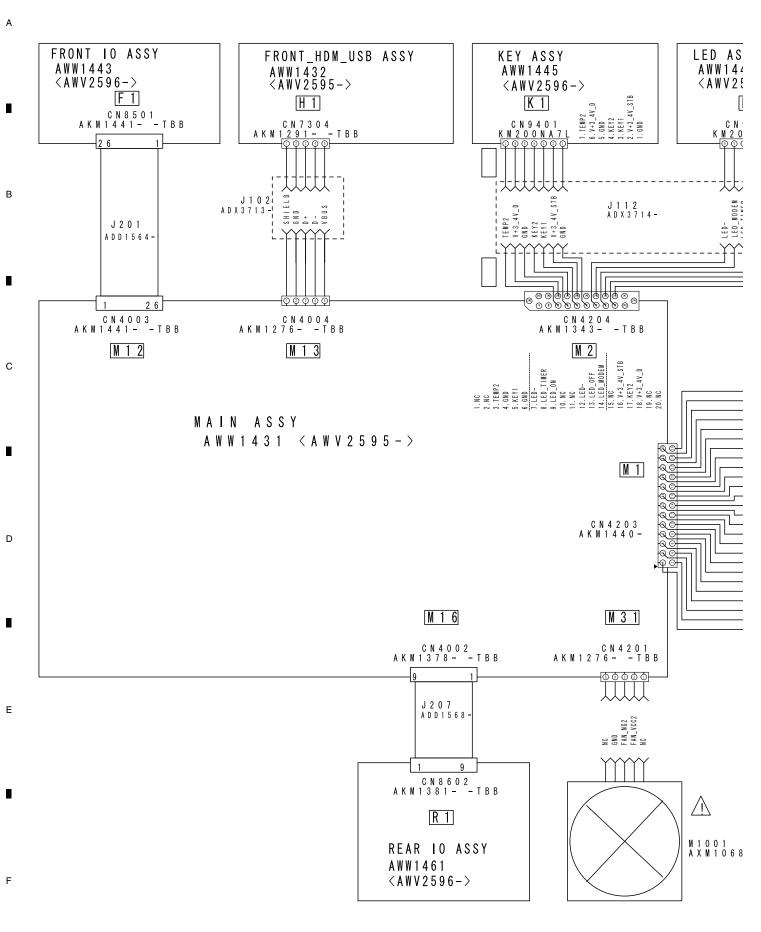
• The \triangle mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.

Mark		Part No.	Mark	No.	Description	Part No.	
<u>LIST</u>	OF ASSEMBLIES						
NSP	1MAIN ASSY	AWV2595	NSP	1FUK	(UGO ASSY	AWV2596	
	2FRONT_HDM_USB ASSY	AWW1432		2R	EAR IO ASSY	AWW1461	
	2MAIN BLOCK ASSY	AWW1431		2LE	ED ASSY	AWW1442	
				2FF	RONT IO ASSY	AWW1443	
				2KE	EY ASSY	AWW1445	F
			\triangle	1POV	VER SUPPLY UNIT	AXY1223	

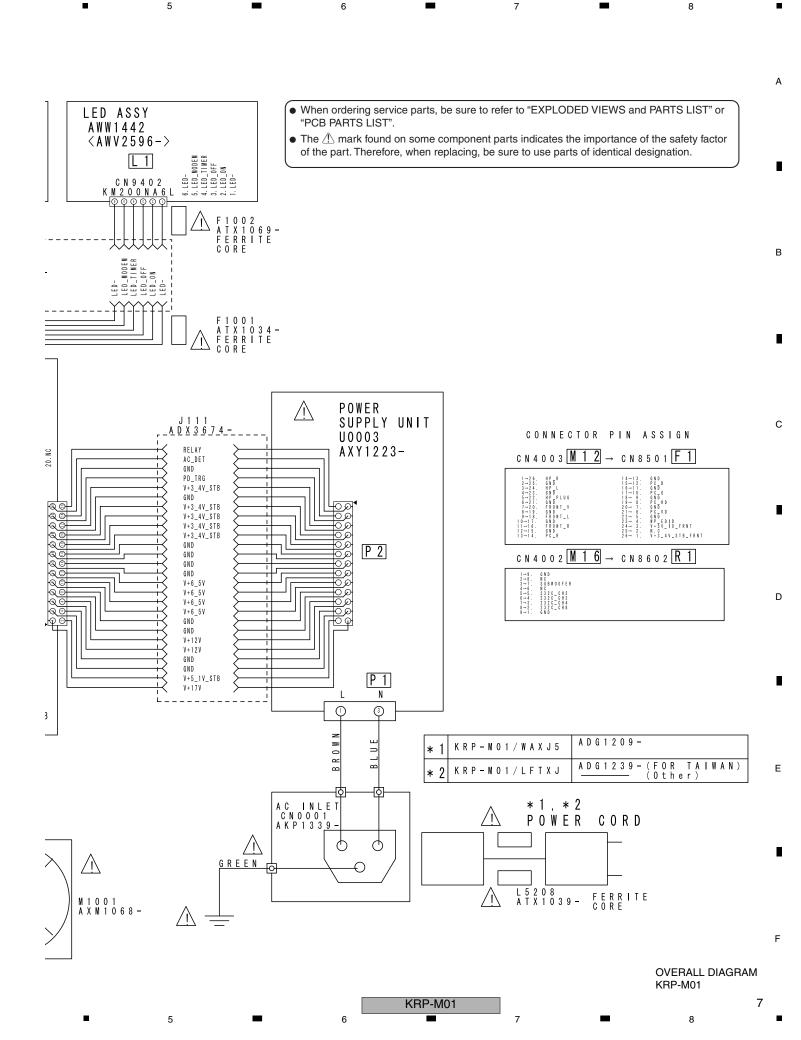
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2.1 OVERALL WIRING DIAGRAM



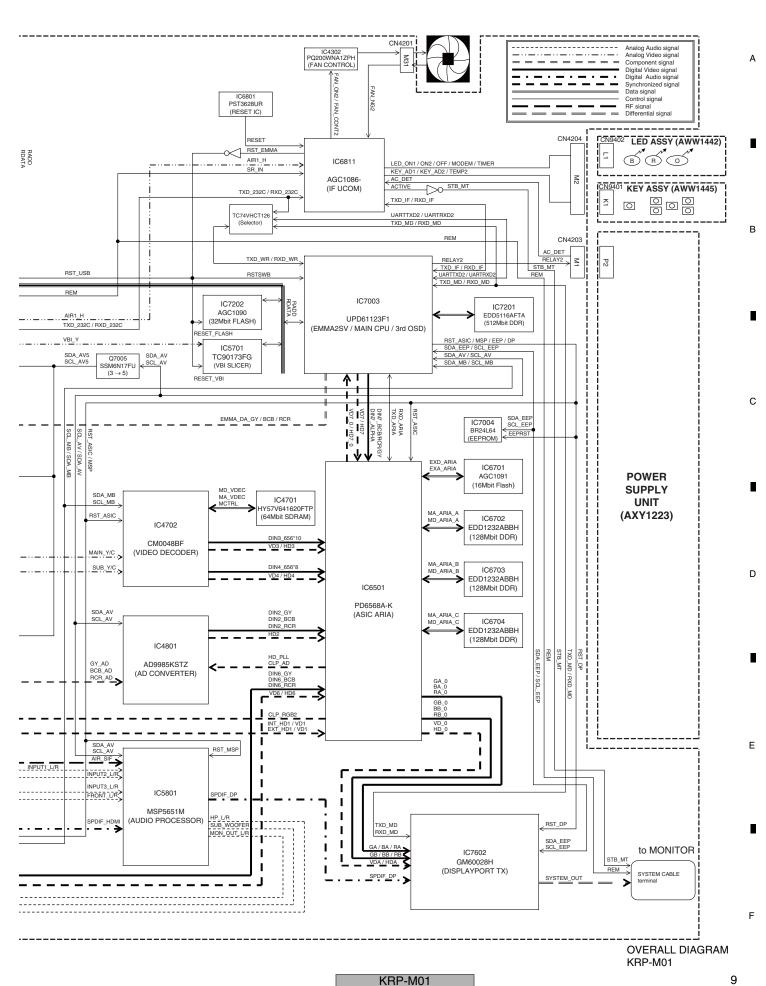
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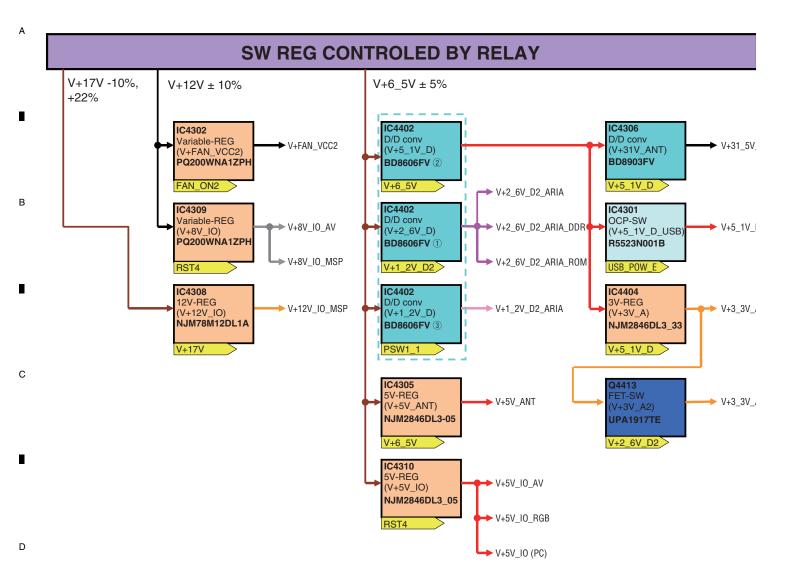
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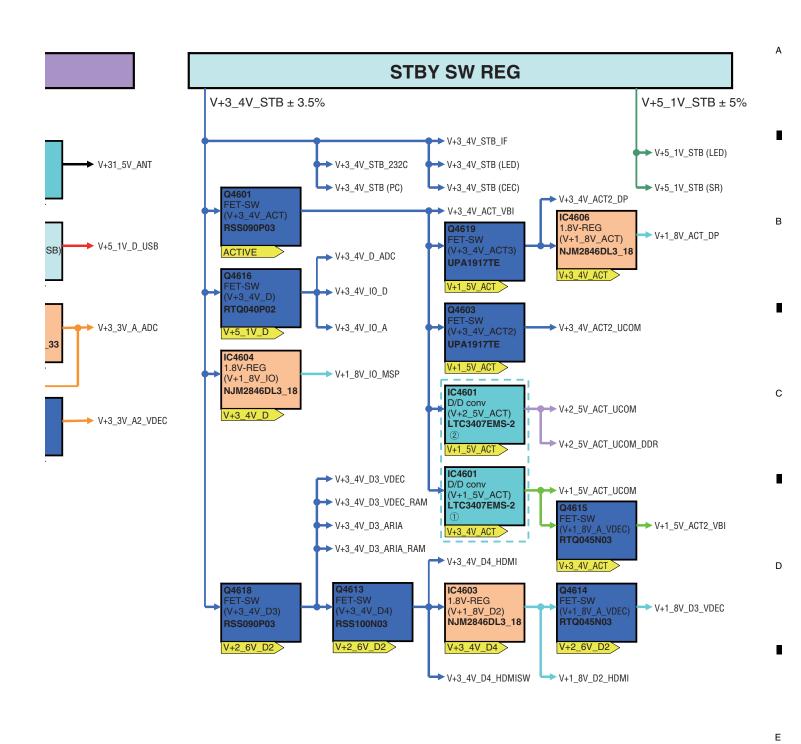
2.3 POWER SUPPLY BLOCK of MAIN BLOCK ASSY



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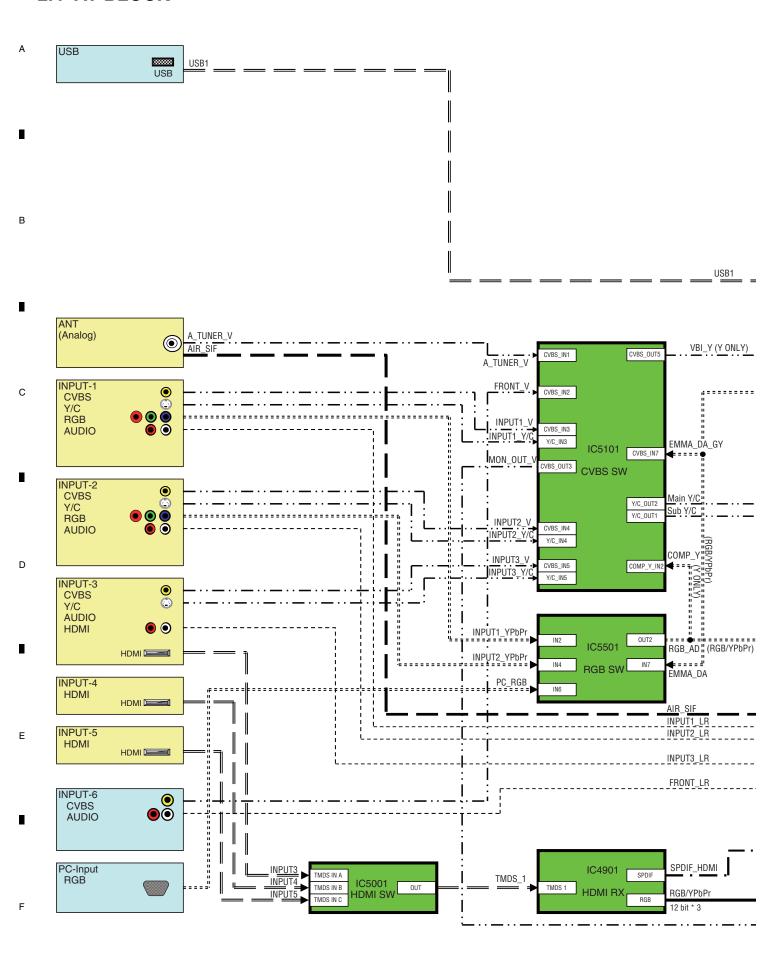
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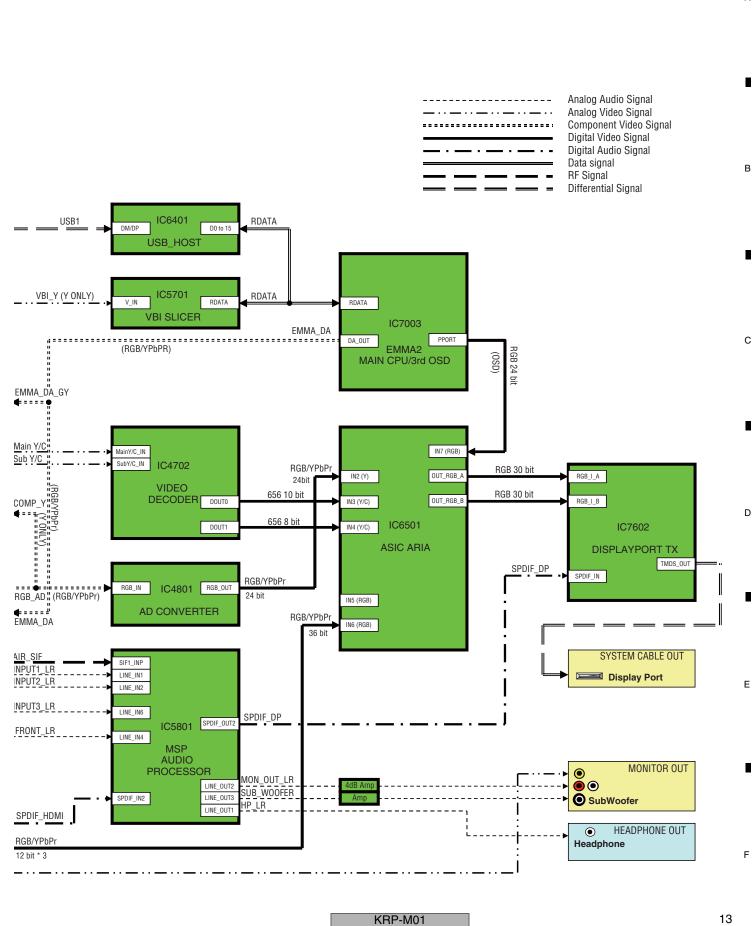


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2.4 AV BLOCK



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3. DIAGNOSIS

Following item is same as the KRP-M01/WYSIXK5.

OUTLINE OF RS-232C COMMAND

3.1 POWER SUPPLY OPERATION

Following item is same as the KRP-M01/WYSIXK5.

[2] POWER ON SEQUENCE

[1] LED DISPLAY INFORMATION

■ LED Pattern

Status	LED		LE	D Pattern / Remarks	3		
Standby Power Management	Blue Red Orange						
Power On	Blue Red Orange						
Power-Down	Blue Red Orange	Once 500ms	Twice	n times	2.5s	Once	*1
Shutdown	Blue Red Orange	500ms Once	Twice	n times	2.5s	Once	*2
Shutdown (Subcategory flashing)	Blue Red Orange	500ms Once 500ms	Twice	n times	2.5s	Once	*2 *3
No digital adjustment data copied for backup	Blue Red Orange	200ms					
Updating the PC	Blue Red Orange	100ms 100ms		******	******		
During factory operation	Blue Red Orange						
Power ON of standalone mode (Screen ON)	Blue Red Orange	1000msec 1000r 1000msec	nsec 1000msec	_	_		
Mode switch of system / standalone operation	Blue Red Orange	200ms					
Sleep timer	Blue Red Orange						

*1: Notify upon the power-down content by Red LED flashing number of times.

*2: Notify upon the shutdown content by Blue LED flashing number of times.

*3: Notify upon the subcategory number by Orange LED flashing number of times.

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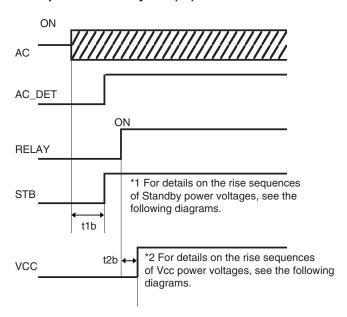
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[3] DETAILS OF POWER ON SEQUENCE

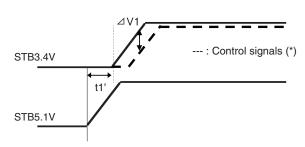
The rise of the output voltage is defined as the point at which 10% output voltage is reached.

1. Sequence of Relay ON (IN)



Γ	Relay ON					
	Item	Specified Time				
	AC to STB	t1b ≤ 0.8s				
Γ	RELAY to VCC	t2b ≦ 0.5s				

2. Rise sequence of Standby power voltages

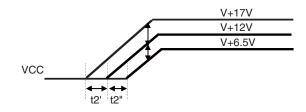


<Specified time and difference of voltages>

Rise					
Item	Specified Time				
STB5.1V to STB3.4V	-50ms ≦ t1' ≦ 50ms				
Item	Specified difference of voltages				
STB3.4V - Control signal (*)	0 V ≦ ΔV 1				

(*) Control signals (output signals) denote AC_DET and PD_TRG signals.

3. Rise sequences of Vcc power voltages



<Specified time of voltages>

Rise					
Item	Specified time (at nominal load)				
V+17V to V+12V	0ms ≤ t2' ≤ 10ms				
V+12V to V+6.5V	0ms ≤ t2" ≤ 10ms				

4. Specifications of the rise time of the output voltages (common to all sequences)

Note that there must not be any temporary voltage drop during rising.

Rise time (time required for reaching from 10% to 90% output voltage)					
Item Specified time					
STB 10% to STB 90% tr_STB ≦ 100ms					
VCC 10% to VCC 90% tr_VCC ≦ 200ms					

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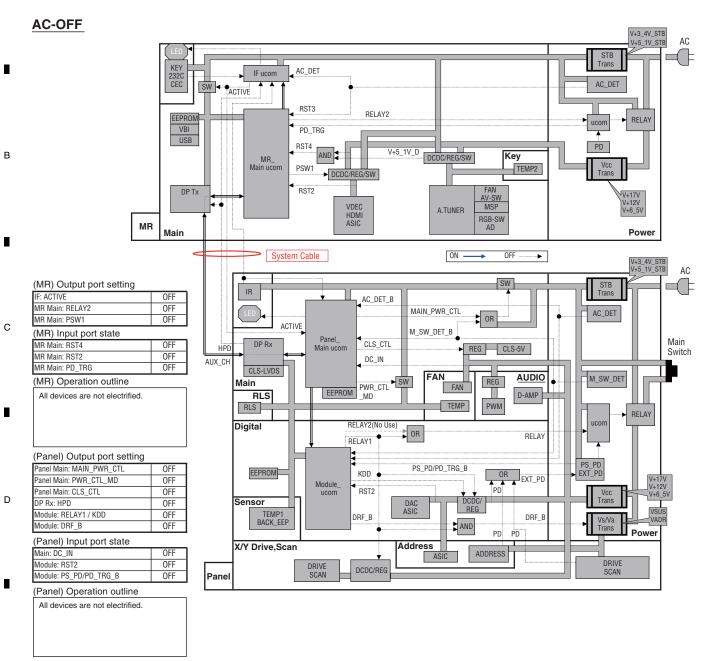
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DETAILS OF POWER ON SEQUENCE



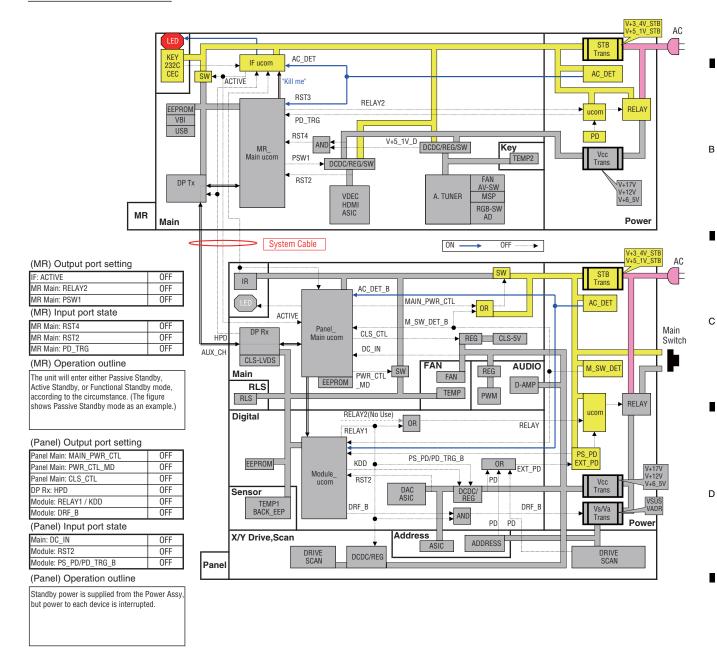
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Panel Main Power OFF



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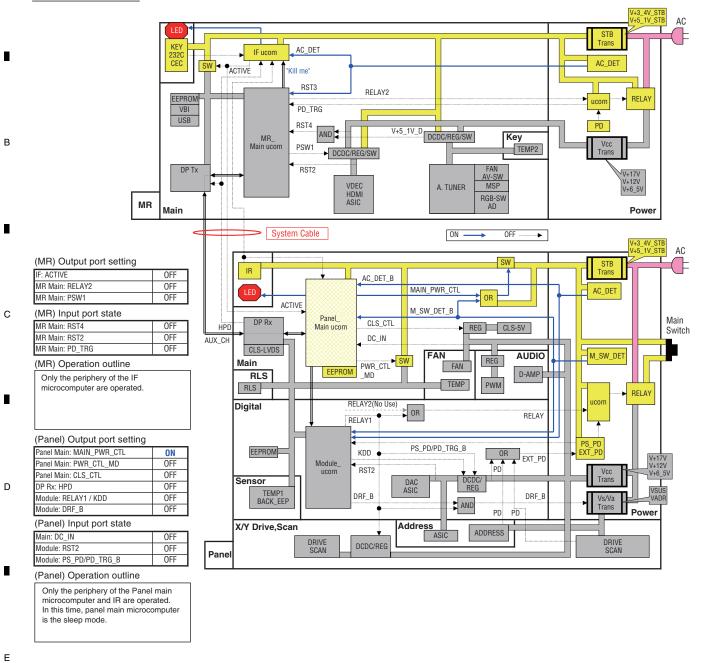
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Passive Standby

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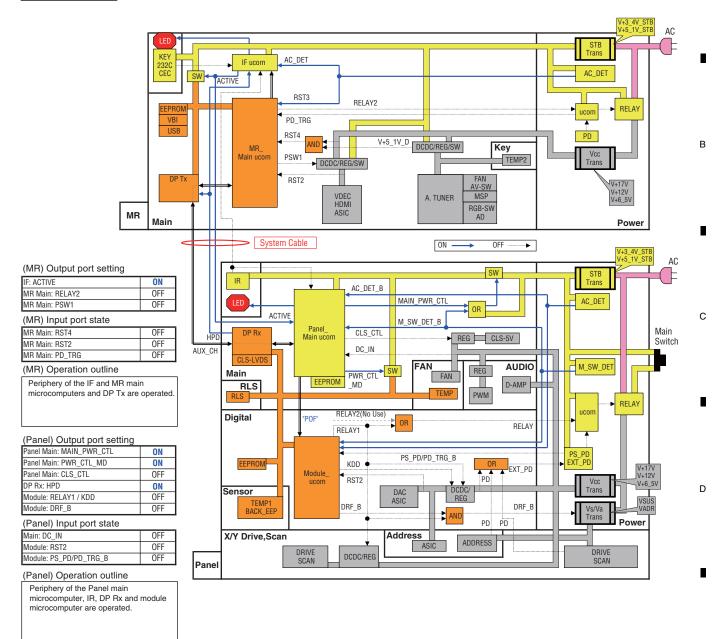


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Active Standby



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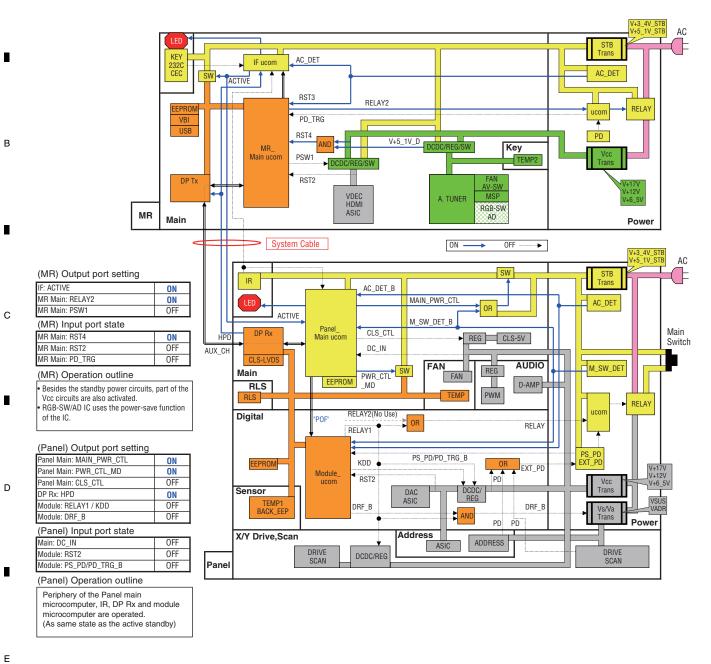
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Function Standby

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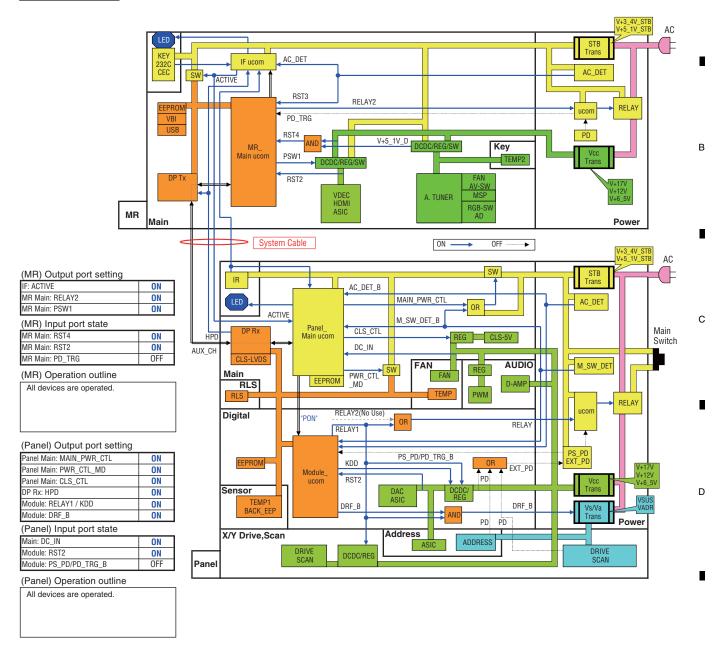
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PDP Screen ON

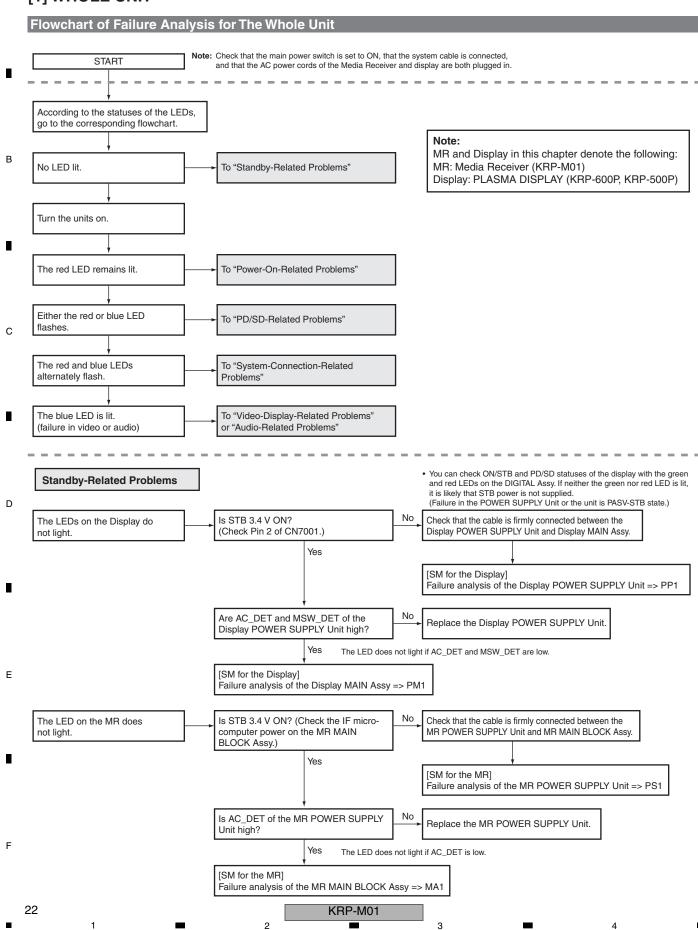


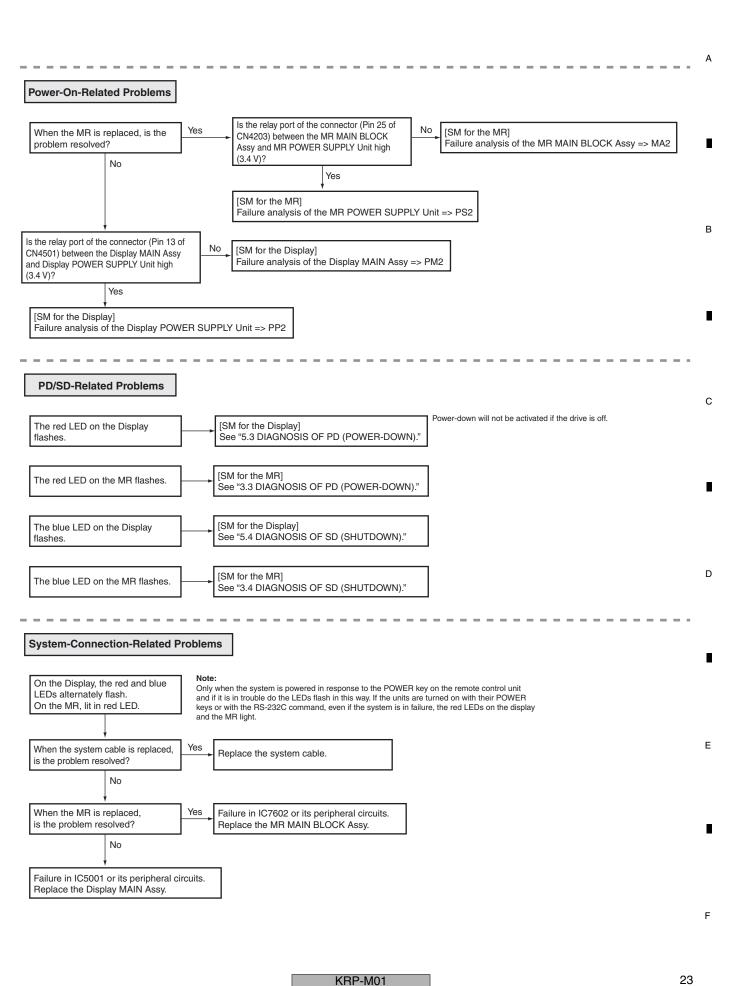
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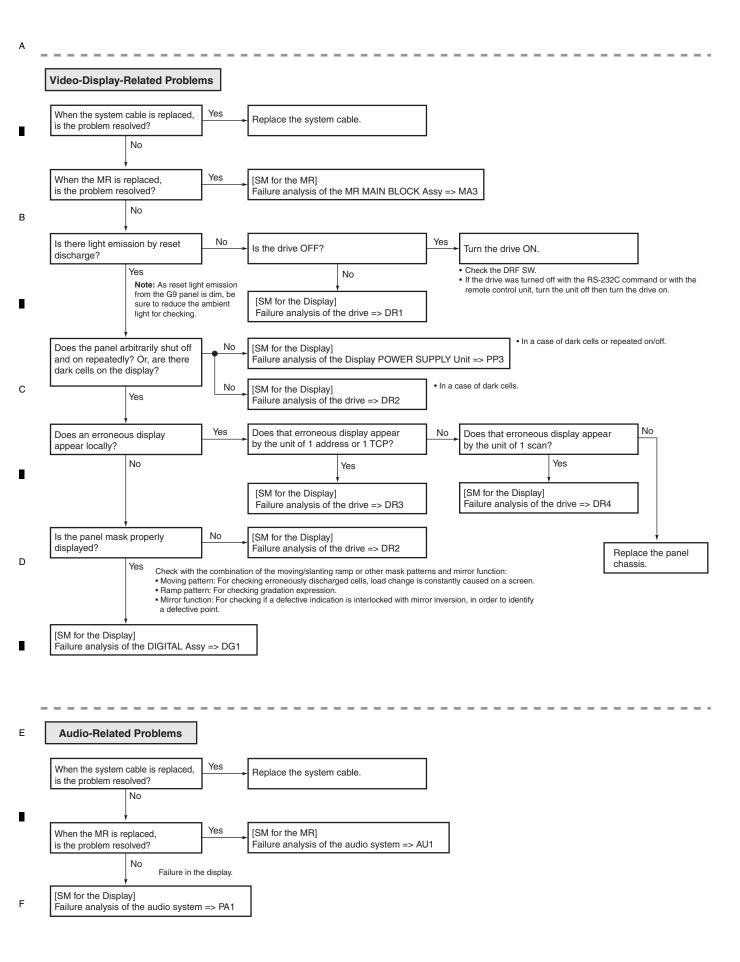
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3.2 DIAGNOSIS FLOWCHART OF FAILURE ANALYSIS

[1] WHOLE UNIT



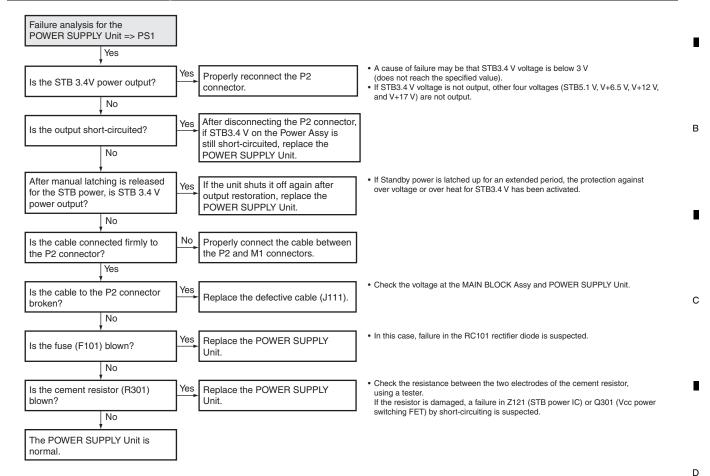


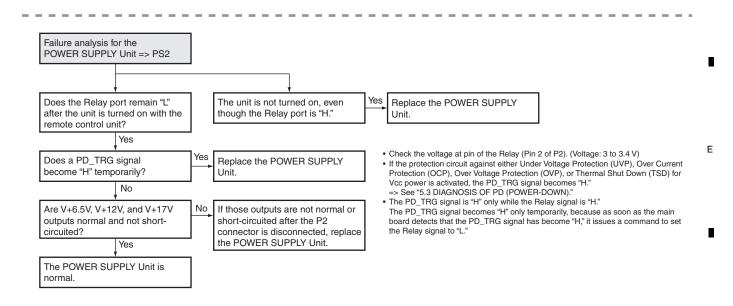


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[2] POWER SUPPLY UNIT

Flowchart of Failure Analysis for The POWER SUPPLY Unit





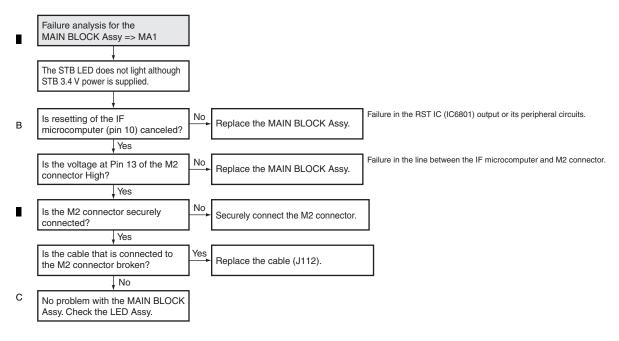
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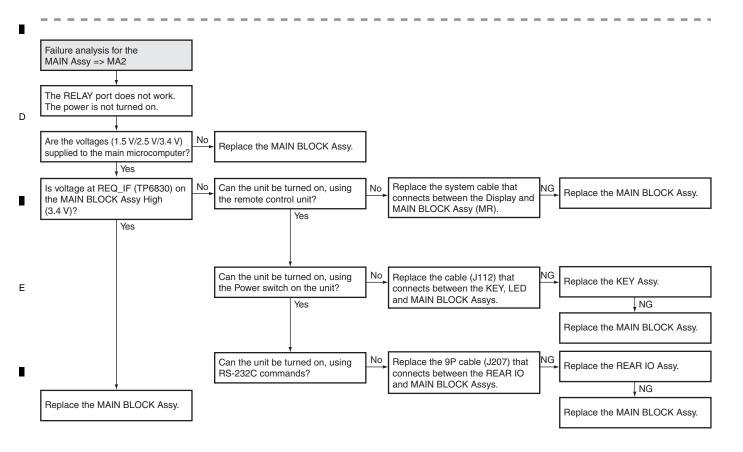
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[3] MAIN BLOCK ASSY

Flowchart of Failure Analysis for The MAIN BLOCK Assy



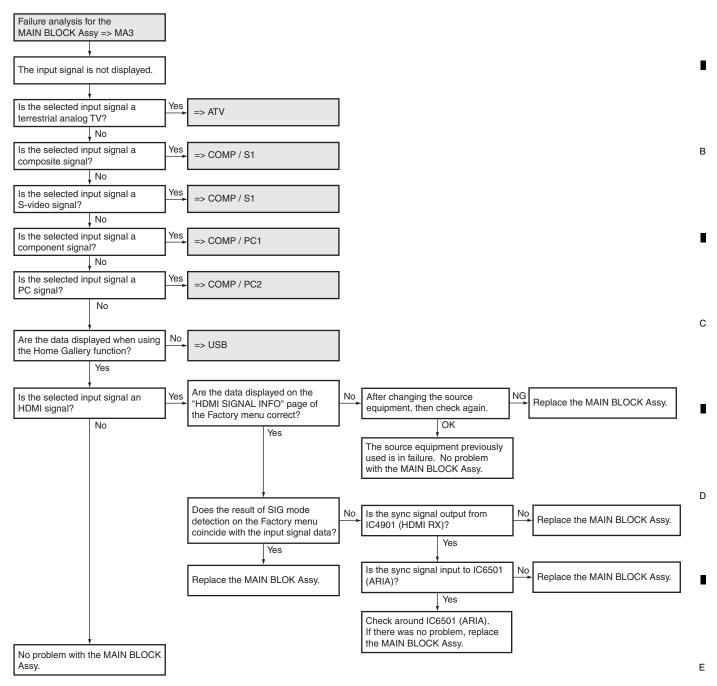


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Flowchart of Failure Analysis for The MAIN BLOCK Assy



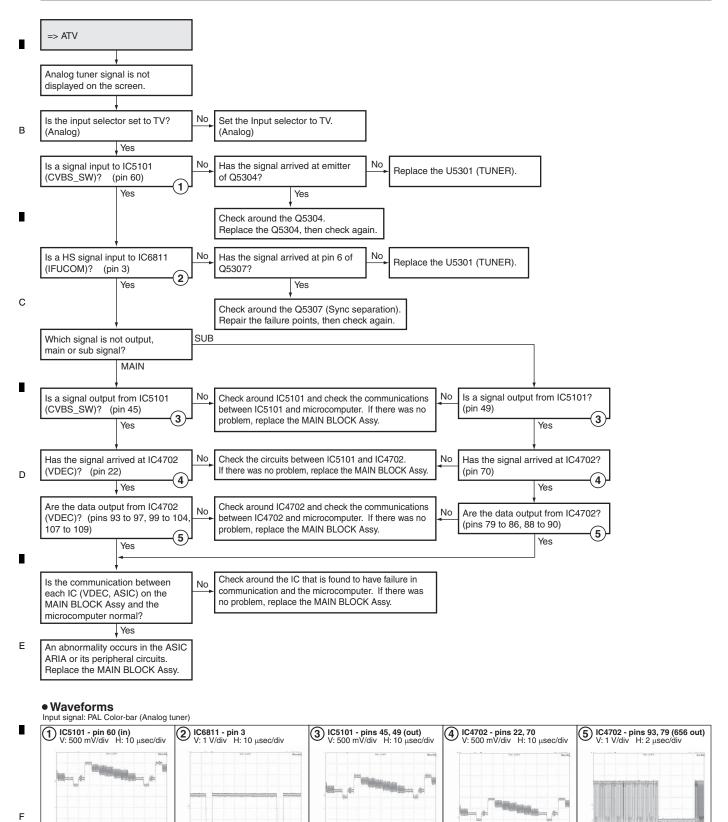
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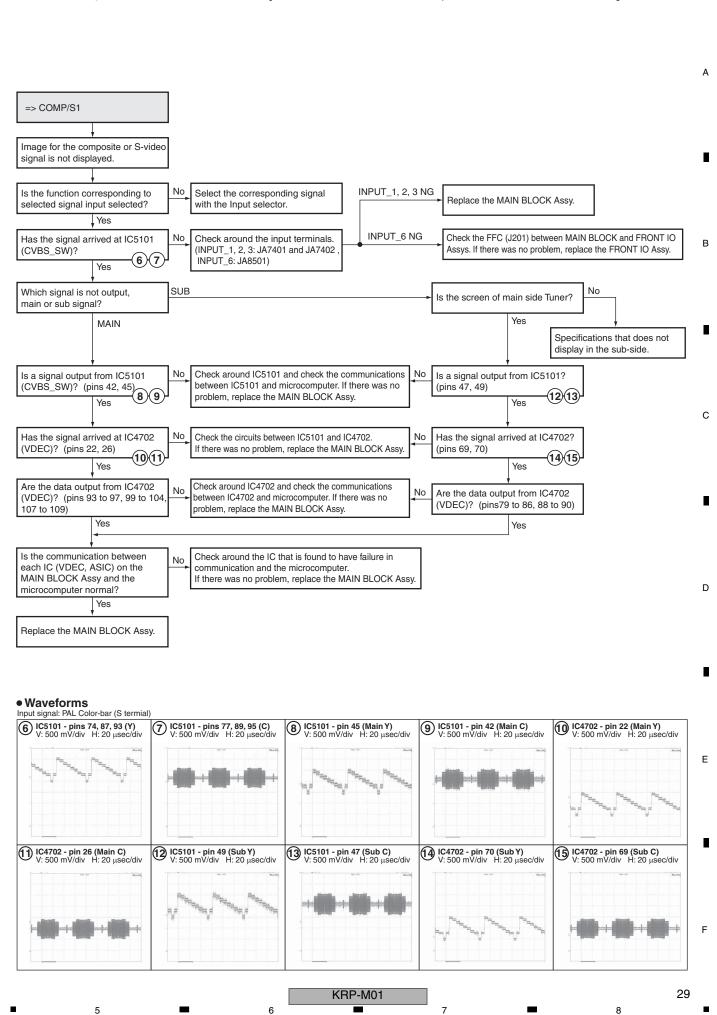
[4] VIDEO SYSTEM

Flowchart of Failure Analysis for The Video System

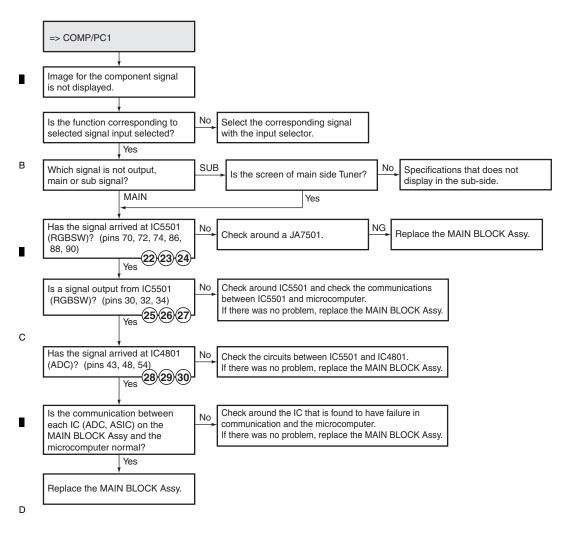


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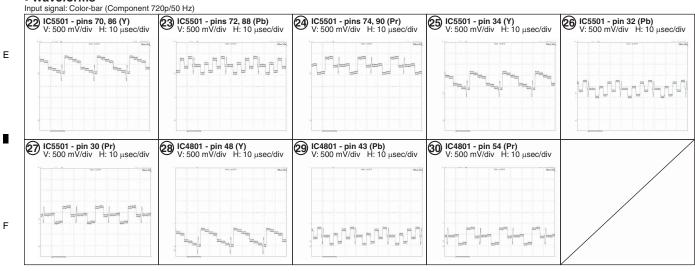


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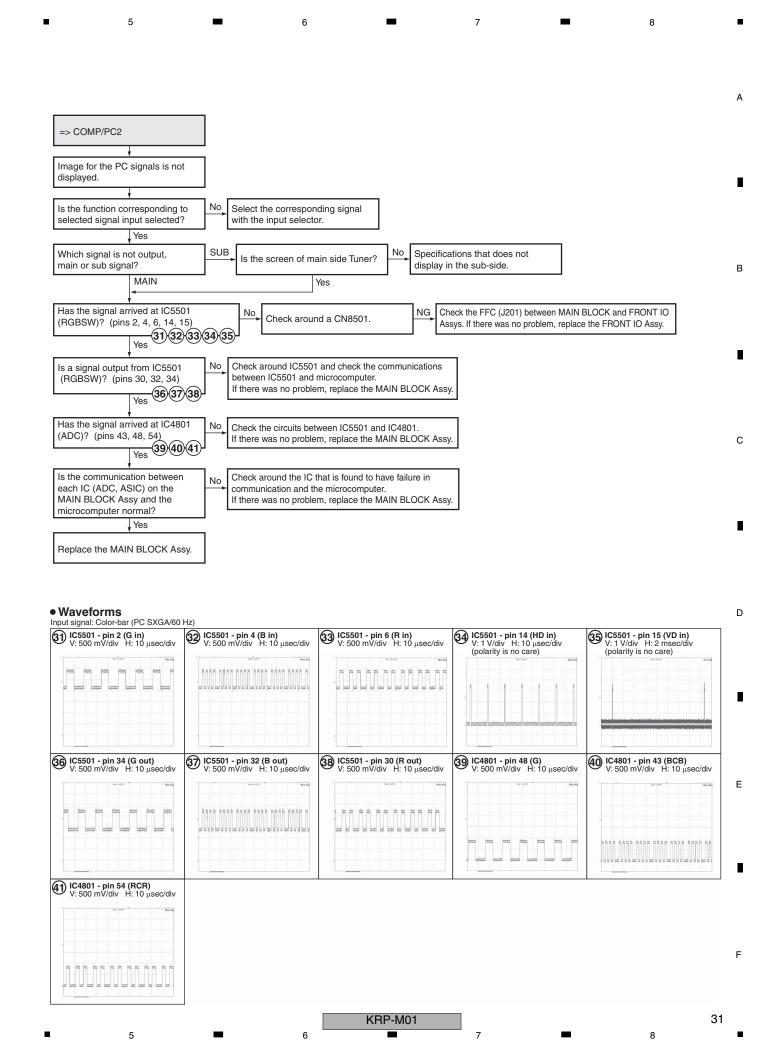
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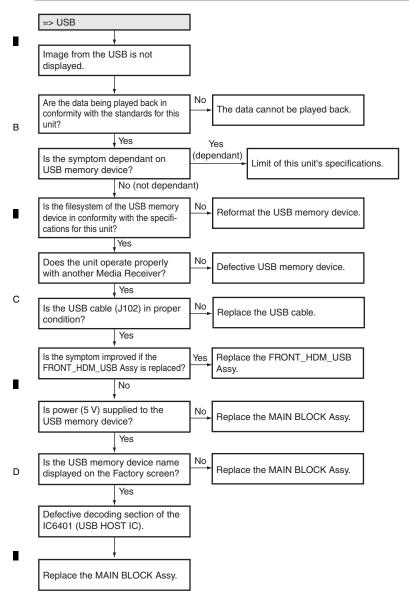


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[5] HOME GALLERY

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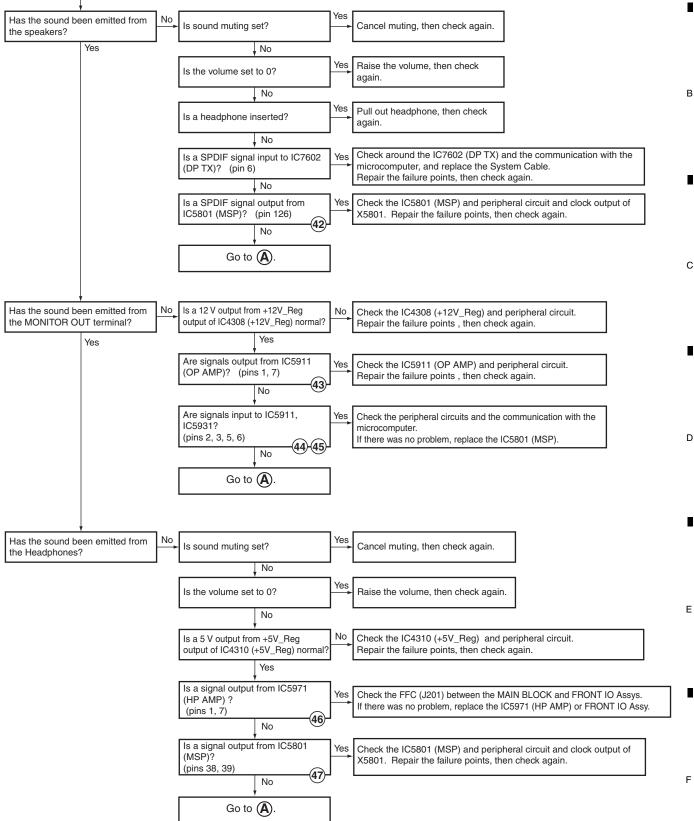
Flowchart of Failure Analysis for The Home Gallery



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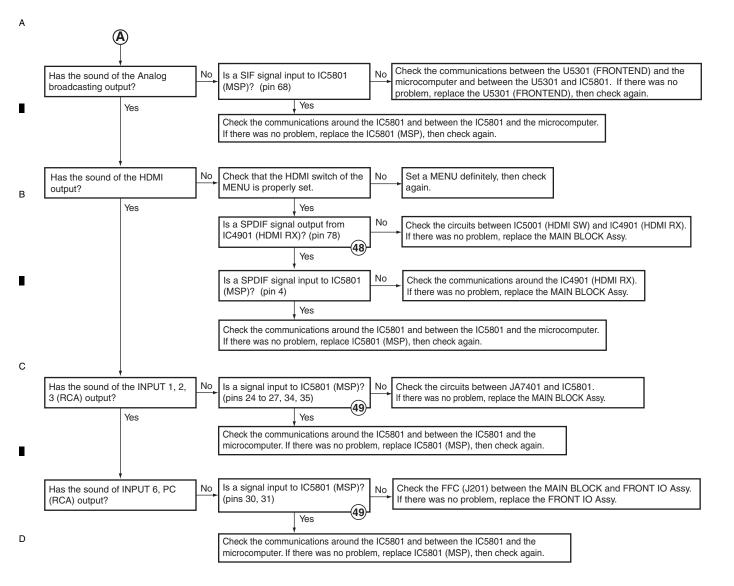
[6] AUDIO SYSTEM

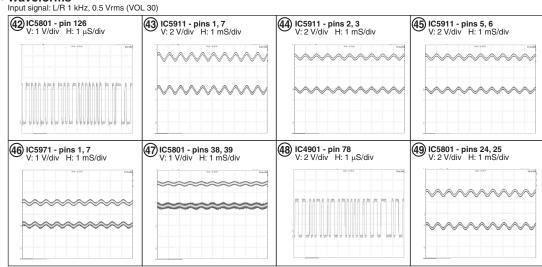
Flowchart of Failure Analysis for The Audio System => AU1



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3.3 DIAGNOSIS OF PD (POWER-DOWN)

[1] BLOCK DIAGRAM OF THE POWER-DOWN SIGNAL

Note:

The figure ① indicate the number of times the Red LED flashes when power-down occurs in the corresponding route.

MAIN BLOCK ASSY M1 V+5.1V_STB V+17V V+12V V+6.5V V+3.4V_STB $(\mathbf{1})$ PD_TRG INV. IC7003 MAIN_ucom **RELAY** (EMMA2) AC_DET RST3 INV. IC6811 IF_ucom

POWER SUPPLY UNIT <PKG 1> P2 STB5.1V +17V +12V +6.5V STB3.4V PD TRG **RELAY** AC_DET <Protection function> Overcurrent Protection (OCP) V+3.4V_STB, V+5.1V_STB, VCC 3 outputs Overvoltage Protection (OVP) V+6.5V, V+12V, V+17V V+3.4V_STB (latches for long time) Under Voltage Protection (UVP) V+6.5V, V+12V, V+17V Thermal Shut Down (TSD) V+6.5V output diode: D351 block V+3.4V_STB (latches for long time)

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SECONDARY PRIMARY POWER THERMISTOR **FUSE ⋘** VCC DC+120V to +390V AC IN DC/DC 85 to 276V CONVERTOR NEUTRAL REGULATOR STB DC/DC CONVERTOR AND MICRO AC_DET CIRCUIT COMPUTER **POWER SUPPLY** UNIT <PKG 2>

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[2] PD (POWER-DOWN) DIAGNOSIS OF FAILURE ANALYSIS

■ How to Distinguish the PD (Power-Down)

About the LED for checking causes of power-down

No LED for checking causes of power-down is provided for the POWER SUPPLY Unit of the MR. However, by checking the waveforms at terminals of the microcomputer, whether a power-down was caused by failure in the POWER SUPPLY Unit, and if it was, which power system among the four was in failure can be inferred. The points at which to check waveforms and how to distinguish power-down causes are described below:

<Points at which to Check Waveforms>

Waveforms between Pin 3 of CN801 and GND (secondary radiator, display chassis, etc.)
Refer to the section "Note on Removing the POWER SUPPLY Unit from the Chassis and Method for Resettig Standby Power Latchup" in the "7.2 DISASSEMBLY".

<How to Distinguish>

If a power-down was caused by failure in the POWER SUPPLY Unit, a pulse waveform is output at the above-mentioned points. (It is assumed that STB3.4 V power is properly output.)

By counting the frequency of "Lo" in the pulse waveform, the cause of power-down can be identified.

Frequency	Cause			
of "Lo"	Output Voltage	Overvoltage (OV) or Undervoltage (UV)		
Once	+12V	OV or UV *		
Twice	+17V	OV or UV *		
3 times	+6.5V	OV or UV *		
4 times	Protection against overheat			

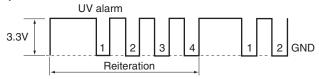
*How to distinguish OV and UV:

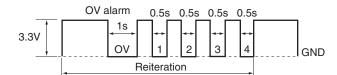
If the first "Lo" duration of a pulse is long (1 s), the cause is OV.

As the three output voltages are electromagnetically linked and interact with one another,

the frequency may vary among 1-3, depending on the type of power-down.

Examples:





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■ How to Diagnose the PD

Frequency of LED Flashing	History Indication in Factory Mode	Assy	Cause of power- down (activated protection circuit)	Point to be Checked	Possible Defective Parts
Red,	MR-PWR	MAIN	Overcurrent in	5V_ANT-REG	IC4305, C4305
once		BLOCK Assy	6.5 V power	5V_IO-REG	IC4310, C4301
		Лээу		3CH-DD	IC4402
				converter	C4405, C4406, C4409, C4463, C4464, C4466 to C4468
				FET	Q4417, Q4416, Q4411
				1CH-DD converter	IC4501, C4517
			Overcurrent in	FAN-REG	IC4302, C4342
			12 V power	8V_IO-REG	IC4309, C4315
				LNB	IC4503
			Overcurrent in 17 V power	12V_IO-REG	IC4308, C4303
			Overcurrent in	1.8V IO-REG	IC4604, C4609
			3.4 V power	1.61_161124	C4820, C8103
		POWER SUPPLY Unit	V+6.5V UVP	TP V+6.5V	Voltage drop due to overcurrent on the load side
		Offic	V+12V UVP	TP V+12V	Voltage drop due to overcurrent on the load side
			V+17V UVP	TP V+17V	Voltage drop due to overcurrent on the load side
			STB3.4V OCP	TP STB3.4V	C151, C153, C152, D152, or Z152, and abnormal current on the load side that is connected to STB3.4 V power
			STB5.1V	TP STB5.1V	C155 and abnormal current on the load side that is connected to STB5.1 V power
			OCP		And abnormal current on the load side that is connected to STB5.1 V power
			VCC	TP V+6.5V	D351, C351, C352, C353, and abnormal current on the load side that is connected to V+6.5V power
			OCP	TP V+12V	D352, C357, C358, and abnormal current on the load side that is connected to V+12V power
				TP V+17V	D353, C359, and abnormal current on the load side that is connected to V+17V power
			STB3.4V OVP	TP STB3.4V	PC121, Z151
			VCC OVP	TP V+6.5V TP V+12V	PC301, Breakage in the line to/from the P2 output connector Z351
			STB3.4V TSD		Z121 control IC and abnormal current on the load side that is connected to STB3.4 V power
			V+6.5V Rectifier diode (D351) TSD		D351 or D352, and abnormal current on the load sides that is connected to V+6.5 V and V+12 V

Note: Although replacement of the whole POWER SUPPLY Unit is required (replacement of only defective parts on the POWER SUPPLY Unit is not possible), the circuit symbols are described for reference

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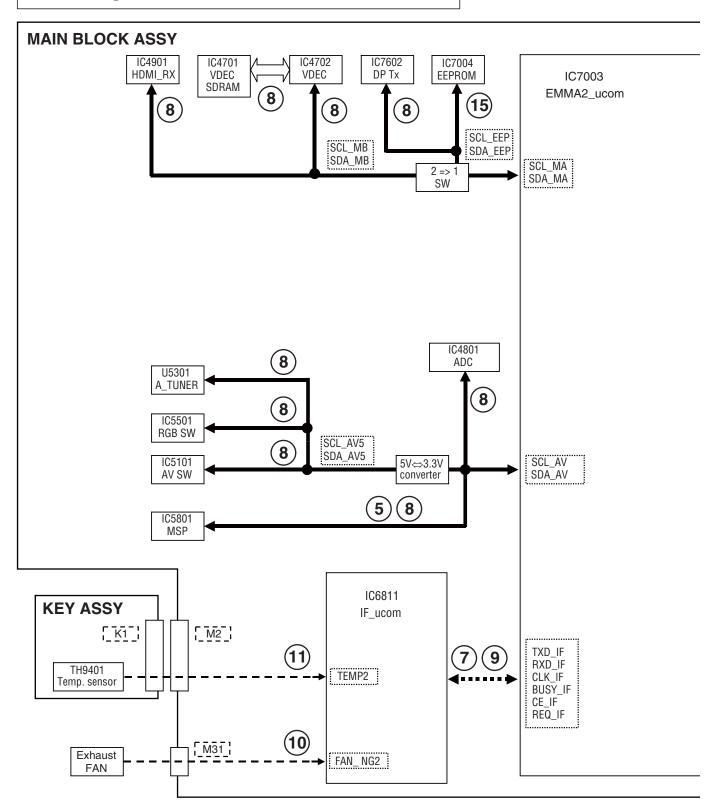
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3.4 DIAGNOSIS OF SD (SHUTDOWN)

[1] BLOCK DIAGRAM OF THE SHUTDOWN SIGNAL

Note: The figures ① to ⑤ indicate the number of times the Blue LED flashes when shut-down occurs in the corresponding route. ② LED is not flashed.

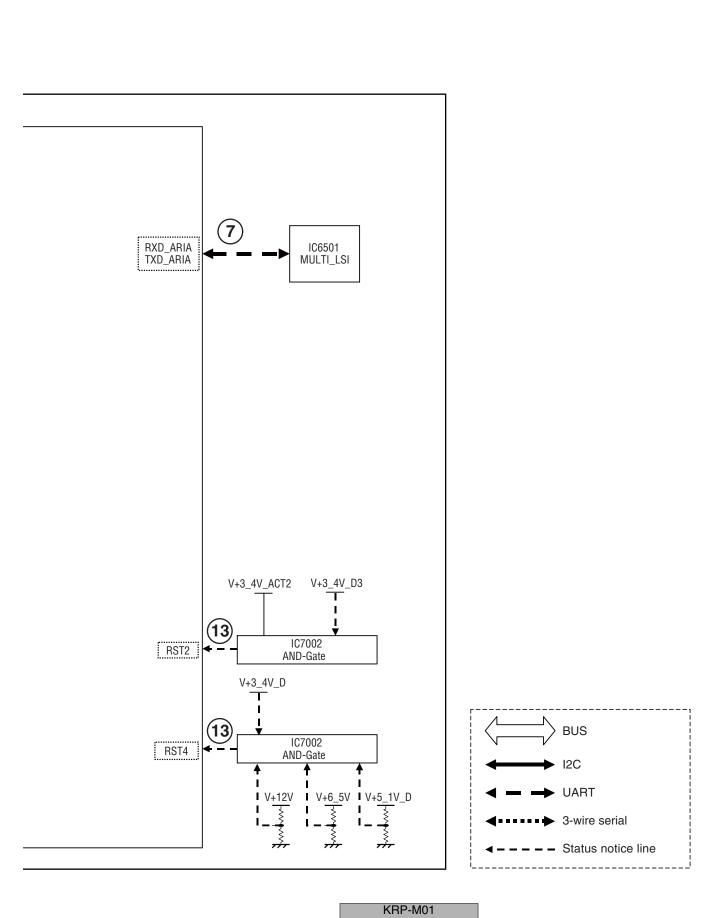


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[2] SD (SHUTDOWN) DIAGNOSIS

Frequency of	Major Type	Detailed Type	Log Indicatio		
LED Flashing	wajor type	Detailed Type	MAIN	SUB	
Blue 5	Audio	Abnormality in MSP	AUDIO	MSPMAP	
Div. 7	Failure in 3-wire serial communication	IF microcomputer	MA OL	IF	
Blue 7	with the main microcomputer	MULTI	MA-3L	MULTI	
		Tuner1		FE1	
		MSP/MAP		MSPMAP	
		AV Switch		AV-SW	
		RGB Switch		RGB-SW	
Blue 8	Failure in IIC communication with	Main VDEC	MA-IIC	VDEC	
	the main microcomputer	VDEC SDRAM		SDRAM	
		AD/PLL		ADC	
		HDMI	7	HDMI	
		DisplayPort Tx		DP-TX	
Blue 9	Failure in communication with the	_	MAIN	_	
	main microcomputer				
Blue 10	Abnormality in FAN	FAN2	FAN	FAN2	
Blue 11	High temperature of the unit	_	TEMP2	_	
Blue 12	Digital Tuner	-	_	_	
		DC-DC Converter power decrease		M-DCDC	
DI 10			DOT		
Blue 13	Failure in the power supply	POWER SUPPLY	RST-MA	RELAY	
Blue 15	Main EEPROM	Main EEPROM communication error	MA-EEP	_	

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Checkpoint	Possible Defective Part	Remarks
Power supply for MSP and MSP	IC5801, IC4604, Q4616	Check the MSP, its power and periphery parts (e.g. reset line).
Communication line between IF and MAIN	IC7003, IC6811	Check the communication lines (TXD_IF/RXD_IF/CLK_IF/BUSY_IF/CE_IF/REQ_IF)
Communication line between MULTI and MAIN	IC7003, IC6501	Check the communication lines (TXD_ARIA/RXD_ARIA)
IIC communication line between Tuner and MAIN	U5301, IC7003	Check the communication lines (SCL_TU/SDA_TU or SCL_AV/SDA_AV)
IIC communication line between MSP/MAP and MAIN	IC5801, IC7003	Check the communication lines (SCL_AV/SDA_AV)
IIC communication line between AV_SW and MAIN	IC5101, IC7003	Check the communication lines (SCL_AV5/SDA_AV5)
IIC communication line between RGB_SW and MAIN	IC5501, IC7003	Check the communication lines (SCL_AV5/SDA_AV5)
IIC communication line between M_VDEC and MAIN	IC4702, IC7003	Check the communication lines (SCL_MB/SDA_MB)
Communication line between VDEC and SDRAM	IC4701, IC4702	Check the communication lines (SDRAM), Failure in SDRAM
IIC communication line between ADC and MAIN	IC4801, IC7003	Check the communication lines (SCL_AV/SDA_AV)
IIC communication line between HDMI_RX and MAIN	IC4901, IC7003	Check the communication lines (SCL_MB/SDA_MB)
IIC communication line between DP_TX and MAIN	IC7602, IC7003	Check the communication lines (SCL_EEP/SDA_EEP)
Communication line between IF and MAIN	IC6811, IC7003	Check the communication lines (TXD_IF/RXD_IF/CLK_IF/BUSY_IF/CE_IF/REQ_IF)
Dirt attached to the fan motor		Check the fan. (SD10 does not detect it at the temperature that fans do not turn.)
Periphery of the FAN		FAN_NG
Periphery of the cable at M31		Check if cables are firmly connected.
Periphery of the fan control regulator	IC4302	Check that the voltage outputs it.
Ambient temperature		TEMP2 A shutdown occurs because of high temperature.
Temperature sensor or its periphery	TH9401	TEMP2
Periphery of the cable between M2 and K1	CN4204, CN9401	Check if cables are firmly connected.
-	-	-
RST2 V+3_4V_ACT2, V+3_4V_D3	IC7002	Check if each voltages are started.
RST4 V+12V, V+6_5V, V+5_1V_D, V+3_4V_D	IC7002	Check if each voltages are started.
V+12V, V+6_5V, V+17V	POWER SUPPLY Unit	Check if each voltages are started.
Check the cable M1	CN4203	Check if cables are firmly connected.
IIC communication line between EEPROM and MAIN	IC7004, IC7003	Check the communication lines (SCL_EEP/SDA_EEP)

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3.5 NON-FAILURE INFORMATION

[1] INFORMATION ON SYMPTOMS THAT DO NOT CONSTITUTE FAILURE

Symptom	Cause, item to check, information
HDMI: Symptoms concerning the input format and setting	s
The picture color for an INPUT 3 or 4 or 5 signal is not correct.	The color setting for INPUT 3 or 4 or 5 is not compatible with that of the output equipment. Check whether the color setting is YPbPr or RGB.
The video signal to INPUT 3 or 4 or 5 is not displayed, and a message is displayed.	A unsupported video signal is input.
The audio signal input to the INPUT 3 is not output. No HDMI signal is input.	The audio setting for INPUT 3 is any setting and a video signal is not input. If the audio setting is any setting to output an analog audio signal, the HDMI signal must be input. (If a DVI device is to be connected, use a DVI-HDMI conversion cable.) If the HDMI video signal is not input, the analog audio signal is not output.
No sound of signals to INPUT 3 or 4 or 5 is output.	The setting on the side of the HDMI output equipment is wrong. (Example: Dolby Digital)
The 1080p input signal is not displayed properly or at all, although the 1080i input signal is displayed properly.	Check that the connected cable supports HDMI Category 2. (As the clock frequency for the 1080p signal is triple that for the 1080i signal, signal degradation caused by a cable must not be neglected. A cable supporting HDMI Category 2 can be used for the 1080p signal. Although some conventional cables can support the 1080p signal, some others cannot.)
MONITOR video output	
The video output signal from the MONITOR output is deteriorated. Or when the video output signal from the MONITOR output is recorded, its playback picture is deteriorated.	The video signal output from the MONITOR output is Macrovision protected.
The video signal is not output when the component signal is input to INPUT 1 or 2.	The video signal is not output from the MONITOR output when the component signal is selected.
The video signal is not output when the video signal is input to INPUT 3 or 4 or 5.	The video signal is not output from the MONITOR output when the HDMI signal is selected.
MONITOR audio output	
The image displayed on the PDP is not synchronized with the sound from the MONITOR audio output.	The audio signal from the MONITOR output is synchronized with the video output signal from the MONITOR output.
Miscellaneous	
The no-signal off function is not activated.	The no-signal off and no-operation off functions are effective only if video (composite, S video,
The no-operation off function is not activated.	component, HDMI [excluding PC]) input or TV input is selected.
Power management does not function.	Power Management is effective only while an analog PC signal is being input. It is not effective with HDMI-PC signal input.
The AUTO SETUP function is not activated.	The Auto Setup function is effective only while an analog PC signal is being input. This function does not work if an analog PC signal is not input, even if the INPUT PC is selected.
The picture-quality setting (AV Selection) is not stored.	The picture-quality setting is stored for each input. As the setting is changed when another input is selected, the user may have a false idea that the setting is not stored.
The picture size changes arbitrary.	The Auto Size setting is set to ON.
The display position of the screen changes slightly while the screen is on.	The orbiter function for minimizing the effects of phosphor burn is activated. Although the setting for this function can be changed on the Home menu, retaining the factory setting is strongly recommended.
The video signal to the S video connector is not displayed.	The component video cable is connected to the same input function as for the S video (even if no signal is input to the component video connector, merely having something plugged in to the connector will result in judgement that a signal is being fed in and the component video connector takes priority). (Priority of connectors: component video > S video > composite video)
The video signal to the composite video connector is not displayed.	The S Video or component video cable is connected to the same input function as for the composite video.(Priority of connectors : component video > S video > composite video)

SUPPLEMENT: On the video setting for HDMI

There are three types of HDMI output formats: color difference 4:4:4, color difference 4:2:2, and RGB4:4:4.

(The proportions, such as 4:4:4 and 4:2:2, represent those of the amount of data for video signal components. For example, as for color difference 4:4:4, the proportion of the amount of data as for Y, Cb, and Cr is 4:4:4.)

It is required to make the settings of the PDP according to the settings of the output equipment. For usual operation, however, set them to AUTO. If the color is inappropriate, make the settings manually.

In the HDMI system, video signals are coded at 24 bits per pixel and transmitted as a series of 24-bit pixels. In a case of color difference 4:4:4, Y, Cb, and Cr use 8 bits each. In a case of color difference 4:2:2, Y, Cb, and Cr use 12 bits each, but Cb and Cr are transmitted at a half sampling rate of Y. This unit is capable of processing the upper 10 bits out of 12 bits of video data. Recent high-end DVD players, such as Pioneer DV-79AVi, are capable of outputting 10-bit color-difference signals. In general, it is said that picture quality for color difference 4:2:2 format is assumed to be higher, because human eyes are more sensitive to luminance than to colors. In the case of RGB4:4:4, R, G, and B use 8 bits each.

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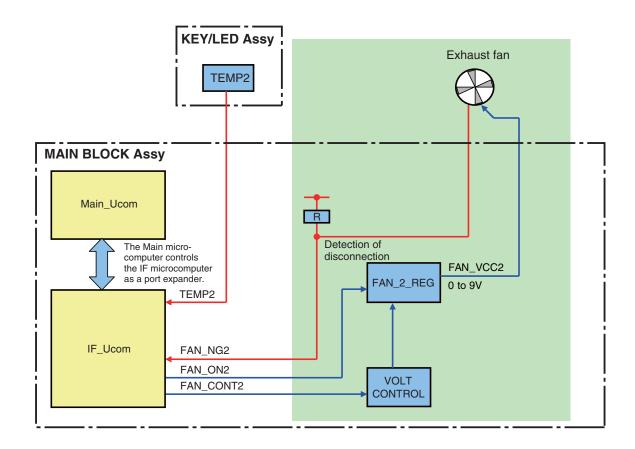
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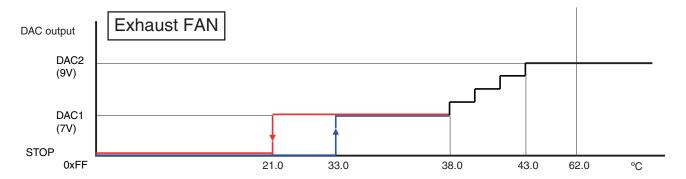
3.6 OUTLINE OF THE OPERATION

[1] SPECIFICATION OF THE FAN CONTROL

■ Block diagram



■ Operation specifications



Notes:

- The operating temperature of the fan is different from the ambient temperature, because the sensor temperature is read by the microcomputer.
- The fan may not start rotating until the internal temperature of the unit reaches a certain level, such as immediately after the unit is turned on.
- When the temperature rises, the sensor voltage of TEMP2 decreases.
- When the voltage of the DAC output for exhaust FAN decreases, rotation speed of FAN rises.

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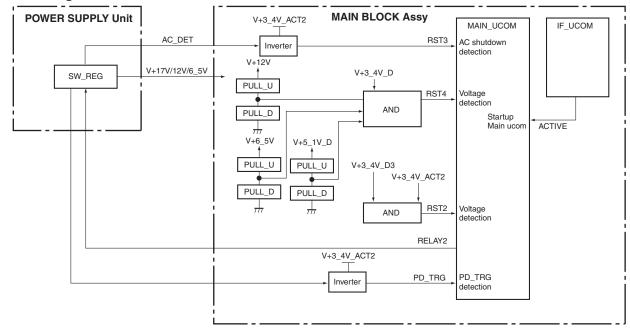
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[2] PROCESSING IN ABNORMALITY

Power supply and DC-DC converter

Circuit configuration



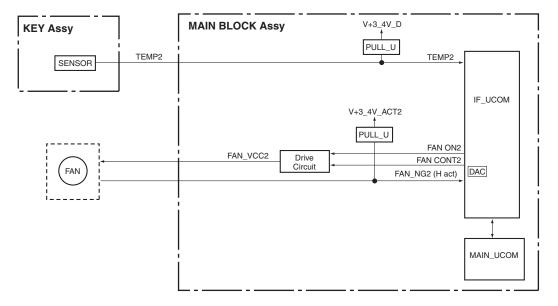
Specifications for port monitoring

Port Name	SD/PD Indication	Determination Condition	Monitoring conditions	Operation
RST2	ASIC power (M-DCDC)	Shutdown occurs when the signal is "L." for 5 sec after PSW1 is ON. or for 2 sec while the unit is ON.	Panel screen ON (RST4 = H and PSW1 = H) While awaiting restoration of RST2 (RST2 = L)	Shutdown occurs immediately Blue LED flashes 13 times.
RST3	_	_	Excepting passive standby	If "RST3 = H" (AC_OFF) is detected under the monitoring conditions, a power-off process starts. Monitoring of the RST3 port is continued, and monitoring of other ports is interrupted. Communication is controlled only by the IF microcomputer. The port outputs are set as specified. If the signal at the RST3 port continues to be H after 30 mS of waiting, monitoring is continued. If RST3 is L, a restoration process starts according to the latest power-on/-off status.
RST4	MAIN power (RELAY)	Shutdown occurs if the signal is "L." for 5 sec after RELAY2 is ON. or for 2 sec while the unit is ON or in Functional STB.	RELAY2 = ON (High)	Shutdown occurs immediately Blue LED flashes 13 times.
PD_TRG	VCC power (MR-PWR)	Shutdown occurs when the signal is continuously "L" for 30msec * 3 times after RELAY2 is ON.	RELAY2 = ON Monitor it after 3 sec.	Power-down occurs immediately Red LED flashes once.

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Fan and temperature sensor

Circuit configuration



Specifications for port monitoring

Port Name	SD/PD Indication	Determination Condition	Monitoring conditions	Operation
FAN_NG2	FAN	Shutdown occurs when the signal is "H." 1 S * 3 times.	RST4 = H and FAN_ON2 = H (Monitoring starts 3 sec after the above conditions are established.)	Shutdown occurs immediately Blue LED flashes 10 times.
TEMP2	High temperature at MR	values equal to or	RST4 = H (Monitoring starts 1 sec after the above conditions are established.)	In the Panel screen ON: Shutdown occurs after the warning indication is displayed for 30 sec. In the Functional STB: Shutdown occurs immediately Blue LED flashes 11 times.

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[3] HOW TO OPERATE THE MEDIA RECEIVER SEPARATELY

Necessary items for operation

- · Media Receiver
- DP-to-HDMI conversion jig: GGF1627 (with the AC adaptor)
 AC adaptor INPUT: 100 V to 240 V, 50/60 Hz, 0.3 A
 OUTPUT: DC 6 V, 1.8 A
- Monitor or TV (with which an image with resolution of 1920 x 1080 p, 60 Hz can be displayed, with HDMI input)
 Note: When checking with DVI monitor, setting change of this jig is required.
- DP cable (GGP1117) and HDMI cable
- G8 or G9 remote control unit (in case of controlling by remote control unit)
- PC and RS-232C straight cable (in case of controlling by PC)
- HDMI -DVI cable (in case of connecting with DVI monitor)

Connection





Fig.1 DP - HDMI Conversion tool (Front side)

Fig.2 DP - HDMI Conversion tool (Rear side)

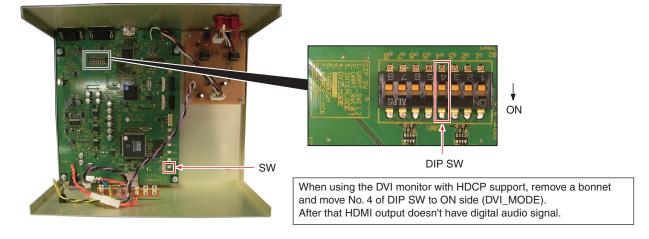


Fig.3 DP - HDMI Conversion tool
DIP SW Setting (output mode setting for HDMI connector)

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Preparation

• Set the MR from System Operation mode to Standalone Operation mode.

The MR is normally set to System Operation mode. If the MR is turned on in this mode, it cannot be operated properly.

To change to Standalone Operation mode, proceed as follows:

[With an RS-232C command]

- 1. Turn the MR on.
- 2. In this state, send the MRMS01 command via RS-232C ports.
- 3. Turn the MR off.

When the MR is turned on next time or after, it will be in Standalone Operation mode.

[With the keys on the MR]

- 1. Set the MR to Standby mode.
- 2. Press and hold the INPUT key of the MR pressed for at least 5 seconds. (This step is for giving a startup trigger in a case where the MR was in Passive Standby mode.)
- 3. Within 5 seconds after the INPUT key is released, press and hold the CHANNEL key of the MR for at least 10 seconds.
- 4. After the modes are changed, the red LED flashes twice then is lit (the unit enters Normal Standby mode).
- 5. Turn the unit off.

When the MR is turned on next time or after, it will be in Standalone Operation mode.

Operation

After the setting in Preparation is completed, turn the units on in the following order then perform analysis:

- 1. Turn the monitor or TV on. (Set the input mode to HDMI.)
- 2. Turn the DP-to-HDMI conversion jig on.
- 3. Turn the MR on.

If no image is displayed on the monitor or TV after the MR is turned on, press and hold the switch on the DP-to-HDMI conversion jig for about 1 sec.

How to control the MR

• With the remote control unit:

The infrared receiver (IR) sensor for remote control unit is placed inside of the jig. Please point the remote towards the AC adaptor connector on the jig.

Unlike normal products, sensor reception of this tool is not so sensitive due to reduce interference with another Pioneer Plasma TV.

Please keep the distance between the remote control unit and the sensor less than 15cm.

• With RS-232C commands:

Connect a PC to the MR via their RS-232C ports and send RS-232C commands from the PC. (Baud rate: 9600 bps)

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After analysis is finished

After analysis in Standalone Operation mode is finished, before returning the MR to the customer, be sure to return the unit to System Operation mode, as shown in the procedures below.

If it remains in Standalone Operation mode, when it is connected with the customer's monitor, the monitor will detect a connection error and not operate properly, and no image will be displayed.

To set the MR to System Operation mode, proceed as follows:

[With an RS-232C command]

- 1. Turn the MR on.
- 2. Send the MRMS00 command via RS-232C ports.
- 3. Turn the MR off.

When the MR is turned on next time or after, it will be in System Operation mode.

4. Connect the MR directly with the monitor and check that they operate properly.

[With the keys on the MR]

- 1. Set the MR to Standby mode.
- 2. Press and hold the INPUT key of the MR pressed for at least 5 seconds. (This step is for giving a startup trigger in a case where the MR was in Passive Standby mode.)
- 3. Within 5 seconds after the INPUT key is released, press and hold the CHANNEL + key of the MR for at least 10 seconds.
- 4. After the modes are changed, the red LED flashes twice then is lit (the unit enters Normal Standby mode).
- 5. Turn the unit off.

When the MR is turned on next time or after, it will be in Standalone Operation mode.

Products whose proper operation has been proved when HDMI connection is performed with this MR

Model Number	Manufacturer	Built-in Audio AMP	
PDP-5000EX	Pioneer	O (SP is required)	
G8	Pioneer	O (SP is required except 42 inch)	
FP241WJ	BenQ	× (External audio amp and SP is required)	
3008WFP	DELL	× (External audio amp and SP is required)	
HD2441W	EIZO NANAO	× (External audio amp and SP is required)	

Attention point for audio volume

Audio output level is connected with MR volume level. If VR level of a MR is normal (around 10 - 15) and displayed HDMI TV or audio AMP is not so high level, sound level is very low. Please turn up the volume to appropriate level either or both units.

In case of turning up volume of MR to very high level during testing, turn down it to normal level and then turn off the unit. Otherwise when connecting the MR with panel, very loud sound is output from speakers and it might be a danger.

Attention point when using another Pioneer Plasma TV

Please pay attention to interference of IR signal when using Pioneer plasma TV as HDMI monitor.

If remote signal is also received to Pioneer plasma TV when operating MR with this tool and remote, you might confuse of which unit is controlled by the remote.

The following methods are some of suggestions to control only MR with the conversion tool.

Using the remote control unit and the conversion tool (AC adaptor connector) as nearly as possible hiding remote sensor of the plasma TV temporally.

Setting Method to connect with DVI monitor with HDCP support (DVI mode)

- 1. Open bonnet with power off condition.
- 2. Refer to Fig. 3, move the DIP SW No. [4] to ON side.

After this setting, DVI mode signal is output from HDMI output connector of HDMI.

- Note: 1. Some of DVI monitors might not display output signal from this conversion tool.
 - 2. Output signal does not contain digital audio signal.

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■ RS-232C command list

Na	mand me	Function		Effective only in Factory mode	Remarks
Α					
AMT	S00	Audio mute OFF			
	S01	Audio mute ON			
С					
CHN	FWD	Changing tuner preset channel (1 step forward)			
	REV	Changing tuner preset channel (1 step reverse)			
CHM		Clearing data of the hour meter		•	Last memory is performed to the panel side.
CHR		Clearing data of the hour meter of MTB/MR side			Clear the hour meter of screen display of MAIN NG.
CNG		Clearing data of the SD history of MTB/MR side			
D					
DPT		Rewriting the Display Port Tx			
DW*		To subtract * to the adjustment value (* = 0 to 9, subtract 10 with DWO and set to minimum value with DWF)			
F					
FAN		Factory mode: OFF		•	
FAY		Factory mode: ON			
FST	S41	Set each memory setting of MR side to the General model.		•	
	S42	Set each memory setting of MR side to the China model.		•	
I					
INA	***	Switching the terrestrial analog signal, direct tuning (***: channel number)	MAIN		
		Switching the terrestrial analog signal (Channnel is in the last.)	MAIN		
INH		Switching the Home Media Gallery / Home Gallery			
INP	S01	Input: INPUT1	MAIN		
	S02	Input: INPUT2	MAIN		
	S03	Input: INPUT3	MAIN		
	S04	Input: INPUT4	MAIN		
	S05	Input: INPUT5	MAIN		
	S06	Input: INPUT6	MAIN		
	S07	Input: INPUT7 (PC)	MAIN		
M					
MRM	S00	Setting the mode to normal operation	MAIN	•	
	S01	Setting the mode to standalone operation	MAIN	•	
MST	S00	Display one screen	100 111		
10101	S01	PsideP (Main size: normal)			
	S02	PinP (Right down)			
	S03	PinP (Right up)			
	S04	PinP (Left down)			
	S05	PinP (Left up)			
	S08	SWAP (Exchanging sub-screen)			
0	000	000 055		T	
OSD	S00	OSD setting: OFF	MAIN		
_	S01	OSD setting: ON	MAIN		
P		D 055			
POF		Power: OFF	MAIN		
PON		Power: ON	MAIN		
PUC	S00	PURE CINEMA: OFF	MAIN	•	
	S01	PURE CINEMA: Standard	MAIN	•	
	S02	PURE CINEMA: Advance	MAIN	•	
	S03	PURE CINEMA: Smooth	MAIN	•	
Q					
QMT		Acquiring temperature of MTB/MR side and Fan speed			
QNG		Acquiring shutdown information of MTB/MR side		1	
QS1		Acquiring unit data, such as the software version			
QSE		Acquiring unit data, such as the software version of MTB/MR side (specific destinatio	n)		

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Command Name		Function	Last Memory	in Factory mode	Remarks
S					
SDF	S00	SRS DEFINITION: OFF			
	S01	SRS DEFINITION: DEFINITION1			
	S02	SRS DEFINITION: DEFINITION2			
	S03	SRS DEFINITION: DEFINITION3			
SML	***	Adjustment of the side mask level	MAIN	•	
SRS	S00	SRS: OFF			
	S01	SRS: SRS1			
	S02	SRS: SRS2			
	S03	SRS: SRS3			
SZM	S00	Setting the screen size to Dot by Dot	MAIN		
	S01	Setting the screen size to 4:3	MAIN		
	S02	Setting the screen size to FULL or FULL 1080i	MAIN		
	S03	Setting the screen size to ZOOM	MAIN		
	S04	Setting the screen size to CINEMA	MAIN		
	S05	Setting the screen size to WIDE or WIDE1	MAIN		
	S06	Setting the screen size to FULL 14:9	MAIN		
	S07	Setting the screen size to CINEMA 14:9	MAIN		
	S11	Setting the screen size to AUTO	MAIN		
	S12	Setting the screen size to WIDE2	MAIN		
Т			<u>'</u>		
TBS	S00	TRUBASS: OFF			
	S01	TRUBASS: TRUBASS1			
	S02	TRUBASS: TRUBASS2			
	S03	TRUBASS: TRUBASS3			
U			'	•	
UP*		To add * to the adjustment value (* = 0 to 9, add 10 with UP0 and set to maximum value with UPF)			
V					
VOL	UP*, DW*, ***	To adjust the volume			Use this command by designating the adjustment value *** (=000 to 060).

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ZME *** Initializing the video EEPROM data of the MTB/MR side

3.8 DETAILS OF RS-232C COMMANDS

Following items are same as the KRP-M01/WYSIXK5.

- [1] QS1 (Software Version Information of the Microcomputer)
- [3] QMT (STATUS INFORMATION OF MTB/MR SECTION
- [4] QNG (SHUTDOWN INFORMATION OF MTB SECTION)
- [5] FAY/FAN (ADJUSTMENT COMMANDS PERMISSION/PROHIBITION)

[2] QSE (DESTINATION PECULIAR INFORMATION)

Induce it peculiar, individual information is acquired.

Command Format	Effective Operation Modes	Function	Remarks		
[QSE]	Every time	Output of status	Return data: 3 (ECO) + 32 (DATA) + 2 (CS) = 37 Byte		

	Data Arrangement	Data Length	Output Example
ECO		3 byte	QSE
1	Reserved	8 byte	*****
2	User setting password	4 byte	1234
3	DP Tx firmware version	16 byte	123456789ABCDEFG
4	DP Tx hardware version	4 byte	ABCD
CS	Check Sum	2 byte	13

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4. SERVICE FACTORY MODE

The following items in the service manual of KRP-M01/WYSIXK5 are not applicable to this model.

DETAILS OF THE FACTORY MENU

- [5] INITIALIZE
 - [5-3] DTB SERVICE MENU (+)
- DIGITAL TUNER SERVICE MENU
 - [1] REMOTE CONTROL CODE IN DIGITAL TUNER SERVICE MENU
 - [2] HIERARCHICAL TABLE OF DIGITAL TUNER SERVICE MENU
 - [3] DIGITAL TUNER SERVICE MENU SCREEN
 - [4] HOME MEDIA GALLERY SCREEN
 - [5] DIGITAL SCREEN
 - [6] SATELLITE SCREEN
 - [7] SOFTWARE VERSION SCREEN

4.1 OUTLINE OF THE SERVICE FACTORY MODE

Following items are same as the KRP-M01/WYSIXK5.

- [1] SERVICE FACTORY MODE TRANSITION CHART
- [2] HOW TO ENTER/EXIT SERVICE FACTORY MODE
- [3] FUNCTIONS WHEN ENTERING THE SERVICE FACTORY MODE
- [5] PDP SERVICE REMOTE CONTROL

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[4] REMOTE CONTROL CODE IN SERVICE FACTORY MODE

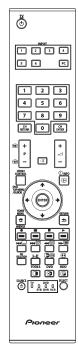
Remote Control Keys	Basic Functions	Remarks
MUTING	Switching the main items.	Shifting to the next main item (top).
↓ (DOWN)	Switching the subtitled items.	Shifting downward to the next subtitiled item.
1 (UP)	Switching the subtitled items.	Shifting upward to the next upper layer.
← (LEFT)	Decreasing the adjustment value.	Decreasing the adjustment value.
→ (RIGHT)	Increasing the adjustment value.	Increasing the adjustment value.
ENTER/SET	Switching the layers.	Shifting downward or upward to the next lower or upper layer.
INPUT	Selecting INPUT.	Shifting the INPUT to the next function.
INPUTxx	Selecting INPUT.	Switching the INPUT to xx. (xx=1 to 6)
CH+/P+	Increasing the channel number.	
CH-/P-	Decreasing the channel number.	
Numeric Keys	Function: TV	Function: TV (previously selected channel number is selected)
POWER	Power OFF.	Turning the power off.
FACTORY	Factory OFF (Factory mode)	In Factory mode, turning Factory mode off.
FACTORY	Factory ON (Non-Factory mode).	In Non-Factory mode, turn Fuctory mode on.
HOME MENU	Menu ON.	In Factory mode, turn Factory mode off.
VOLUME+	Volume UP.	Increasing 10 the adjustment value. (PANEL FACTORY)
VOLUME-	Volume DOWN.	Decreasing 10 the adjustment value. (PANEL FACTORY)
DRIVE OFF (Note1)	Drive Mode OFF.	Turning Drive mode off.
INTEGRATOR	INTEGRATOR MENU ON.	Enter INTEGRATOR MODE.

(Note 1) When ten seconds have passed since the [DRIVE OFF] key was pressed at the standby, it becomes invalid.

Please press [POWER] key from the [DRIVE OFF] key pressing within ten seconds when you do power supply ON while driven OFF.



PDP service remote control



Supplied remote control

KRP-M01

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[6] FACTORY HIERARCHICAL TABLE

Middle Item Small Item Small Item Variable / Adjustment Range Remarks	Large I	tem			
1.1 VERSION (1)		Middle Item		Variable / Adjustment Range	Remarks
[1-1] VERSION (1) [1-2] VERSION (2) [1-3] VERSION (3) [1-4] MAIN NG CLEAR ←> NO ←> YES [1-5] TEMPERATURE [1-6] HOUR METER CLEAR ←> NO ←> YES [1-7] HOM SIGNAL INFO 1 [1-8] HOMS SIGNAL INFO 1 [1-8] HOMS SIGNAL INFO 2 [1-9] VDEC SIGNAL INFO 2 [1-9] VDEC SIGNAL INFO 2 [1-9] VDEC SIGNAL INFO 2 [2-1] PANEL PATCHOR (+) (*1) [2-2] PANEL PATCHOR (+) (*1) [2-2] PANEL MORRIS [2-3] POWER DOWN [2-4] SHUT DOWN [2-5] PANEL-1 ADJ (+) [2-6] PANEL-2 ADJ (+) [2-7] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-12] PANEL MIN FACTORY (*) (*1) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP, RX INFO [3-4] PM, SETUP (+) [4-1] CH PRESET ←> DISABLE ←> ENABLE Exclusively used for production line [4-2] AFT ←> DISABLE ←> ENABLE Exclusively used for production line [4-4] AFT ←> DISABLE ←> ENABLE Exclusively used for production line [4-4] AFT ←> DISABLE ←> ENABLE Exclusively used for production line [4-5] SYNC DET (+) [5-5] PINLS LEVUP [5-7] BINLS LEVUP [5-8] DATA RESET ←> DISABLE ←> ENABLE Exclusively used for production line [4-7] AFT ←> DISABLE ←> ENABLE Exclusively used for the technical analysis			Small Item		
[1-2] VERSION (2) [1-3] VERSION (3) [1-4] MAIN NG [1-5] TEMPERATURE [1-6] HOUR METER [1-7] HOMI SIGNAL INFO 1 [1-8] HOM SIGNAL INFO 2 [1-9] VEC SIGNAL INFO 1 [1-10] VEC SIGNAL INFO 2 [1-9] VEC SIGNAL INFO 2 [1-10] VEC SIGNAL INFO 2 [1-10] VEC SIGNAL INFO 1 [1-10] VEC SIGNAL INFO 2 [1-10] PAREL INFORMATION [1-10] VEC SIGNAL INFO 2 [1-10] PAREL INFORMATION [1-10] VEC SIGNAL INFO 2 [1-10] PAREL INFORMATION [1-10] VEC SIGNAL INFO 2 [1-10] PAREL FUNCTION (+) [1-10] VEC SIGNAL INFO 3 [1-10] PAREL FUNCTION (+) [1-10] VEC SIGNAL INFO 3 [1-10] VEC SIGNAL INFO 4 [1-10] VEC SIGNAL INFO 4 [1-10] VEC SIGNAL INFO 5 [1-10] PAR INFO 5 [1-10] PAR INFO 5 [1-10] PAR INFO 5 [1-10] PAR INFO 6 [1-10] PAR INFO 6 [1-10] PAR INFO 6 [1-10] PAR INFO 7 [1-10] PAR INFO 7 [1-10] PAR INFO 7 [1-10] PAR INFO 8 [1-10] PAR INFO 8 [1-10] PAR INFO 8 [1-10] PAR INFO 8 [1-10] PAR INFO 9 [1-10]	4.2 [1] 1				
[1-3] VERSION (3) [1-4] MAIN NG CLEAR <=> NO <=> YES [1-5] TEMPERATURE [1-6] HOUR METER CLEAR <=> NO <=> YES [1-7] HOM SIGNAL INFO 1 [1-8] HOMI SIGNAL INFO 2 [1-9] VDEO SIGNAL INFO 1 [1-10] VDEO SIGNAL INFO 2 [2-11] PANEL INFORMATION [2-2] PANEL PACTORY (-) (*1) [2-2] PANEL WORKS [2-3] POWER DOWN [2-4] SHUT DOWN [2-5] PANEL VADU (+) [2-6] PANEL-2 ADJ (+) [2-7] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-12] PANEL FUND (-) [3-14] PM NG INFO [3-2] PM STATE INFO [3-3] PM STATE INFO [3-4] PM STATE INFO [3-5] PM STATE INFO [3-6] PM SETUP (+) [4-7] CT PRESET <=> DISABLE <=> EXclusively used for production line [4-7] EXCLUSIVELY Used for production line [4-8] SYNC DET (+) [4-9] ETT (+) [4-1] CT (+) [4-1] STUP (-) [4-1] SIDE MASK LEVEL (+) [5-1] RINALIZE [5-1] RINALIZE [5-1] FINALIZE [5-1] FINALIZE [5-1] FINALIZE [5-2] FINAL SETUP [5-3] Wide XGA AUTO <=> DISABLE <=> CABBLE [5-1] FINALIZE [5-2] FINAL SETUP [5-3] Wide XGA AUTO <=> DISABLE <=> COLINIA [5-4] CIT (+) [5-5] Wide XGA AUTO <=> DISABLE <=> COLINIA [5-5] FINAL SETUP [5-7] FINAL SETUP [5-8] WIGE XGA AUTO <=> DISABLE <=> COLINIA [5-8] FINAL SETUP [5-9] FINAL SETUP		- · · · · · · · · · · · · · · · · · · ·			
11-4] MAIN NG		1 1			
[1-5] TEMPERATURE [1-6] HOUR METER CLEAR <=> NO <=> YES [1-7] HOMI SIGNAL INFO 1 [1-8] HOMI SIGNAL INFO 2 [1-9] VOEC SIGNAL INFO 0 [1-10] VDEC SIGNAL INFO 0 [1-10] VDEC SIGNAL INFO 2 [1-9] VDEC SIGNAL INFO 2 [2-1] PANEL FACTORY (+) (*1) [2-1] PANEL INFORMATION [2-2] PANEL WORKS [2-3] POWER DOWN [2-4] SHUT DOWN [2-5] PANEL ADJ (+) [2-6] PANEL-2 ADJ (+) [2-7] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] RASTER MASK SETUP (+) [2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-12] PANEL MAIN FACTORY (+) (*1) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) [3-4] PM_SETUP (+) [3-5] PM_SETUP (+) [3-6] PM_SETUP (+) [3-7] PM_SETUP (+) [3-8] PM_SETUP (+) [3-9] PM_SETUP (+) [3-1] PM_SETUP (+) [3-2] PM_SETUP (+) [3-3] PM_SETUP (+) [3-4] PM_SETUP (+) [3-5] PM_SETUP (+) [3-6] PM_SETUP (+) [3-7] PM_SETUP (+) [3-8] PM_SETUP (+) [3-9] PM_SETUP (+) [3-1] PM_SETUP (+) [3-1] PM_SETUP (-) [3-1] PM_SETUP (-) [3-2] PM_SETUP (-) [3-3] PM_SETUP (-) [3-4] PM_SETUP (-) [3-5] PM_SETUP (-) [3-6] PM_SETUP (-) [3-7] PM_SETUP (-) [3-8] PM_SETUP (-) [3-9] PM_SETUP (-) [3-1] PM_SETUP (-) [3-1] PM_SETUP (-) [3-1] PM_SETUP (-) [3-1] PM_SETUP (-) [3-2] PM_SETUP (-) [3-3] PM_SETUP (-) [3-4] PM_SETUP (-) [3-5] PM_SETUP (-) [3-6] PM_SETUP (-) [3-7] PM_SETUP (-) [3-8] PM_SETUP (-) [3-9] PM_SETUP (-) [3-1] PM_SETUP (-) [3-1] PM_SETUP (-) [3-1] PM_SETUP (-) [3-1] PM_SETUP (-) [3-2] PM_SETUP (-) [3-3] PM_SETUP (-) [3-4] PM_SETUP (-) [3-5] PM_SETUP (-) [3-6] PM_SETUP (-) [3-7] PM_SETUP (-) [3-8] PM_SETUP (-) [3-9] PM_SETUP (-) [3-1] PM_SETUP (-) [3-1] PM_SETUP (-) [3-1] PM_SETUP (-) [3-1] PM_SETUP (-) [3-2] PM_SETUP (-) [3-3] PM_SETUP (-) [3-4] PM_SETUP (-) [3-5] PM_SETUP (-) [3-6] PM_SETUP (-) [3-7] PM_SETUP (-) [3-8] PM_SETUP (-) [3-9] PM_SETUP (-) [3-9]			CLEAR <=>	NO <-> VES	
[1-6] HOUR METER			OLL/III (=>	110 (=> 120	
[1-7] HDMI SIGNAL INFO 1 [1-8] HDMI SIGNAL INFO 2 [1-9] VDEC SIGNAL INFO 1 [1-10] VDEC SIGNAL INFO 2 [1-10] VDEC SIGNAL INFO 2 [2-1] PANEL INFO MARCH SIGNAL INFO 2 [2-2] PANEL FACTORY (+) (**1) [2-2] PANEL FACTORY (+) (**1) [2-2] PANEL WORKS [2-3] POWER DOWN [2-4] SHUT DOWN [2-5] PANEL 2 ADJ (+) [2-6] PANEL 2 ADJ (+) [2-7] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-12] PANEL MAIN FACTORY (+) (**1) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) [4-4] GTI (-) [4-7] CAPTERN MASK SETUP (+) [4-8] STORE STOR		· ·	CLEAR <=>	NO <=> YES	
[1-8] HDMI SIGNAL INFO 2 [1-9] VDEC SIGNAL INFO 2 [1-10] VDEC SIGNAL INFO 2 [2-1] PANEL INFORMATION [2-2] PANEL INFORMATION [2-2] PANEL WORKS [2-3] POWER DOWN [2-4] SHUT DOWN [2-5] PANEL-1 ADJ (+) [2-6] PANEL-2 ADJ (+) [2-7] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] ETC (+) [2-9] RASTER MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-12] PANEL MAIN FACTORY (+) (*1) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-3] DP_RX INFO [3-4] PM SETUP (+) [4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) [4-4] GTI (+) [5-5] SIDE MASK LEVEL (->) [5-2] FINAL SETUP DATA RESET <=> NO <=> GENERAL <=> CHINA for the technical analysis		• •			
[1-9] VDEC SIGNAL INFO 1 [1-10] VDEC SIGNAL INFO 2 4.2 [2] PANEL FACTORY (-) (-) (-) [2-1] PANEL INFORMATION [2-2] PANEL WORKS [2-3] POWER DOWN [2-4] SHUT DOWN [2-5] PANEL-1 ADJ (+) [2-6] PANEL-2 ADJ (+) [2-7] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] PASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) 4.2 [4] OPTION [4-1] CH PRESET <-> DISABLE <-> DISABLE <-> ENABLE Exclusively used for production line [4-2] AFT <-> [4-3] SYNC DET (+) [4-4] CTI (+) [5-1] SIDE MASK LEVEL (+) [5-2] FINAL SETUP DATA RESET <-> NO <-> OCCUPANIEL SETUP SIDE MASK LEVEL (-) [5-2] FINAL SETUP DATA RESET <-> NO <-> OCCUPANIEL SETUP SIDE MASK LEVEL (-> DISABLE <-> DISABLE <-> CON The technical analysis For the technical analysis For the technical analysis For the technical analysis		• •			
[1-10] VDEC SIGNAL INFO 2		· ·			
4.2 [2] PANEL FACTORY (+) (*1) [2-1] PANEL INFORMATION [2-2] PANEL WORKS [2-3] POWER DOWN [2-4] SHUT DOWN [2-5] PANEL-1 ADJ (+) [2-6] PANEL-2 ADJ (+) [2-7] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [3-12] PM NG INFO [3-12] PM STATE INFO [3-13] PM NG INFO [3-14] PM NG SETUP (+) [4-14] CH PRESET <⇒> DISABLE <⇒> ENABLE Exclusively used for production line [4-13] SYNC DET (+) [4-14] CTI (+) [4-14] CTI (+) [5-15] INITIALIZE [5-17] INITIALIZE [5-16] INITIALIZE [5-16] INITIALIZE [5-17] INITIALIZE [5-18] INITIALIZE [5-18] INITIALIZE [5-19] INITIALIZE [5-1		· ·			
[2-1] PANEL INFORMATION [2-2] PANEL WORKS [2-3] POWER DOWN [2-4] SHUT DOWN [2-5] PANEL-7 ADJ (+) [2-6] PANEL-2 ADJ (+) [2-7] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) [4-4] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) [4-4] CTI (+) [4-4] CTI (+) [4-7] SIDE MASK LEVEL (+) [5-8] INITIALIZE [5-1] SIDE MASK LEVEL (+) [5-9] FINAL SETUP DATA RESET <=> NO <=> GENERAL <=> CHINA DISABLE <=> CHINA	4.2 [2] F				
[2-3] POWER DOWN [2-4] SHUT DOWN [2-5] PANEL-1 ADJ (+) [2-6] PANEL-2 ADJ (+) [2-7] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) [4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) [4-4] CTI (+) [4-4] CTI (+) [5-1] SIDE MASK LEVEL (+) [5-2] FINAL SETUP DATA RESET <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis for the technical analysis					
[2-4] SHUT DOWN [2-5] PANEL-1 ADJ (+) [2-6] PANEL-2 ADJ (+) [2-6] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [3-1] PM NEL MIN FACTORY (+) (*1) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) 4.2 [4] OPTION [4-1] CH PRESET <> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) for the technical analysis [4-4] CTI (+) [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> (5-2) FINAL SETUP [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis [5-1] FINAL SETUP [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis [5-1] FINAL SETUP [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis [5-1] FINAL SETUP [5-2] FINAL SETUP [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis [5-1] FINAL SETUP [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis		[2-2] PANEL WORKS			
[2-4] SHUT DOWN [2-5] PANEL-1 ADJ (+) [2-6] PANEL-2 ADJ (+) [2-6] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [3-1] PM NEL MIN FACTORY (+) (*1) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) 4.2 [4] OPTION [4-1] CH PRESET <> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) for the technical analysis [4-4] CTI (+) [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> (5-2) FINAL SETUP [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis [5-1] FINAL SETUP [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis [5-1] FINAL SETUP [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis [5-1] FINAL SETUP [5-2] FINAL SETUP [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis [5-1] FINAL SETUP [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis		[2-3] POWER DOWN			
[2-5] PANEL-1 ADJ (+) [2-6] PANEL-2 ADJ (+) [2-7] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) 4.2 [4] OPTION [4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line for the technical analysis for the technical analysis for the technical analysis [5-1] SIDE MASK LEVEL (+) [5-2] FINAL SETUP DISABLE <=> ENABLE for the technical analysis					
[2-6] PANEL-2 ADJ (+) [2-7] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [3-12] PM NG INFO [3-13] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) 4.2 [4] OPTION [4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) for the technical analysis [4-4] CTI (+) for the technical analysis [4-5] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> [5-1] SIDE MASK LEVEL (+) [5-2] FINAL SETUP DATA RESET <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis		· ·			
[2-7] PANEL FUNCTION (+) [2-8] ETC (+) [2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-11] COMBI MASK SETUP (+) 4.2 [3] PANEL MAIN FACTORY (+) (*1) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) 4.2 [4] OPTION [4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-4] CTI (+) for the technical analysis 4.2 [5] INITIALIZE [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> [5-2] FINAL SETUP DATA RESET <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis		* * * * * * * * * * * * * * * * * * * *			
[2-8] ETC (+) [2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) [2-11] COMBI MASK SETUP (+) 4.2 [3] PANEL MAIN FACTORY (+) (*1) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) 4.2 [4] OPTION [4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line ine [4-3] SYNC DET (+) for the technical analysis [4-4] CTI (+) for the technical analysis 4.2 [5] INITIALIZE [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis					
[2-9] RASTER MASK SETUP (+) [2-10] PATTERN MASK SETUP (+) [2-11] COMBI MASK SETUP (+) 4.2 [3] PANEL MAIN FACTORY (+) (*1) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) 4.2 [4] OPTION [4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) for the technical analysis 4.2 [5] INITIALIZE [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> [5-2] FINAL SETUP DISABLE <=> ENABLE for the technical analysis [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis [6-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis					
[2-10] PATTERN MASK SETUP (+)					
[2-11] COMBI MASK SETUP (+) 4.2 [3] PANEL MAIN FACTORY (+) (*1) [3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) 4.2 [4] OPTION [4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) for the technical analysis [4-4] CTI (+) for the technical analysis 4.2 [5] INITIALIZE [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> [5-2] FINAL SETUP DATA RESET <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis for the technical analysis					
[3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) 4.2 [4] OPTION [4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) for the technical analysis [4-4] CTI (+) for the technical analysis 4.2 [5] INITIALIZE [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis for the technical analysis					
[3-1] PM NG INFO [3-2] PM STATE INFO [3-3] DP_RX INFO [3-4] PM_SETUP (+) 4.2 [4] OPTION [4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) for the technical analysis [4-4] CTI (+) for the technical analysis 4.2 [5] INITIALIZE [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis for the technical analysis	4.2 [3] F				
[3-3] DP_RX INFO [3-4] PM_SETUP (+) 4.2 [4] OPTION [4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) for the technical analysis [4-4] CTI (+) for the technical analysis 4.2 [5] INITIALIZE [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis					
[3-4] PM_SETUP (+)		[3-2] PM STATE INFO			
4.2 [4] OPTION [4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) for the technical analysis [4-4] CTI (+) for the technical analysis 4.2 [5] INITIALIZE [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis		[3-3] DP_RX INFO			
4.2 [4] OPTION [4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) for the technical analysis [4-4] CTI (+) for the technical analysis 4.2 [5] INITIALIZE [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis		[3-4] PM_SETUP (+)			
[4-1] CH PRESET <=> DISABLE <=> ENABLE Exclusively used for production line [4-2] AFT <=> DISABLE <=> ENABLE Exclusively used for production line [4-3] SYNC DET (+) for the technical analysis [4-4] CTI (+) for the technical analysis 4.2 [5] INITIALIZE [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis for the technical analysis	4.2 [4] (
[4-2] AFT <=>				DISABLE <=> ENABLE	Exclusively used for production line
[4-4] CTI (+) for the technical analysis 4.2 [5] INITIALIZE [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> [5-2] FINAL SETUP DATA RESET <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis		[4-2] AFT <=>		DISABLE <=> ENABLE	Exclusively used for production line
4.2 [5] INITIALIZE [5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> [5-2] FINAL SETUP DATA RESET <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis		[4-3] SYNC DET (+)			for the technical analysis
[5-1] SIDE MASK LEVEL (+) SIDE MASK LEVEL <=> [5-2] FINAL SETUP DATA RESET <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis		[4-4] CTI (+)			for the technical analysis
[5-2] FINAL SETUP DATA RESET <=> NO <=> GENERAL <=> CHINA [5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis	4.2 [5] I	NITIALIZE			
[5-3] Wide XGA AUTO <=> DISABLE <=> ENABLE for the technical analysis		[5-1] SIDE MASK LEVEL (+)	SIDE MASK LEVEL <=>		
		[5-2] FINAL SETUP	DATA RESET <=>	NO <=> GENERAL <=> CHINA	
[5-4] AUTO ADJUST. <=> AUTO ADJUST. <=> NO <=> YES		[5-3] Wide XGA AUTO <=>		DISABLE <=> ENABLE	for the technical analysis
		[5-4] AUTO ADJUST. <=>	AUTO ADJUST. <=>	NO <=> YES	

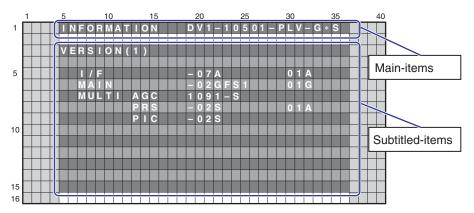
^{(*1):} For details on the setting items, refer to the Service manual of the PLASMA DISPLAY (KRP-600P, KRP-500P).

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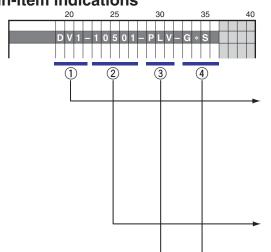
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[7] INDICATIONS IN SERVICE FACTORY MODE



■ Main-item indications



1 Input function

Input Functions	OSD
DV 1 to 6	DV 1 to 6
Terrestrial Wave (Analog)	AIR
Cable (Digital)	CBD
Home Gallery	HG
PC	PC

② SIG mode and Screen size

Note: See SIG-Mode Tables. (See next page.)

③ Color system and Signal type

<u> </u>	OSD							
Color System and Signal Type	At Composite Input	At S-connector Input						
NTSC	NTV	NTS						
PAL	PLV	PLS						
PAL M	PMV	PMS						
PAL N	PNV	PNS						
PAL 60	P6V	P6S						
SECAM	SCV	SCS						
4.43 NTSC	4NV	4NS						
BLACK/WHITE	BWV	BWS						
Y/CB/CR	(CBR						
Y/PB/PR	F	PBR						
RGB	F	RGB						
Digital Video signal		DIG						

4 Option (Destination, Panel Generation, etc.)

Options	OSD
KRP-500P/LFTXJ	G*S
KRP-500P/WAXJ5	0.0
KRP-600P/WAXJ5	G*S

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2 SIG Mode and Screen size (by User is displayed)

1st and 2nd characters: Resolution of the input signal 3rd and 4th characters: Refresh rate of the input signal 5th character: Selection of the screen size

■ Input signal mode table for video signals (resolutions and V frequencies)

1st to 4th	1st to 4th Character		Fv (Hz)	Fh (kHz)						
10	50	SDTV*625i	50.000	15.750						
10	60	SDTV*525i	60.000	15.750						
20	50	SDTV*625p	50.000	31.500						
20	60	SDTV*525p	60.000	31.500						
30	50	HDTV*1125i	50.000	33.750						
00	60	HDTV*1125i	60.000	33.750						
40	50	HDTV*750p	50.000	45.000						
40	60	HDTV*750p	60.000	45.000						
	24	HDTV*1125p	24.000	27.000						
50	50	HDTV*1125p	50.000	56.250						
	60	HDTV*1125p	60.000	67.500						

Fv: Vertical Frequency, Fh: Horizontal Frequency

■ Input signal mode table for PC signals (resolutions and V frequencies)

1st to 4th	Character	Signal Type	Fv (Hz)	Fh (kHz)					
C1	70	720 x 400	70.087	31.469					
C2	60	640 x 480	59.940	31.469					
C4	60	800 x 600	60.317	37.879					
C6	60	1280 x 720	44.800						
C7	60	1024 x 768	60.004	48.363					
C9	60	1360 x 768	60.015	47.712					
D6	60	1280 x 1024	60.000	64.000					

Fv: Vertical Frequency, Fh: Horizontal Frequency

■ Current selection of the screen size

5th Character	GUI Notation	VIDEO	PC	Remarks
0	DOT BY DOT	•	_	
1	4:3	•	•	
2	FULL	•	•	
3	ZOOM	•		
4	CINEMA	•		
5	WIDE	•		
6	FULL 14:9	•		
7	CINEMA 14:9	•		
9	WIDE1	•	=	
Α	WIDE2	•	=	

●: supported, -: unsupported

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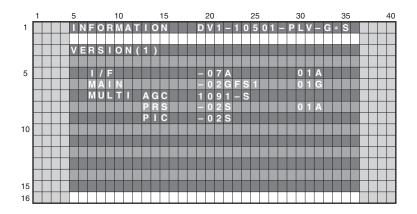
4.2 DETAILS OF THE FACTORY MENU

[1] INFORMATION

■ Operation items

No.	Function	Content							
[1-1]	VERSION (1)	The Flash memory versions for each device are displayed.	QS1						
[1-2]	VERSION (2)	The Flash memory versions for each device are displayed.	QSE						
[1-3]	VERSION (3)	The Flash memory versions for each device are displayed.	QSB						
[1-4]	MAIN NG	The Shutdown NG information and Event Times in the MTB section are displayed.	QNG						
[1-5]	TEMPERATURE	The present temperature and the FAN rotating status are displayed.	_						
[1-6]	HOUR METER	The accumulation power ON count of the panel is displayed.	-						
[1-7]	HDMI SIGNAL INFO 1	The status registers of HDMI receiver are displayed with hexadecimal.							
[1-8]	HDMI SIGNAL INFO 2	The status registers of Fibral receiver are displayed with nexadecimal.	_						
[1-9]	VDEC SIGNAL INFO 1	Display the signal information input to VDEC							
[1-10]	VDEC SIGNAL INFO 2	Display the signal information input to VDEC.	_						

[1-1] VERSION (1)



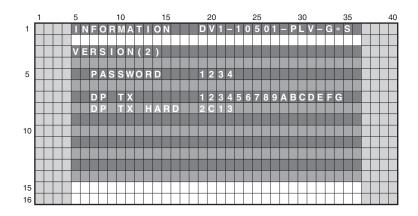
Display Item	Meaning	Display Example (Program)	Display Example (Boot)
I/F	I/F microcomputer	-07A	01A
MAIN	Main microcomputer	-02GFS1	01G
MULTI AGC	AGC data of Multi processor	1091-S	
MULTI PRS	Program of Multi processor	-02S	01A
MULTI PIC	Picture quality data of Multi processor	-02S	

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[1-2] VERSION (2)



Display Item	Meaning	Display Example
PASSWORD	User setting password	1234
DP TX	DP TX Firmware Version	123456789ABCDEFG
DP TX HARD	DP TX Hardware Version	2C13

[1-3] VERSION (3)

	1		5 10							15							20					25					30					35			4	0		
1				П	N	E	O	E	M	Α	П	П	0	Ν				D	٧	1		1	0	5	0	1		Р	Ц	۷		G	*	S		T]
	L			۷	Ε	R	S	П	O	N	(3)																									
5	L					P		V	Α	Ш	Ν								0	2	Α	S							0		Α							
						V	0	E	U	Г	E								0	6	Α								0	1	Α							
						S	E	Q		P	R	S						П	0	3	Υ								0		Α							
	L																																					
						D	E		E	Х								1	2	3	4	5	6	7	8	9	Α	В	С	D	囯	E	G					
10						D	E		E	Х		Ξ	Α	R	D			2	C	1	2																	
						E	Α	N	Œ	п			Ν	E	0			Х	Х	Х	Х	Х	Х	Х	Х													
15																																						
16																																						

Display Item	Meaning	Display Example (Program)	Display Example (Boot)					
P_MAIN	Panel Main microcomputer	-02AS	01A					
MODULE	Module microcomputer	-06A	01A					
SEQ PRS	Program of the sequence processor	-03Y	01A					
Display Item	Meaning	Display Example						
DP RX	DP RX Firmware Version	123456789	9ABCDEFG					
DP RX HARD	DP RX Hardware Version	20	C12					
Display Item	Meaning							
PANEL INFO	It displays the generation of the panel, inchage and the type of the panel. For details on display values and settings, see "10: Panel Information" in "5.9 [1] QS1 (Software Version Information of the Microcomputer)" on the Service Manual of KRP-M01/WYSIXK5.							

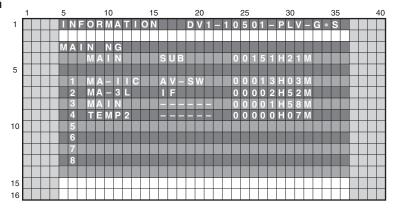
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[1-4] MAIN NG



Shutdown NG information

Error Display: MAIN	Error Display: SUB	Cause of Shutdown
MR-PWR		Abnormality in VCC power
AUDIO	MSPMAP	Abnormality in MSP
MA-3L		3-wire Serial Communication error of Main microcomputer.
	IF	Communication error of IF microcomputer
	MULTI	Main communication error of Multi Processor
MA-IIC		IIC Communication error of Main microcomputer
	FE1	Tuner 1
	MSPMAP	MSP/MAP
	AV-SW	AV Switch
	RGB-SW	RGB Switch
	VDEC	Main VDEC
	SDRAM	VDEC - SDRAM
	ADC	AD/PLL
	HDMI	HDMI
	DP-TX	DisplayPort Tx
MAIN		Communication error of Main microcomputer
FAN		FAN abnormal
	FAN1	FAN1 abnormal stop
	FAN2	FAN2 abnormal stop
TEMP2		Abnormality high temperature
RST-MA		Abnormality in MTB power
	M-DCDC	Abnormality in ASIC power (DC-DC)
	RELAY	Power decrease of RELAY power

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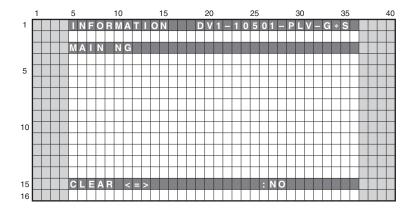
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• Clear the MAIN NG history

To shift to the MAIN NG history clear screen, while the MAIN NG screen is displayed, press the ENTER/SET key.



Operation:

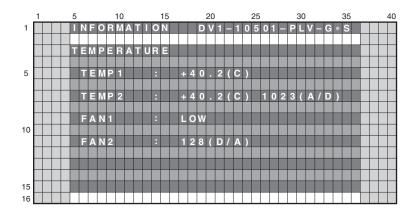
- Even if [←] key or [→] key is pressed, {CLEAR <=> :YES} \Leftrightarrow {CLEAR <=> :NO} is repeated.
- Selecting <NO> then pressing the ENTER/SET key will return the screen to the next higher layer, without doing anything.
- Selecting <YES> then holding the ENTER/SET key pressed for 5 seconds will clear the NG log data that are managed in MTB then return the screen to the next higher layer.

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[1-5] TEMPERATURE

A present temperature and the FAN rotation are displayed.

If either [←] key or [→] key is pressed, the display data is refreshed.



Display Item	Meaning
TEMP1	The temperature of the sensor on the panel side is displayed by the Centigrade (C).
TEMP2	The temperature conversion display is done with 10 bit the A/D input value of IF microcomputer. It is displayed by both the Centigrade (C) and 8 bit A/D value. Note: When temperature (C) of the sensor becomes more than a specified temperature, the shutdown start of processing.
FAN1	Although STOP, LOW, or HIGH may be displayed, they are meaningless. Ignore those displays.
FAN2	The value of the rotation state of FAN is displayed. During a rotation of FAN, 8bit D/A value output from IF microcomputer is displayed. It is displayed with OFF during a stop.

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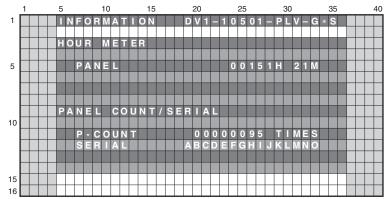
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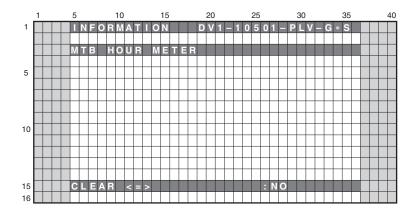
[1-6] HOUR METER



Display Item	Meaning	Display Example
PANEL	HOUR METER of the panel	00151H 21M
P-COUNT	Accumulation power ON count of the panel	00000095 TIMES
SERIAL	Serial number of the Display (panel)	ABCDEFGHIJKLMNO

• MTB HOUR METER

In HOUR METER screen on Factory Menu, press the [ENTER/SET] key, and then it moves to the screen to clear MTB HOUR METER. (MTB HOUR METER is cleared only.)



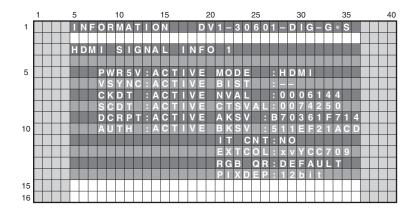
Operation:

- Even if [←] key or [→] key is pressed, {CLEAR <=> :YES} ⇔ {CLEAR <=> :NO} is repeated.
- Selecting <NO> then pressing the ENTER/SET key will return the screen to the next higher layer, without doing anything.
- Selecting <YES> then holding the ENTER/SET key pressed for 5 seconds will clear the HOUR METER (HOUR METER while the MAIN NG screen is displaed) data that are managed in MR then return the screen to the next higher layer.

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[1-7] HDMI SIGNAL INFO 1



Displays the input signal information of HDMI terminal

Display Item	Meaning
PWR5V	+5 V power detection (18 pin of HDMI terminal)
VSYNC	VSYNC detection
CKDT	Clock detection
SCDT	SYNC detection
DCRPT	HDCP decryption status
AUTH	HDCP authentication status
MODE	HDMI mode status
BIST	HDCP Key status (Always display it with "".)
NVAL	N value
CTSVAL	CTS value
AKSV	Shadow AKSV value
BKSV	Shadow BKSV value
IT CNT	IT content (AVI info)
EXTCOL	Extension colorimetry (AVI info)
RGB QR	RGB range (AVI info)
PIXDEP	Number of pixel/bit

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[1-8] HDMI SIGNAL INFO 2

	1		5					10					15					20	1				25					30					35			40	J
1	П		П	Ν	E	0	R	М	Α	Т		0	Ν				D	٧	1		3	0	6	0	1		D	П	G		G	*	S		П		1
	П																																		П		1
	П		Н	D	M	П		s	П	G	Ν	Α	L			Ν	E	0		2															П		1
	П																																		П		1
5						Œ		R	Ε	s	:	2	2	0	0				С	0	L		S	Р	8	4	2	2							T		1
	П					٧		R	B	S	1	0	5	6	3				С	0		M	囯	T	8	7	0	9							П		1
	Г					Œ		D	固		8	1	9	2	0				Α	s	P	囯	С	T	В	1	6	П	9						Т		1
	П					٧		D	Ξ		1	0	5	4	0				Α	С	П		٧	Ξ	8										П		1
	П					П	Ν	П	R	L	В		Ν	П					S	а	m	е		а	s		р	П	С	t					П		1
10	Г					٧		P	0	L	1	P	0	S					٧		Б	M			8		İ								Т		1
	П					Œ		P	0	L	8	P	0	S					1	9	2	0	х	1	0	8	0	1	@	6	0				П		1
	П					Α	U	D	П	0	1	4	8	k					P	П	Х		R	P	8	0	0								П		1
	Г											Р	С	М					S	o	U	R	С	囯	В	P	П	0	Ν	Ξ	Е	R			Т		1
	П											2	0	b	П	t			D	٧	R	П	D	T	9	0									П		1
15																																			T		1
16																																					

Displays input signal status of HDMI terminal

Display Item	Meaning
H RES	Number of horizontal pixels
V RES	Number of vertical lines
H DE	Number of effectively horizontal pixels
V DE	Number of effectively vertical lines
INTRL	Interlace (=INT) or progressive (=PRG)
V POL	VSYNC polarity
H POL	HSYNC polarity
AUDIO (first line)	Sampling frequency. (ex. DVD: 48kHz, CD: 44.1kHz) *1
AUDIO (second line)	Audio format PCM (PCM) or No PCM (no PCM)
AUDIO (third line)	Quantization bit
COL SP	Color space (AVI Info) 422 or 444 or RGB *2
COLMET	Colorimetry (AVI Info)
ASPECT	Aspect (AVI Info)
ACTIVE	Active format (AVI Info)
V FMT	Video format (AVI Info)
PIX RP	Pixel count
SOURCE (first line)	Vendor name of the emission device
SOURCE (second line)	Model name of the emission device

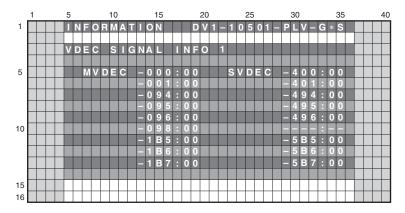
Display of HDMI FACTORY and correspondence of resolution Please confirm the following items when the picture doesn't come out.

Input			FACTOR\	/ Display	
Signal	H RES	V RES	H DE	V DE	V FMT
480i (525i)@60	858	262 or 263	720	240	720x480i@60
480p (525p)@60	858	525	720	480	720x480p@60
1080i (1125i)@60	2200	562 or 563	1920	540	1920x1080i@60
720p (750p)@60	1650	750	1280	720	1280x720p@60
1080p (1125p)@60	2200	1125	1920	1080	1920x1080p@60
1080p (1125p)@24	2750	1125	1920	1080	1920x1080p@24
576i (625i)@50	864	312 or 313	720	288	720x576i@50
576p (625p)@50	864	625	720	576	720x576p@50
1080i (1125i)@50	2640	562 or 563	1920	540	1920x1080i@50
720p (750p)@50	1980	750	1280	720	1280x720p@50
1080p (1125p)@50	2640	1125	1920	1080	1920x1080p@50

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^{*1:} Confirm if this item is displayed when the audio is not outputted.
*2: If may not match to the state of emission devices when the color is abnormal.

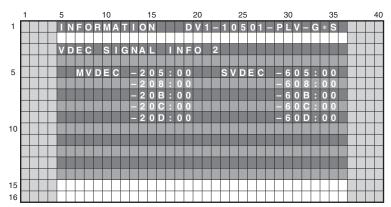
[1-9] VDEC SIGNAL INFO 1



Displays signal status that is input to VDEC.

Device	Sub Address (Main screen)	Sub Address (Sub screen)	Meaning						
	000h	400h	Line system distinction result						
	001h	401h	VTR distinction result						
	094h	494h	Slot number						
VDEC	095h	495h	Color system distinction result						
	096h	496h	ACC coefficient						
	098h		3D YC flag						
	1B5h	5B5h	MV detection 1						
	1B6h	5B6h	MV detection 2						
	1B7h	5B7h	MV detection 3						

[1-10] VDEC SIGNAL INFO 2



Displays signal status that is input to VDEC.

Device	Sub Address (Main screen)	Sub Address (Sub screen)	Meaning
	205h	605h	CC detection 1
	208h	608h	CC detection 2
VDEC	20Bh	60Bh	CC-CRI detection
	20Ch	60Ch	XDS content advisory 0
	20Dh	60Dh	XDS content advisory 1

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[2] PANEL FACTORY (+)

■ Operation Items

No.	Function	Content	RS-232C
[2-1]	PANEL INFORMATION		
[2-2]	PANEL WORKS		
[2-3]	POWER DOWN		
[2-4]	SHUT DOWN		
[2-5]	PANEL-1 ADJ (+)		
[2-6]	PANEL-2 ADJ (+)		
[2-7]	PANEL FUNCTION (+)		
[2-8]	ETC. (+)		
[2-9]	RASTER MASK SETUP (+)		
[2-10]	PATTERN MASK SETUP (+)		
[2-11]	COMBI MASK SETUP (+)		

Note: For details on the setting items, refer to the Service manual of the PLASMA DISPLAY (KRP-600P, KRP-500P).

[3] PANEL MAIN FACTORY (+)

■ Operation Items

No.	Function	Content	RS-232C
[3-1]	PM NG INFO		
[3-2]	PM STATE INFO		
[3-3]	DP_RX INFO		
[3-4]	PM_SETUP (+)		

Note: For details on the setting items, refer to the Service manual of the PLASMA DISPLAY (KRP-600P, KRP-500P).

[4] OPTION

Operation item

No.	Function	Content	RS-232C
[4-1]	CH PRESET <=>	Set the channel map for production line	SCP
[4-2]	AFT <=>	Set AFT of the Analog broadcasting	AFT
[4-3]	SYNC DET (+)	Set the synchronized signal detection of VDEC	
[4-4]	CTI (+)	Set the synchronized signal detection of VDEC	

[4-1] CH PRESET <=>

Exclusively used for production line.

[4-2] AFT <=>

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Exclusively used for production line.

[4-3] SYNC DET (+)

Exclusively used for technical analysis (details omitted).

[4-4] CTI (+)

Exclusively used for technical analysis (details omitted).

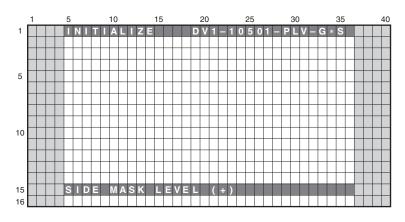
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[5] INITIALIZE

Operation item

No.	Function	Content	RS-232C
[5-1]	SIDE MASK LEVEL (+)	Configure the color of the side mask.	SML
[5-2]	FINAL SETUP	Initialize flash memorys on virgin product status	FST
[5-3]	Wide XGA AUTO <=>	Exclusively used for technical analsyis.	
[5-4]	AUTO ADJUST. <=>	Perform the auto-adjustment setting process	

[5-1] SIDE MASK LEVEL (+)

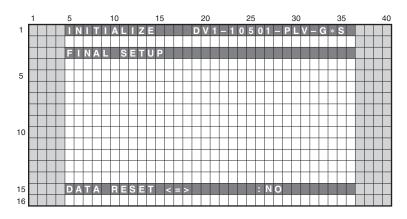


To configure sidemask level (To adjust the values, input signal is required).

Display Item	Content	RS-232C
SIDE MASK LEVEL <=>	Adjust Side Mask level (Adjustable range: 000 to 255, Initial value: 115)	SML

Note: In this mode (SIDE MASK LEVEL), adjustment value cannot changed with the VOLUME +/- keys.

[5-2] FINAL SETUP



- To reset each memory values to factory default values. Factory command is "FST".
- When the configuration is set to <NO> and the [ENTER/SET] key is pressed, no action is taken and the menu returns to previous screen.

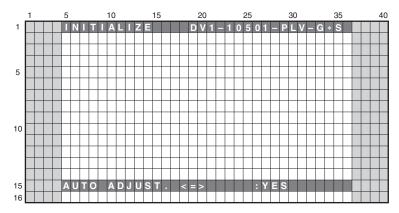
KRP-M01

- When the configuration is set to <GENERAL> and the [ENTER/SET] key is pressed for 5 seconds, the reset action executes as the general model.
- When the configuration is set to <CHINA> and the [ENTER/SET] key is pressed for 5 seconds, the reset action executes as the China model.

Be sure to disconnect and connect the AC cable after FINAL SETUP. When replacing the MAIN BLOCK Assy, the FINAL SETUP is required.

Exclusively used for technical analysis (details omitted).

[5-4] AUTO ADJUST. <=>



- When the configuration is set to <NO> and the [ENTER/SET] key is pressed, no action is taken and the menu returns to previous screen.
- When the configuration is set to <YES> and the [ENTER/SET] key is pressed for 5 seconds, the auto-adjustment action executes.
- Be sure to power off with the remote control unit or disconnect and connect the AC cable after the auto-adjustment is completed.
- When some ICs on the MAIN BLOCK Assy are replaced individually, auto-adjustment is required. For details on IC numbers, see the list "■ Parts whose replacement is difficult" in "6.1 ADJUSTMENT REQUIRED WHEN THE UNIT IS REPAIRED OR REPLACED."

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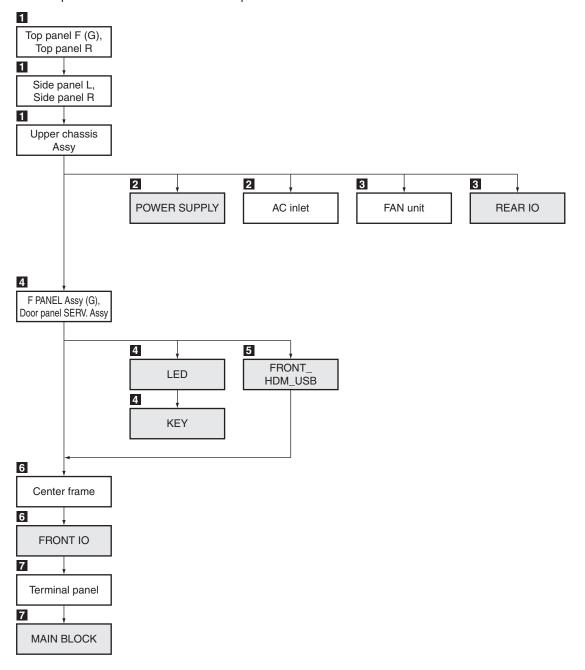
5. DISASSEMBLY

5.1 FLOWCHART OF REMOVAL ORDER

Note: Even if the unit shown in the photos and illustrations in this manual may differ from your product, the procedures described here are common.

Flowchart of removal order for the main parts and boards

It is efficient to proceed with removal of the main parts and boards in the order shown in the chart below:



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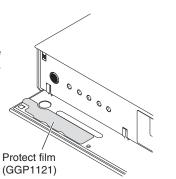
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Preparations

- The high-gloss resin parts of the exterior of this product are easily scratched. During disassembly and reassembly of this product, be careful not to scratch the exterior.
- If the door of this product is pressed firmly from the front or when the KEY Assy and LED Assy are reassembled, print of the front-panel operating section may be transferred to the inside surface of the door. To avoid this, be sure to attach the protect film to the inside surface of the door before repairing. If protect film is not available, slip a cleaning cloth or the like inside the door for protection.
- Remove the attached protect film after product installation is completed. If the repaired product is to be delivered to the customer's home or a dealer, leave the protect film attached.



Note on Disassembly/Reassembly

Fixing screws for the HDMI connector and system cable connector

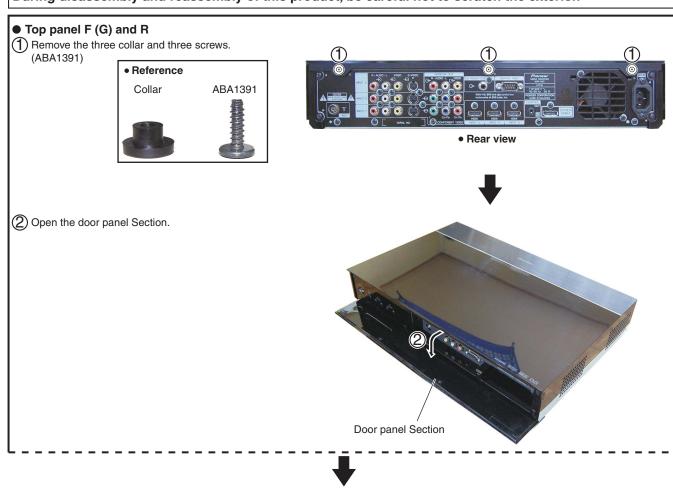
For tightening the screws for the HDMI connector and system cable connector, do not use an electric screwdriver. Tighten them manually. If they are tightened too forcefully with an electric screwdriver, the screw heads may be damaged, in which case the screws cannot be loosened/tightened any more.

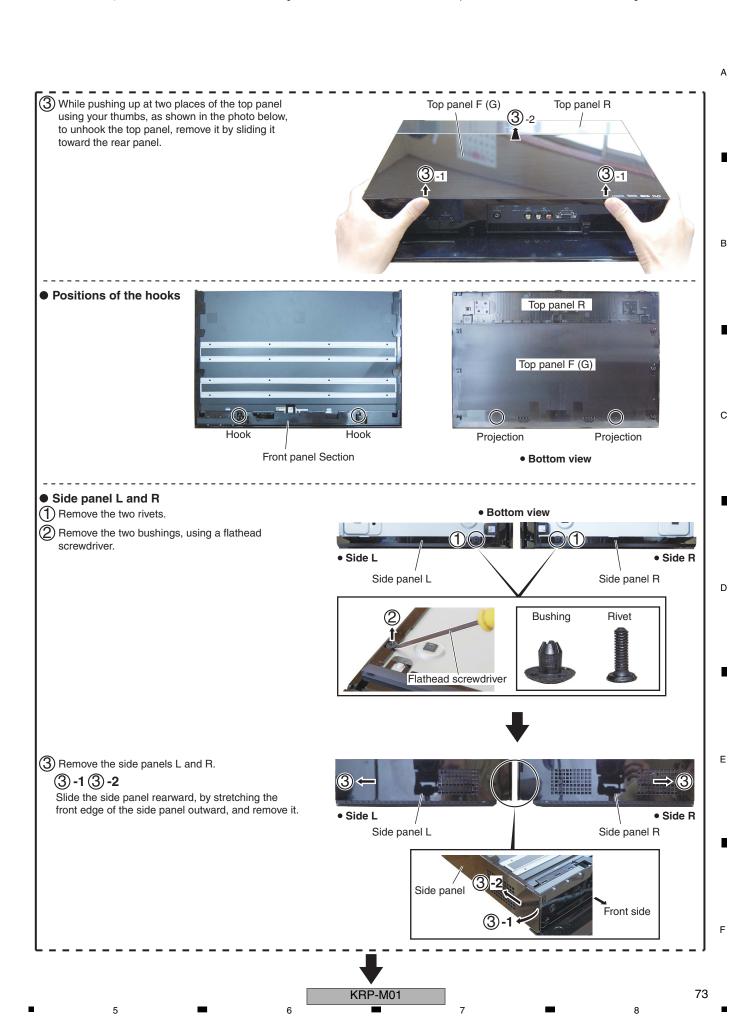
1 Exterior Section

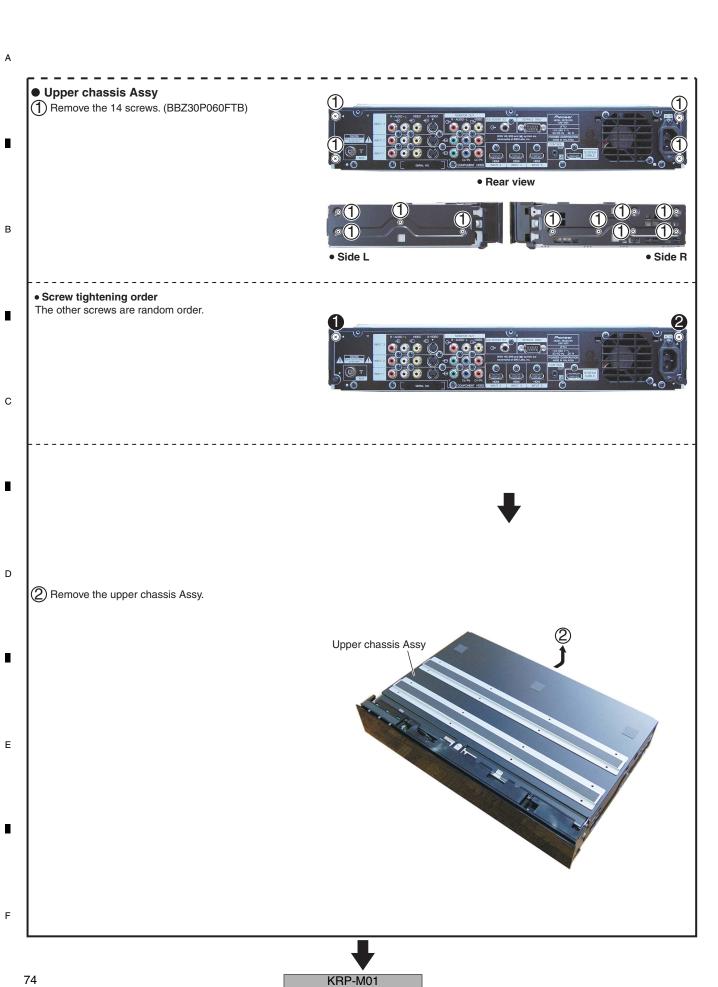
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The high-gloss resin parts of the exterior of this product are easily scratched.

During disassembly and reassembly of this product, be careful not to scratch the exterior.

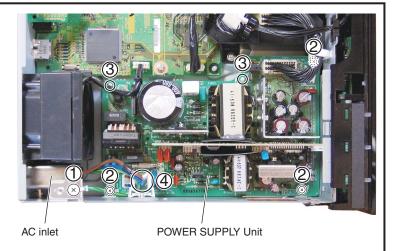






2 POWER SUPPLY Unit

- (1) Remove the one screw. (BMP40P080FSN)
- 2 Remove the three screws. (BBB30P080FSN)
- Remove the two circuit board spacers.
- (4) Release the jumper wire.

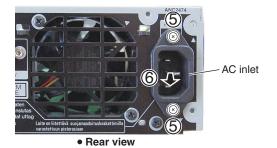




- (5) Remove the two screws. (ABZ30P080FTB)
- 6 Remove the AC inlet.
 - An installation direction of the AC inlet

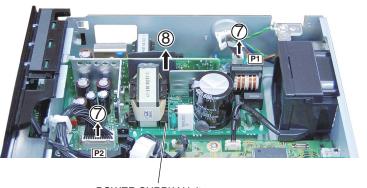








- Disconnect the two connectors.
- Remove the POWER SUPPLY Unit.



POWER SUPPLY Unit

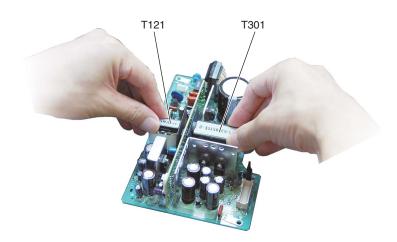
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■ Notes on Removing the POWER SUPPLY Unit

• How to lift up the POWER SUPPLY Unit

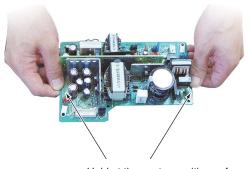
When you remove the POWER SUPPLY Unit from the chassis, first lift the board by pinching T121 and T301 transformers with your fingers. When the board is lifted up to a certain height, hold it by hand. NEVER hold the board by the radiator that is adjacent to the transformer.

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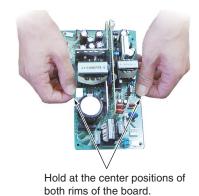


• How to hold the board after removing it from the chassis

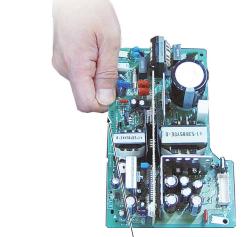
The following two ways are recommended for holding the POWER SUPPLY Unit:



Hold at the center positions of both rims of the board.

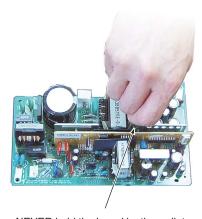


Ways to be avoided:



NEVER hold a corner of the board with one hand.

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NEVER hold the board by the radiator with one hand.

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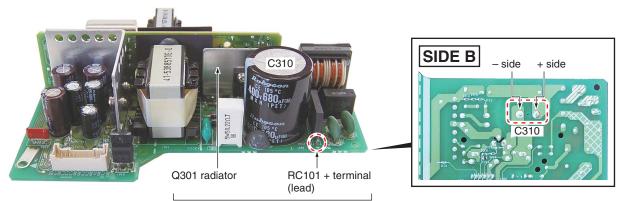
Note on Removing the POWER SUPPLY Unit from the Chassis and Method for Resetting Standby Power Latchup

For 3-5 minutes after the unit is turned off, residual electric charge remains in the C310 capacitor on the POWER SUPPLY Unit. Before removing the POWER SUPPLY Unit from the chassis, be sure to confirm that residual charge inside the POWER SUPPLY Unit has become sufficiently low. (Without forced discharge, residual charge that remains after 3-5 minutes will fall to one-tenth or less, which is still about 20 V. Therefore, even after the POWER SUPPLY Unit is removed, it is recommended to perform forced discharge on the POWER SUPPLY Unit, as shown below.)

For quick removal of residual charge, forced discharge is recommended, using two 220 ohm/10 W resistors (440 ohm/20 W).

How to remove the POWER SUPPLY Unit

- 1. Make sure that the AC power cord is unplugged. Using a tester, check the voltage between the + terminal of RC101 bridge diode and Q301 radiator (equivalent to the voltage between two electrodes of C310).
- 2. Let the unit sit for more than 5 minutes until the voltage equivalent to that between two electrodes of C310 falls to under 20 V.
- 3. After checking that the voltage is under 20 V, disconnect the connectors of the POWER SUPPLY Unit and remove the POWER SUPPLY Unit.
- 4. Using two resistors mentioned above, completely discharge residual charge from C310.

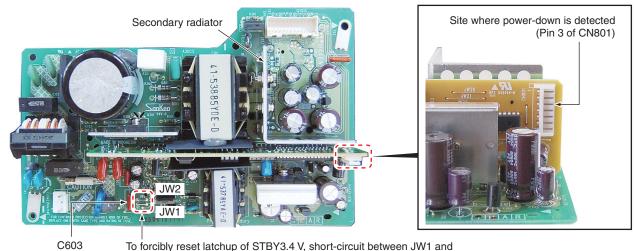


After checking that the voltage at the measurement points (equivalent to the voltage between two electrodes of C310) is under 20 V, remove the POWER SUPPLY Unit.

Then, completely discharge residual charge, using resistors.

How to reset Standby power latchup (In a case where the protection against Standby power excess voltage is activated)

- 1. After removing the causes of the malfunction, short-circuit between the JW1 and JW2 jumpers.
- 2. If the POWER SUPPLY Unit functions properly, after opening the above jumpers, the unit starts up.



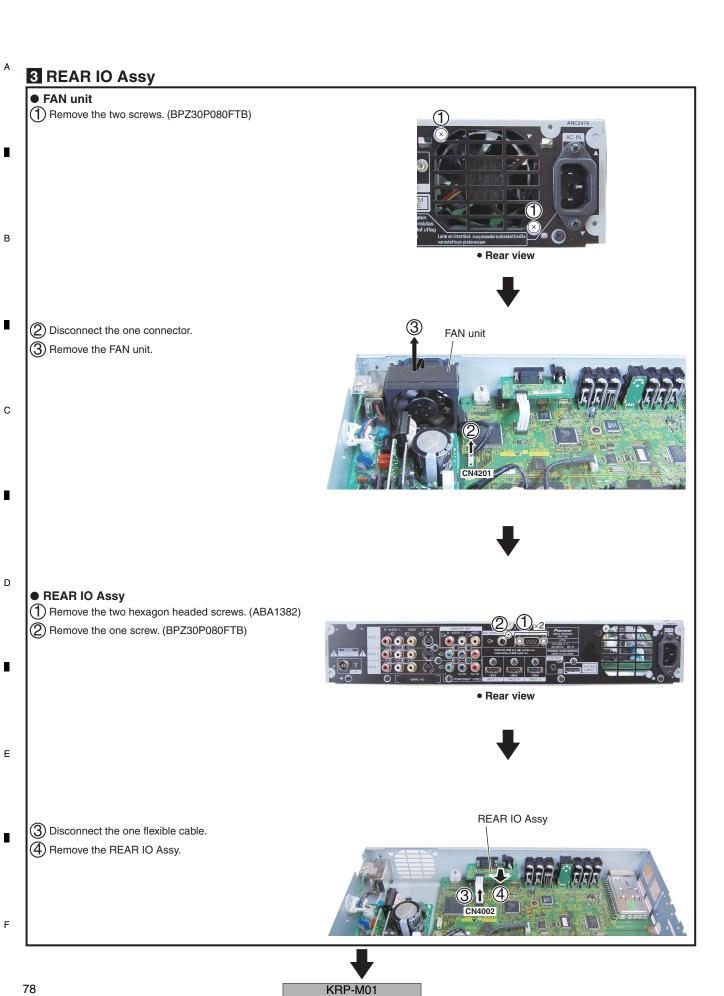
JW2 (near C603), using a flathead screwdriver or similar object.

If the causes of the malfunction are removed, after opening the jumpers, the unit starts up.

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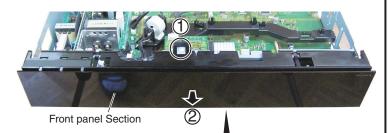
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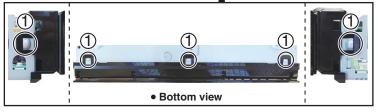


4 Front Panel Section

Front panel Section

- 1 Unhook the six hooks.
- (2) Remove the front panel Section.

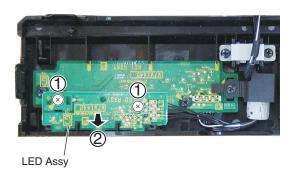






LED and KEY Assys

- (1) Remove the two screws. (BPZ30P080FTB)
- (2) Remove the LED Assy.



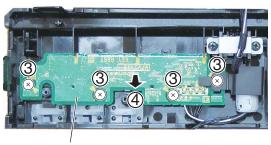


- Remove the four screws. (BPZ30P080FTB)
- (4) Remove the KEY Assy.

Note:

Before tightening screws, make sure that the protect film has been attached.

(For details on the place at which the protect film is to be attached, see "■ Preparations.")



KEY Assy



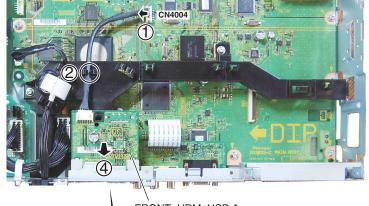
5 FRONT_HDM_USB Assy

1 Disconnect the one connector.

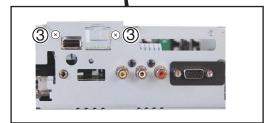
2 Release the jumper wire.

Remove the two screws. (BBZ30P060FTB)

(4) Remove the FRONT_HDM_USB Assy.



FRONT_HDM_USB Assy



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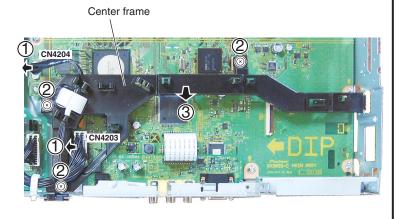
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6 FRONT IO Assy

Center frame

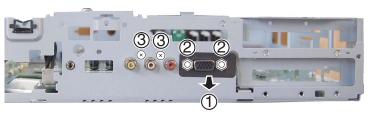
- 1 Disconnect the two connectors.
- 2 Remove the three screws. (ABA1383)
- Remove the center frame.





● FRONT IO Assy

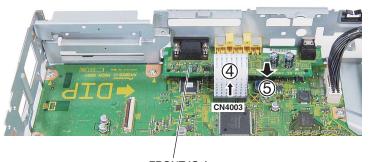
- 1 Remove the cover sheet.
- 2 Remove the two hexagon headed screws. (ABA1382)
- (3) Remove the two screws. (BPZ30P080FTB)







- (4) Disconnect the one flexible cable.
- (5) Remove the FRONT IO Assy.



FRONT IO Assy



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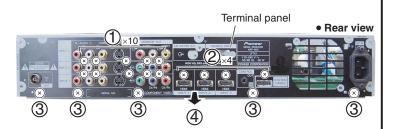
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7 MAIN BLOCK Assy

Terminal panel

- (1) Remove the 10 screws. (BPZ30P080FTB)
- (2) Remove the four screws. (BMZ30P060FTB)
- Remove the five screws. (BBZ30P060FTB)
- (4) Remove the terminal panel.





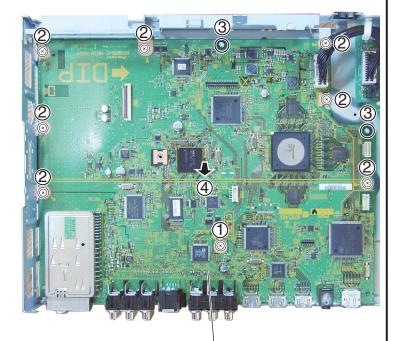
MAIN BLOCK Assy

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- (1) Remove the one screw. (AMZ30P060FTB)
- 2 Remove the seven screws. (ABA1383)
- 3 Remove the two circuit board spacers.
- (4) Remove the MAIN BLOCK Assy.



MAIN BLOCK Assy

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6. EACH SETTING AND ADJUSTMENT

The following items in the service manual of KRP-M01/WYSIXK5 are not applicable to this model.

HOW TO UPDATE USB HOW TO UPDATE DISPLAY PORT FIRMWARE



- 1. At shipment, the unit is adjusted to its best conditions. Normally, it is not necessary to readjust even if an assembly is replaced. If the adjustment is shifted or if it becomes necessary to readjust because of part replacement, etc., perform the adjustment as described below.
- 2. Any value changed in Service/Factory mode will be stored in memory as soon as it is changed. Before readjustment, take note of the original values for reference in case you need to restore the original settings.
- 3. Use a stable AC power supply.

6.1 ADJUSTMENT REQUIRED WHEN THE UNIT IS REPAIRED OR REPLACED

■ When any of the following assemblies is replaced

POWER SUPPLY Unit	\Rightarrow	No adjustment required
MAIN BLOCK Assy (*)	\rightarrow	Execute section [5-4] AUTO ADJUSTMENT of 4.2 [5] INITIALIZE.
Other assemblies	\Rightarrow	No adjustment required

(*): When replacing the MAIN BLOCK Assy, be sure to perform the FINAL SETUP.

■ Replacement of the whole Assy is required when one of the following part on the corresponding Assy is in failure

PCB Assy No.	Assy Name	Ref No.	Function Name	Part No.	Reason
		IC5002	HDCP EEPROM	BR24L02FV-W	
		IC5003	HDCP EEPROM	BR24L02FV-W	
		IC5004	HDCP EEPROM	BR24L02FV-W	Because adjustments and data writing at
AWW1431	MAIN BLOCK	IC7004	EMMA2 EEPROM	BR24L64F-W	the level of production line are required after replacement
	Assy	IC6701	Flash ROM	AGC1091	and replacement
		IC6811	IF UCOM	AGC1086	
		IC7202	Flash ROM	AGC1090	
AWW1443	FRONT IO Assy	IC8501	PC EEPROM	BR24L01AFJ-W	Because adjustments and data writing at the level of production line are required after replacement

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■ Part whose replacement is difficult

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PCB Assy No.	Assy Name	Ref No.	Function Name	Part No.	Reason
AXY1223	POWER SUPPLY Unit	U0003	_	_	The maker forbids Pioneer from repairing the Assy.
		IC7003	SYSTEM IC (EMMA2)	UPD61123F1-100KA3A-K	Because these ICs are packaged in BGA
		IC6501	ASIC (ARIA)	PD6568A-K	
		IC6702	DDR SDRAM (ARIA)	EDD1232ABBH-5C-E-K	
		IC6703	DDR SDRAM (ARIA)	EDD1232ABBH-5C-E-K	
AWW1431	MAIN BLOCK	IC6704	DDR SDRAM (ARIA)	EDD1232ABBH-5C-E-K	
	Assy	IC4801	ADC	AD9985KSTZ	Because these ICs require readjustment
		IC5101	AV SW	R2S11006FT	after replacement
		IC5501	RGB SW	R2S11001FT	
		IC4702	VDEC	CM0048BF	
		IC4901	HDMI	SII9135CTU-K	Because a radiation pad is provided
		IC4601	Regulator	LTC3407EMSE-2	

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■ Adjustment Procedures After a Part that Requires Readjustment is Replaced

Execute section [5-4] AUTO ADJUST. <=> of 4.2 [5] INITIALIZE.

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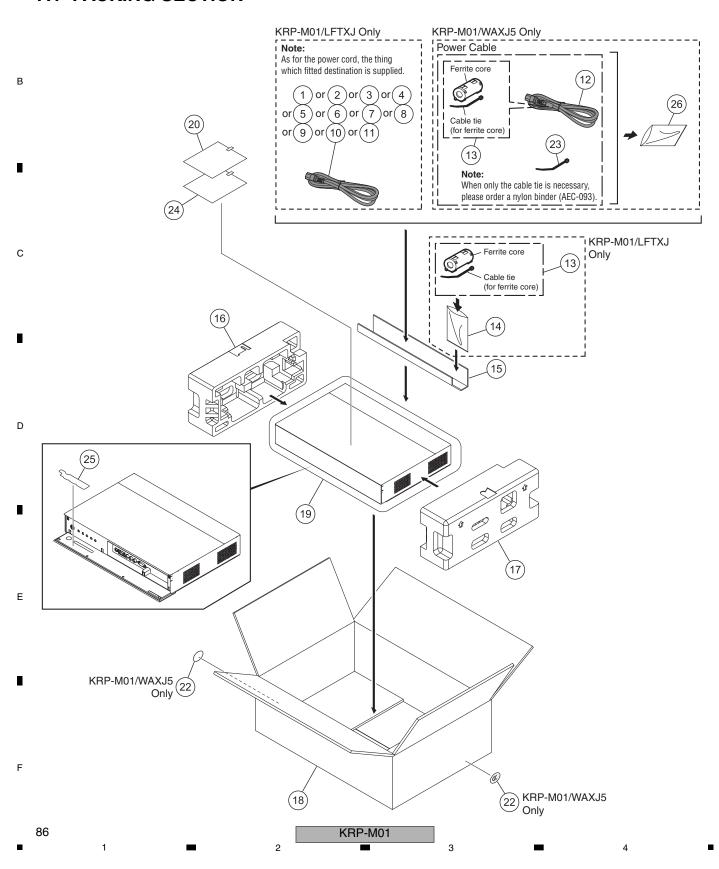
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7. EXPLODED VIEWS AND PARTS LIST

NOTES: • Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.

- The riangle mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- Screws adjacent to ▼ mark on product are used for disassembly.
- For the applying amount of lubricants or glue, follow the instructions in this manual. (In the case of no amount instructions, apply as you think it appropriate.)

■ 7.1 PACKING SECTION



(1) PACKING SECTION PARTS LIST

Mark No.	<u>Description</u>	Part No.	Mark No	<u>0.</u>	<u>Description</u>	Part No.	
∴ NSP 1	Power Cord	See Contrast table (2)	1	16	Pad L (G)	AHA2739	
⚠ NSP 2	Power Cord	See Contrast table (2)	1	17	Pad R (G)	AHA2740	Α
⚠ NSP 3	Power Cord	See Contrast table (2)	1	18	Carton	See Contrast table (2)	
∴ NSP 4	Power Cord	See Contrast table (2)	1	19	Mirror Mat	AHG1420	
⚠ NSP 5	Power Cord	See Contrast table (2)	2	20	Caution Card	See Contrast table (2)	
<u></u> NSP 6	Power Cord	See Contrast table (2)	2	21	••••		
♠ NSP 7	Power Cord	See Contrast table (2)	NSP 2	22	CCC S&E Label	See Contrast table (2)	•
<u> </u>	Power Cord	See Contrast table (2)	2	23	Nylon Binder	See Contrast table (2)	
	Power Cord	See Contrast table (2)	2	24	Film Caution Card	See Contrast table (2)	
⚠ NSP 10	Power Cord Assy	See Contrast table (2)	2	25	Protect Film	GGP1121	
<u>^</u> 11	Power Cord Set	See Contrast table (2)	2	26	Vinyl Bag	See Contrast table (2)	В
⚠ 12	Power Cord	See Contrast table (2)					
<u> </u>	Ferrite Core (L5208)	ATX1039					
14	Polyethylene Bag	See Contrast table (2)					
15	ACC Carton (G)	AHD3679					

(2) CONTRAST TABLE KRP-M01/LFTXJ and WAXJ5 are constructed the same except for the following:

THE WORLD TABLE WAXOO are constructed the same except for the following.					
Mark	No.	Symbol and Description	KRP-M01/LFTXJ	KRP-M01/WAXJ5	
⚠ NSP	1	Power Cord	ADG1232	Not used	
⚠ NSP	2	Power Cord	ADG1234	Not used	
⚠ NSP	3	Power Cord	ADG1236	Not used	
⚠ NSP	4	Power Cord	ADG1239	Not used	
⚠ NSP	5	Power Cord	ADG1241	Not used	
⚠ NSP	6	Power Cord	ADG1243	Not used	
⚠ NSP	7	Power Cord	ADG1244	Not used	
♠ NSP	SP 8 Power Cord		ADG1246	Not used	
⚠ NSP	9	Power Cord	ADG1251	Not used	
⚠ NSP	10	Power Cord Assy	AWX1095	Not used	
\triangle	11	Power Cord Set	AXY1194	Not used	
<u> </u>	12	Power Cord	Not used	ADG1209	
	14	Polyethylene Bag	AHG1433	Not used	
	18	Carton (G)	AHD3676	Not used	
	18	Carton (C)	Not used	AHD3685	
	20	Caution Card	ARM1444	ARM1446	
NSP	22	CCC S&E Label	Not used	DRW2310	
	23	Nylon Binder	Not used	AEC-093	
	24	Film Caution Card	ARM1449	ARM1451	
	26	Vinyl Bag	Not used	AHG1336	

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EXTERIOR SECTION PARTS LIST

		JR SECTION PARTS LI	
<u>Mark</u>	<u>No.</u>	<u>Description</u>	Part No.
	1	FRONT_HDM_USB Assy	AWW1432
	2	FRONT IO Assy	AWW1443
		••••	
<u> </u>	4	Ferrite Core (F1001)	ATX1034
	5	••••	
	6	••••	
	7	Flexible Cable (J201)	ADD1564
	8	••••	
	9	••••	
	10	••••	
		USB Cable (J102)	ADX3713
		Upper Chassis Assy	ANA2224
		PCB Holder	ANG3217
		Cover Sheet	AAK2850
	15	Collar	ABN1095
		Upper Cushion	AEB1504
		Top Cushion	AEB1505
		Scrivet	AEC1657
		Ferrite Core Holder	AEC1818
	20	Edge Saddle	AEC1946
	٠.	0 . 5	AMPAGAA
•		Center Frame	AMR3844
<u> </u>		Gasket HP	ANK1994
		Rubber Foot	VEB1349
		Top Panel F (G)	AAK2962
	25	Side Panel L	AAK2941
	00	Cida Danal D	A A K 0 0 4 0
		Side Panel R	AAK2942
		Top Panel R	AAK2946
		••••	
ı.		Gasket LIP2	∆NK1000
<u> </u>	30	Gasket UP2	ANK1999
	21	••••	
		••••	
		••••	
		••••	
		Hexagon Headed Screw	ABA1382
	00	Jagon i loudou Golow	, 15, 11002
	36	Screw	ABA1383
		Screw	ABA1391
		Screw	ABZ30P060FTC
		Screw	BBZ30P060FTB
		Screw	BPZ30P080FTB
	.0	==:•::	00. 0001 12

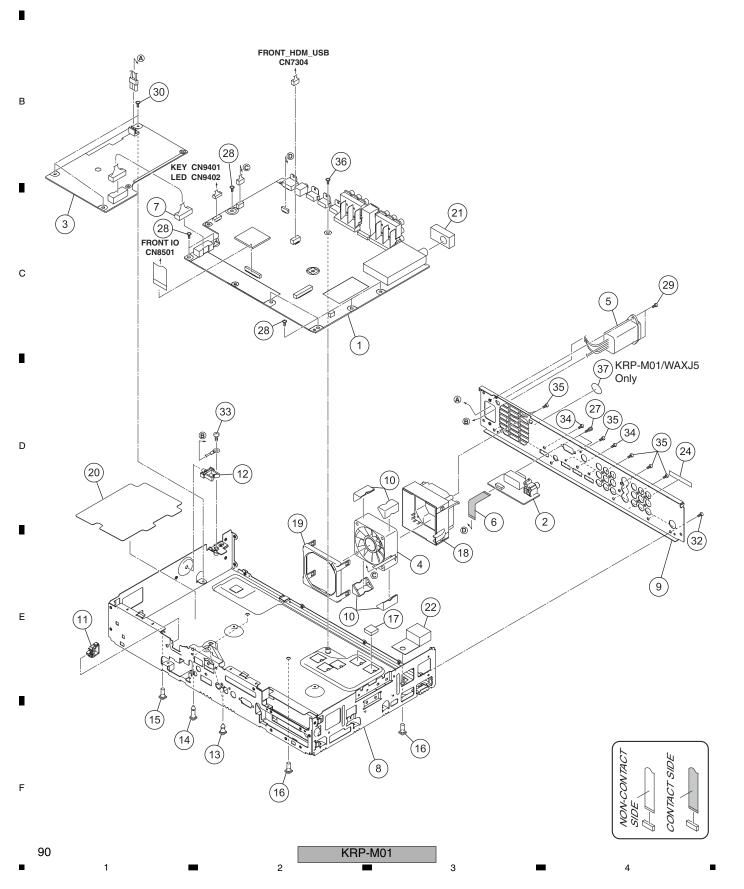
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7.3 BOTTOM SECTION

Cleaning paper : GED-008



(1) BOTTOM SECTION PARTS LIST

Mark No	Description	Part No.	<u>Mark</u>	<u>No.</u>	<u>Description</u>	Part No.	
	1 MAIN BLOCK Assy	AWW1431	<u> </u>	21	Gasket GE	ANK1984	
	2 REAR IO Assy	AWW1461	<u> </u>	22	Gasket GE2	ANK1986	Α
<u> </u>	3 POWER SUPPLY Unit	AXY1223		23	••••		
<u> </u>	4 DC FAN Motor 60 x 25L	AXM1068	NSP	24	Serial Label	ARW1100	
\triangle	5 AC Inlet (CN1)	AKP1339		25	••••		
	6 Flexible Cable (J207)	ADD1568		26	••••		
	7 26P Housing Wire (J111)	ADX3674		27	Hexagon Headed Screw	ABA1382	-
	8 Base Chassis Assy	ANA2225		28	Screw	ABA1383	
	9 Terminal Panel	See Contrast table (2)		29	Screw	ABZ30P080FTB	
1	0 Floating Rubber 60	AEB1410		30	Screw	BBB30P080FSN	
1	1 Reuse Clamp	AEC2129		31	••••		В
1	2 Reuse Wire Saddle	AEC2134		32	Screw	BBZ30P060FTB	
1	3 Circuit Board Spacer	AEC2150		33	Screw	BMP40P080FSN	
1	4 Circuit Board Spacer	AEC2151		34	Screw	BMZ30P060FTB	
1	5 Circuit Board Spacer	AEC2152		35	Screw	BPZ30P080FTB	
1	6 Circuit Board Spacer	AEC2163		36	Screw	AMZ30P060FTB	
1	7 Silicon Sheet	AEH1182	NSP	37	CCC S&E Label	See Contrast table (2)	
1	8 FAN Holder 60 A	AMR3918					
1	9 FAN Holder 60 B	AMR3919					
2	0 Insulation Sheet	AMR3891					

(2) CONTRAST TABLE KRP-M01/LFTXJ and WAXJ5 are constructed the same except for the following:

Mark	No.	Symbol and Description	KRP-M01/LFTXJ	KRP-M01/WAXJ5
	9	Terminal Panel (G)	ANC2476	Not used
	9	Terminal Panel (C)	Not used	ANC2477
NSP	37	CCC S&E Label	Not used	DRW2310

KRP-M01

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MAIN BLOCK CN4204

7.4 FRONT PANEL SECTION

A disassembly direction of the door section

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FRONT PANEL SECTION PARTS LIST

Mark No.		<u>Description</u>	Part No.
	1	LED Assy	AWW1442
	2	KEY Assy	AWW1445
	3	20P Housing Wire (J112)	ADX3714
<u> </u>	4	Ferrite Core (F1002)	ATX1069
	5	F Panel Assy (G)	AMB3116
NSP	6	Damper Holder	ANG3198
	7	Magnet Holder Assy	AEC1077
	8	Damper	AXA1022
NSP	9	Shading Sheet	AMR3903
NSP	10	Front Panel (G)	AMB3111
NSP	11	Control Button	AAD4160
	12	Input Sheet (G)	AAL3039
NSP	13	Front LED Lens L	AMR3841
NSP	14	Front LED Lens C	AMR3904
NSP	15	Front LED Lens R	AMR3905
	16	Screw	BPZ30P080FTB
	17	Door Panel Service Kit	GXX1283
	18	Door Catcher	••••
	19	Door Base	••••
	20	Door Panel	••••
	21	Door Cushion	AED1337
	22	Ferrite Holder	AMR3925
	23	••••	
	24	••••	
	25	Screw	BPZ30P080FTB

KRP-M01 93

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■ When Replacing the F PANEL Assy (E)

When replacing the F PANEL Assy (E), discard the following parts of the new Assy kit for service and use the parts from the original door panel:

No.18 Door catcher No.19 Door base No.21 Door cushion

■ Reassembly Procedures for the Door Panel Service Kit

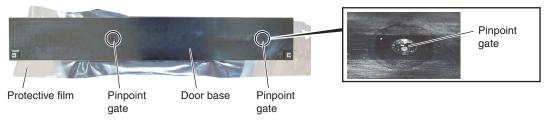
• Component parts of the GXX1283 Door Panel Service Kit

No.18 Door catcher (x2)
No.19 Door base (x1)
No.20 Door panel (x1)
No.21 Door cushion (x2)

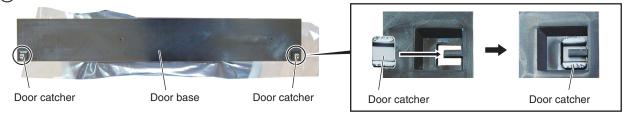
Check that two marks of pinpoint gates do not protrude from the surface of the door base to which the door panel is to be attached.

Do NOT peel off the protective film of the door base in this step.

Peel it off after all the reassembly procedures are completed.



2 Attach the two door catchers.



Peel off the separator of double-back tape on the door panel.

Do NOT peel off the protective film on the exterior surface of the door panel in this step.

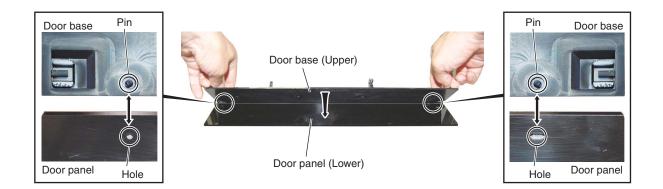
Peel it off after all the reassembly procedures are completed.

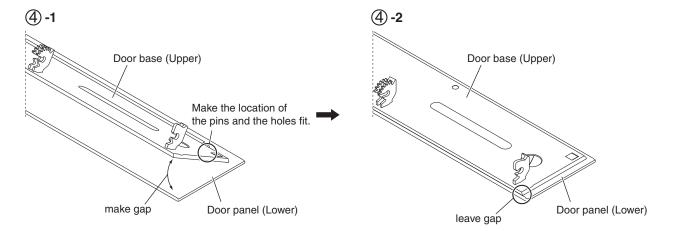
94

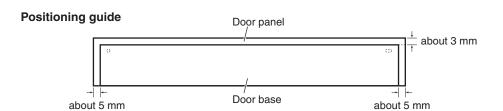
Е

Align the two positioning pins of the door base with the holes in the door panel.

When positioning, leave gaps between the door panel and door base, as shown in the figure below:







- (5) Stick the door base and door panel together, by pressing them all over.
- (6) Attach the two door cushions.

KRP-M01

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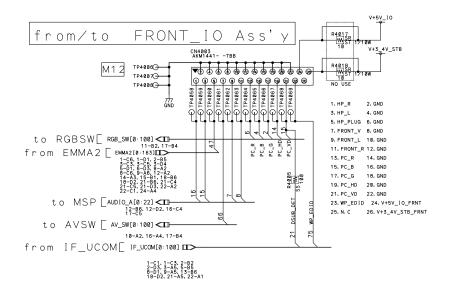
D

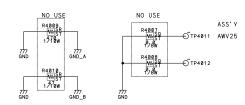
Е

8. SCHEMATIC DIAGRAM

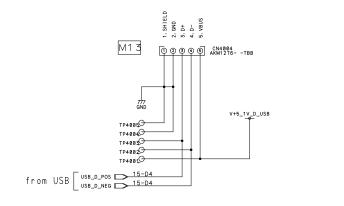
8.1 MAIN BLOCK ASSY (1/24) [BOARD_IF_0 BLOCK]

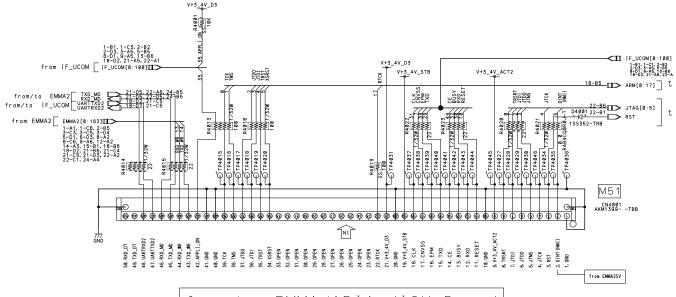
3





from/to FRONT HDMI USB Ass'y





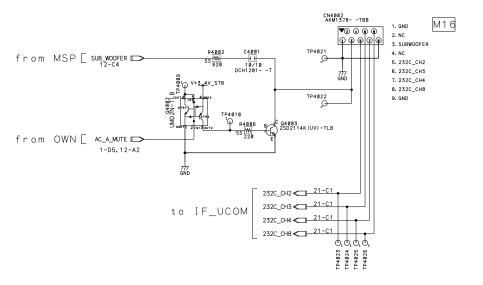
from/to EMMA/ARIA JIGU Board

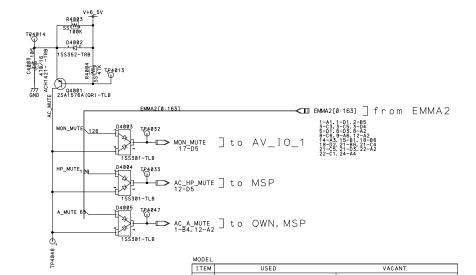
96

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from/to REAR_IOAss'y





4001-4010, 4013-4023

4001. 4003 4001-4003 4001-4005 4001-4004



M 5 1 CN4001 AKM1399- -TBB

from EMMA2SV

MAIN ASSY (MR_IBD) (01/25) BOARD_IF_0 BLOCK

AWV2595- ; AWW1431

4007-4010, 4017, 4018

.....

KRP-M01

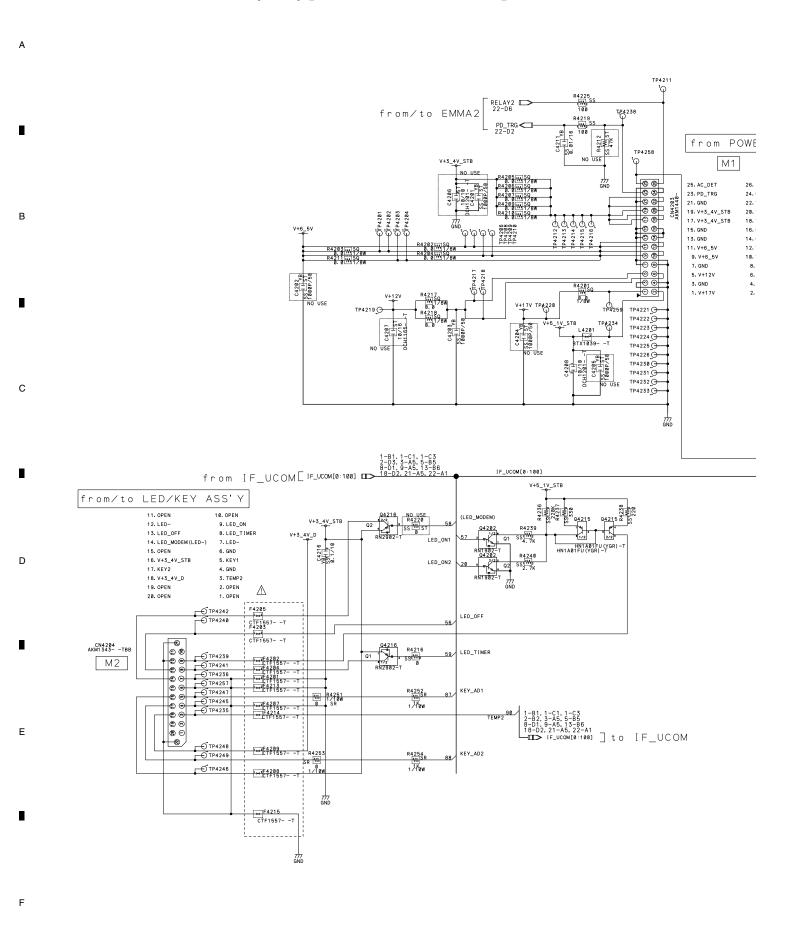
97

В

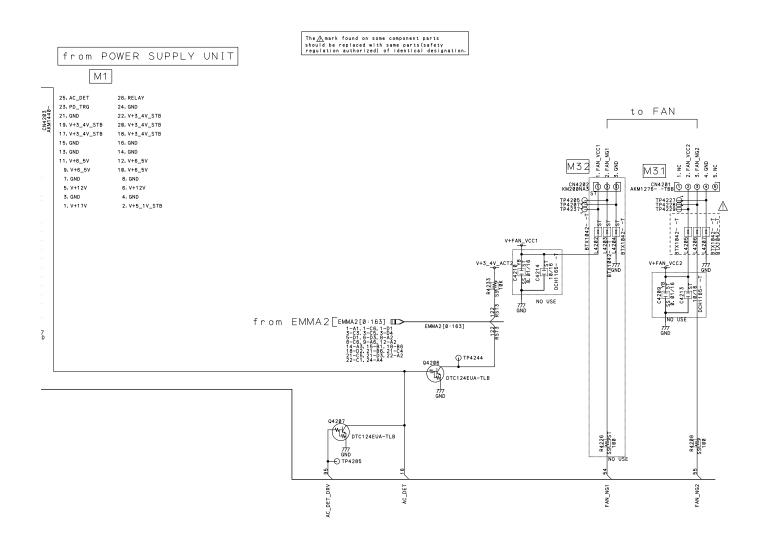
D

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8.2 MAIN BLOCK ASSY (2/24) [BOARD_IF_1 BLOCK]



98



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MODEL		
ITEM	USED	VACANT
R	4201-4212, 4216-4220, 4225, 4226, 4233, 4236-4240, 4251- 4254	4212, 4220, 4226
С	4201-4211, 4213, 4214, 4216	4201, 4202, 4204-4207, 4209, 4210, 4213, 4214
Q	4202, 4206, 4207, 4215, 4216	
10		
F	4201-4205, 4207-4209, 4213-4215	
L	4201-4207	4202-4204
CN	4201-4204	4202

MAIN ASSY (MR_IBD) (02/25)

BOARD_IF_1 BLOCK

AWV2595- ; AWW1431

KRP-M01

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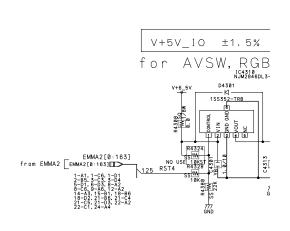
D

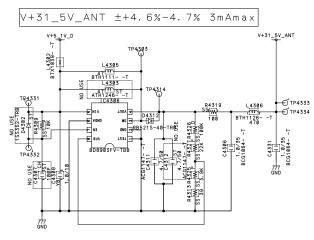
Е

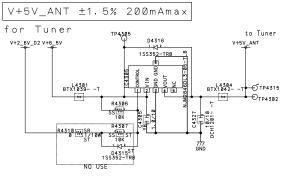
F

8.3 MAIN BLOCK ASSY (3/24) [POWER_0 BLOCK]

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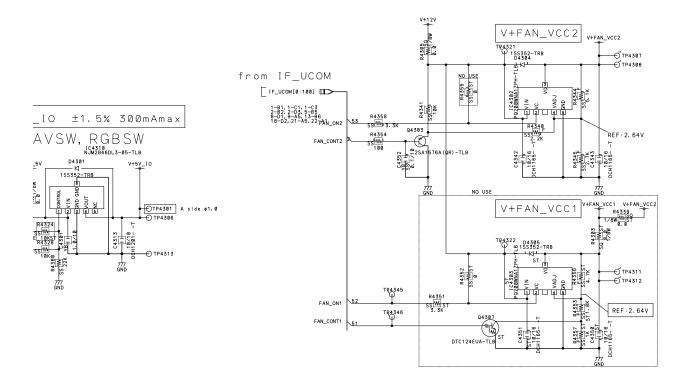


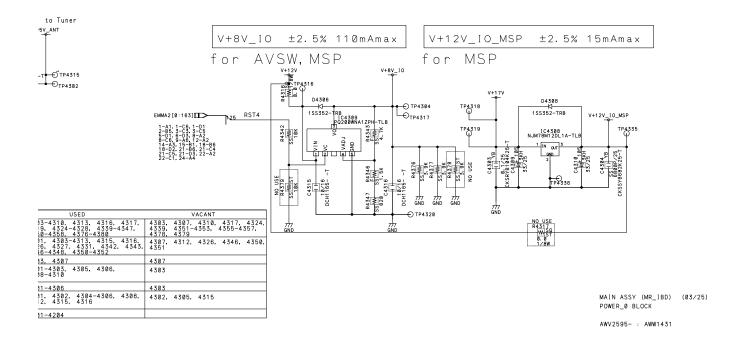
ITEM	USED
R	4303-4310, 4313, 4316, 4 4319, 4324-4328, 4339-43 4350-4358, 4376-4380
С	4301, 4303-4313, 4315, 4 4326, 4327, 4331, 4342, 4346-4348, 4350-4352
Q	4303, 4307
1C	4301-4303, 4305, 4306, 4308-4310
L	4301-4306
D	4301, 4302, 4304-4306, 4 4312, 4315, 4316
CN	4201-4204

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KRP-M01

The ∆mark found on some component parts should be replaced with same parts(safety regulation authorized) of identical designation.





KRP-M01

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V+2_6V_D2 +2%, -1.6% 700mAmax 22-B2 V+5_1V_D +1.7%,-2% 900mAmax (CH. 2) V+1_2V_D2 ±1.3% 1200mAmax (CH. 3) NO USE D4410 1SS352-TRB ST ST R4423 SST WPST DTA124EUÄ-TLB L4413 TP4421 DTC124EUA-TLB TP4401⊖ TP4482 TC124EUA-TLB R4401 O TP4426 04404 DTQ124EU SS 22K L4401 BTX1039- -T L4402 BTX1039- -R4424 SS W 5. 6K R4480 SSIWB 10K R4442 WID 33K R4443 C4441 WISS III 0 220P/! V+5_1V_D C4438 R4420 SS 11 CH SS 12 CH SS 12 CH SS 12 CH R4438 R4421 R4439 SS 120K S 56K 1KS L4414 BTX1039- -TP4407 TEST1 3 TP4408 R4422 WW SR N R4436 SS W 4.7K L4403 BTX1039- -T DCH1165-C4409 L4404 BTX1039- -T C4486 10/16 DCH1165-777 777 777 777 777 GND GND GND GND GND 777 GND

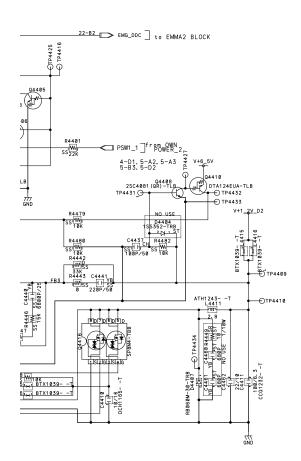
MODEL	
ITEM	USED
R	4401-4410, 4412, 4417, 4 4451, 4461, 4469-4471, 4 4475, 4479-4482
С	4401, 4403-4407, 4409-44 4414, 4415, 4419, 4425-4 4435-4441, 4445-4468
ø	4404-4410, 4413, 4416-44
IC	4402, 4404
L	4401-4406, 4409-4411, 44 4416
D	4401-4408, 4410, 4411
CN	4201-4204

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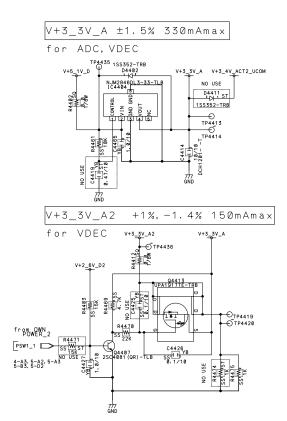
102

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The Amark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.



USED	VACANT				
31-4410, 4412, 4417, 4420- 51, 4461, 4469-4471, 4474, 75, 4479-4482	4404. 4471,	4422, 4474,		4437,	4449,
31, 4403-4407, 4409-4411, 4, 4415, 4419, 4425-4427, 5-4441, 4445-4468	4415, 4449, 4461		4425, 4453,		4446, 4459-
34-4410, 4413, 4416-4418					
12. 4404					
31-4406, 4409-4411, 4413- 6					
31-4408, 4410, 4411	4401. 4411	4403,	4404.	4408,	4410.
11-4204					



MAIN ASSY (MR_IBD) (04/25)

POWER_1 BLOCK

AWV2595- ; AWW1431

KRP-M01

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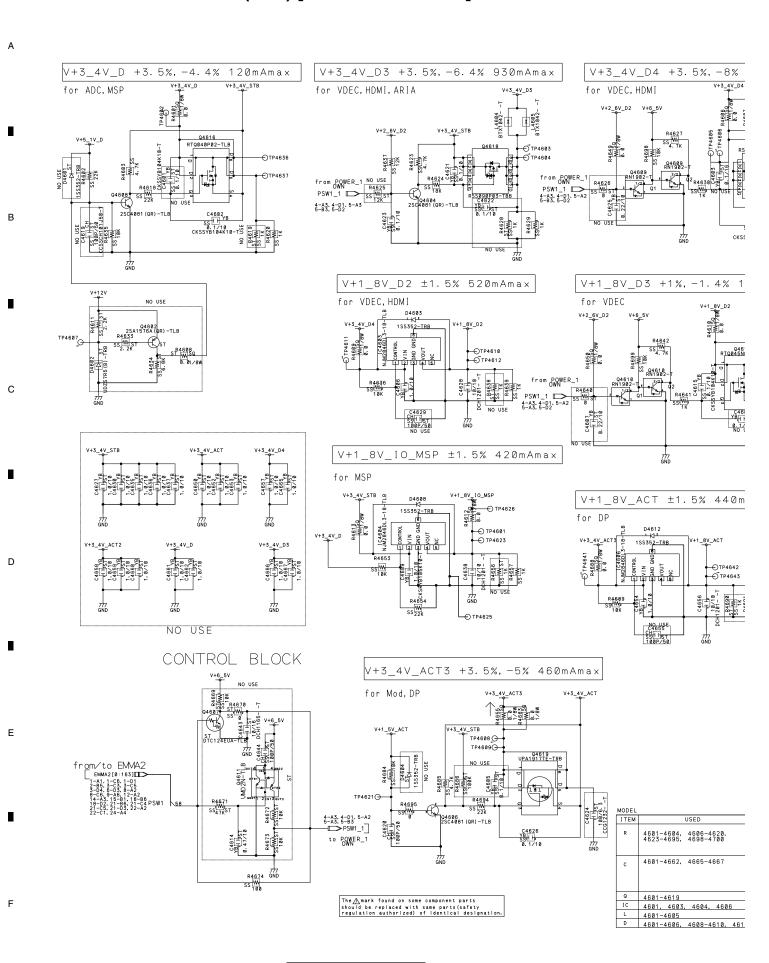
В

С

D

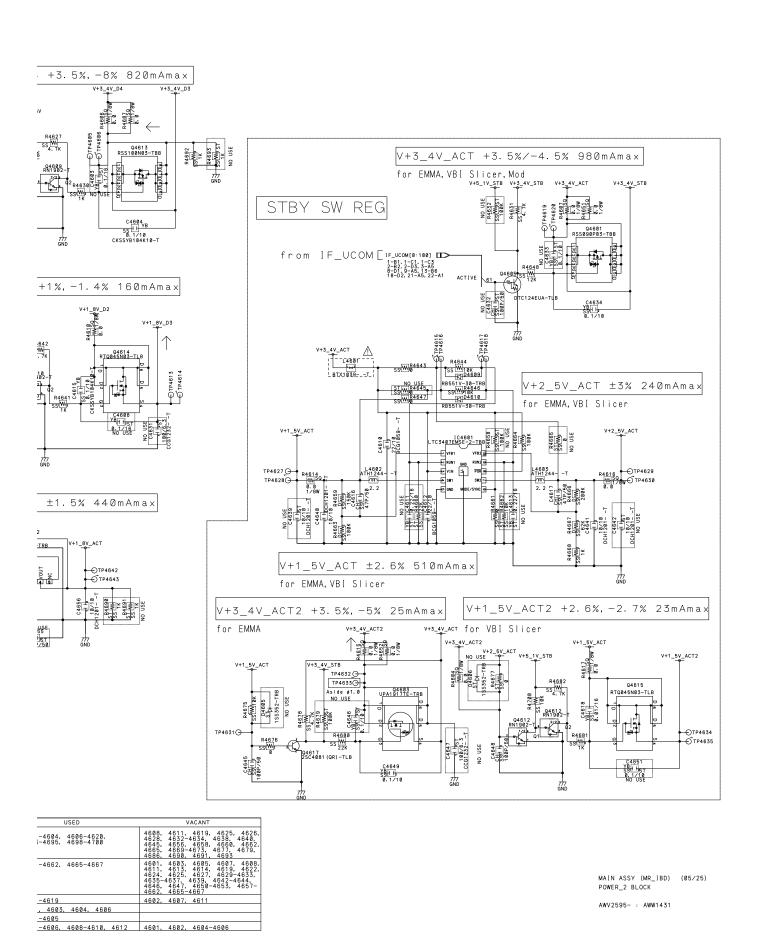
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8.5 MAIN BLOCK ASSY (5/24) [POWER_2 BLOCK]



104

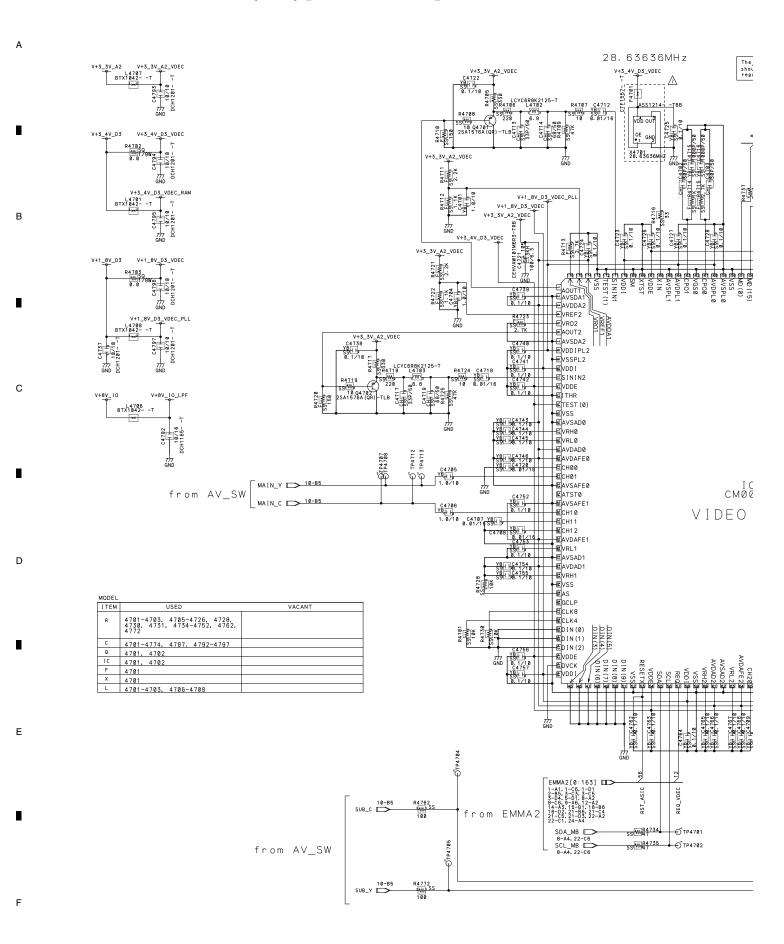
KRP-M01



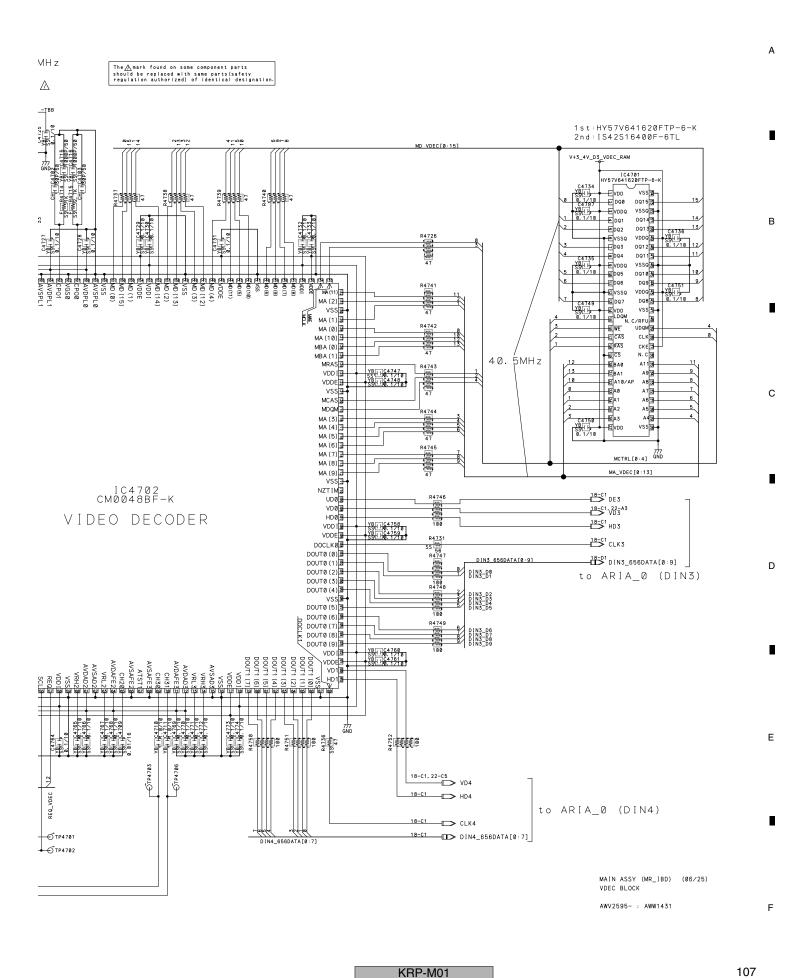
KRP-M01 105 В

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8.6 MAIN BLOCK ASSY (6/24) [VDEC BLOCK]



106



V+3_3V_A

V+3_4V_D

V+3_3V_A_ADC

GND E V+3_3V_A_ADC_PLL

C4819 GX 1819 DCH1201-

V+3_4V_D_ADC

V+3_3V_A_ADC_PLL

I C4801 AD9985KSTZ-110

AD CONVERTI

V+3_3V_A_ADC

C4805 SS^{VI} IP 0. 1/10

C4807 SSUII 0. 1/10

C4809 SSUIIZ 0. 1/10

图dv 图dno 图nioos 图niao

O TP4881 O TP4883

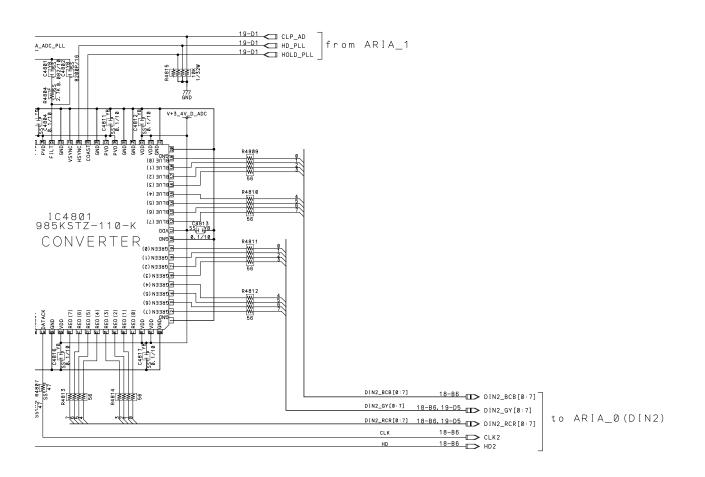
from EMMA2 SCL_AV 12-A2, 22-C6

from RGBSW

L4801 BTX1042- -T

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MODEL

I TEM

R 4803-4815
C 4801-4820 VACANT C 4801-4820 IC 4801 L 4801, 4802

MAIN ASSY (MR_IBD) (07/25)

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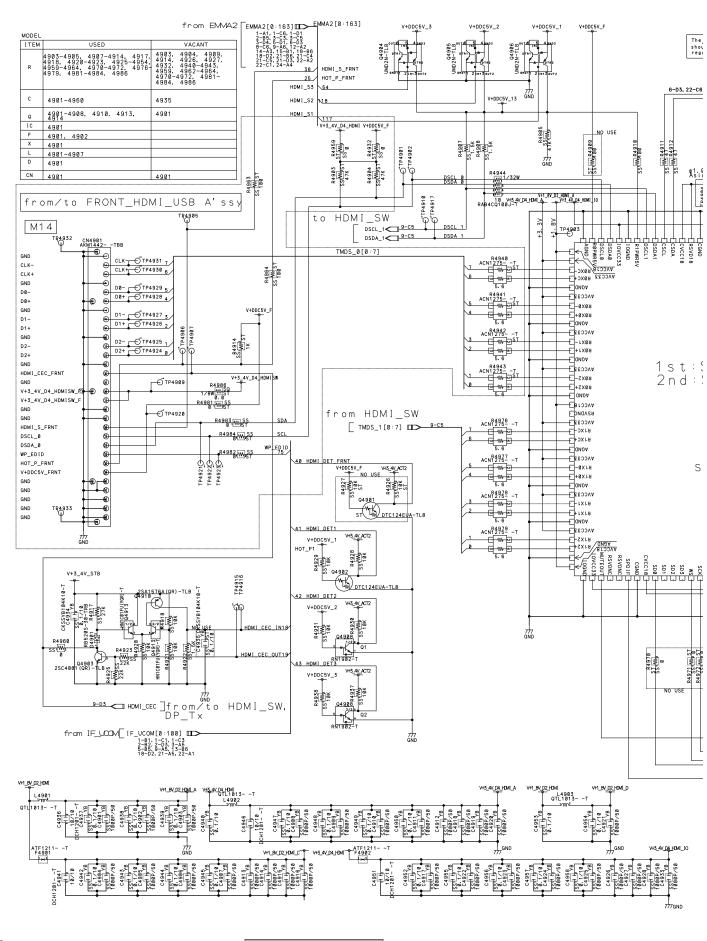
Е

109

ADC BLOCK

AWV2595- ; AWW1431

8.8 MAIN BLOCK ASSY (8/24) [HDMI_RX BLOCK]

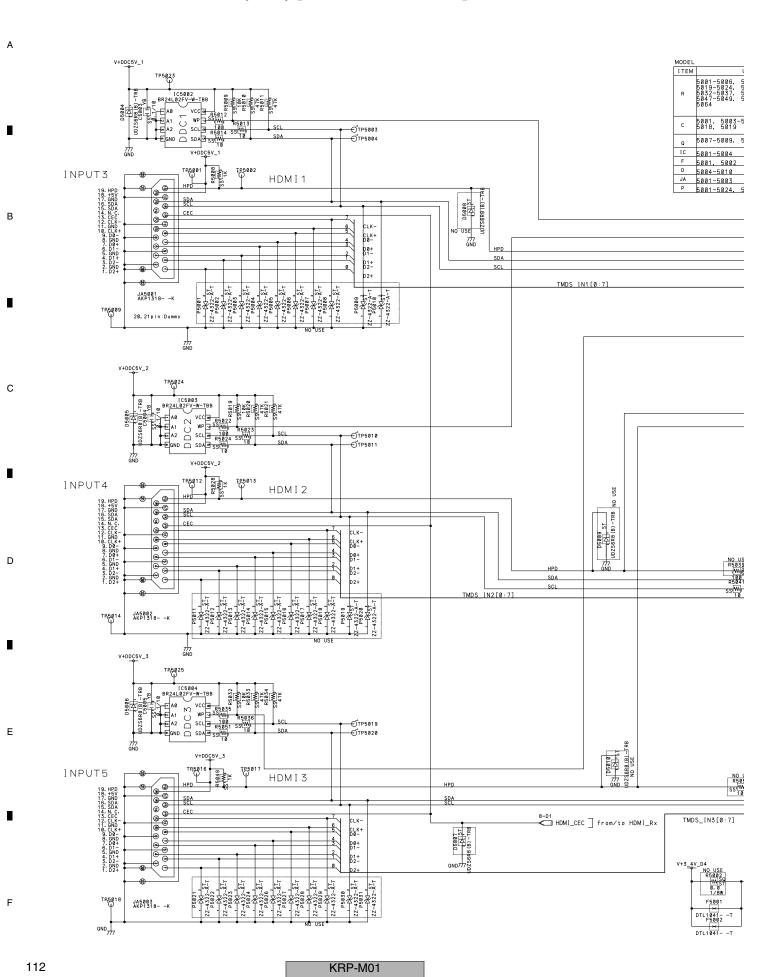


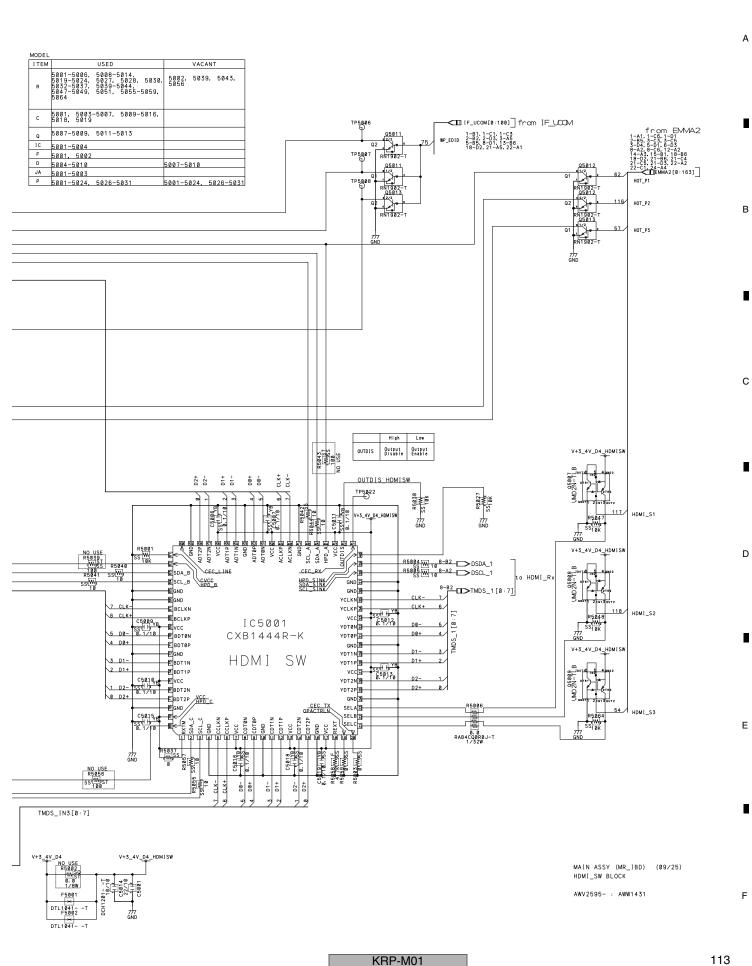
110

The ∕mark found on some component parts should be replaced with same parts(safety regulation authorized) of identical designation. 6-D3, 22-C6 SCL_MB from EMMA2 RAB4CQ680J-T W4 W4 W4 1 / 32W 68 R4913 SS W4 33 18-05 XE V+1_8V_D2_HDMI_C ∮1.0 Aside TP4984 to ARIA_0(DIN6) BA2_PLK[0:11] сеив GA2_PLK [0:11] 5101 RA2_PLK[0:11] *10 FI OACC 22 910 HDMI RX 810 1 s t : S I I 9135ACTU-K 2 n d : S I I 9135CTU-K DOVO[Сеир 120 18 F052 TP4911 φ Посив RAB4C0880J-T R4951 | Wi | Wi | Wi | Wi | RAB4C@880J-T ₽20<u>□</u> 7/7 GND 4025 IC4901 SII9135ACTU-K 920 420F #ICVCC18 Сеир 1028 RAB4C0880J-T 020 RAB4C \$\frac{0}{8}\text{BB} \text{BB} \text{J} \text{T} \ HI OACC 22 Посир 7025 XTALVC 1-A1, 1-C6, 1-D1 2-B5, 3-C3, 3-C5 3-D4, 5-D1, 6-D3 8-A2, 9-A6, 12-A2 14-A3, 15-B1, 18-B6 18-D2, 21-B6, 21-C4 21-C5, 21-D3, 22-A2 22-C1, 24-A4 <u>5</u>0' 22-C1,24-A4 TI EMMA2[0:163] | from EMMA2 TR4908 HDMI_INT TR4918 ASS1226- -1 28. 322MHZ R4936 RST_ASIC ΓΡ49³⁷ <u>ν+3_4ν_</u>D4_HDMI Ε TR4919 R4962 SS WAST NO USE 12-B1 | 12S_BCLK_HDM1 12-B1 |2S_LRCLK_HDM| to MSP 12-C1 SPDIF_HDMI V+3_4V_D4_HDMI L4905 QTL1013- -T MAIN ASSY (MR_IBD) (08/25) HDMI_RX BLOCK AWV2595- : AWW1431

111

8.9 MAIN BLOCK ASSY (9/24) [HDMI_SW BLOCK]





8.10 MAIN BLOCK ASSY (10/24) [AV_SW BLOCK]

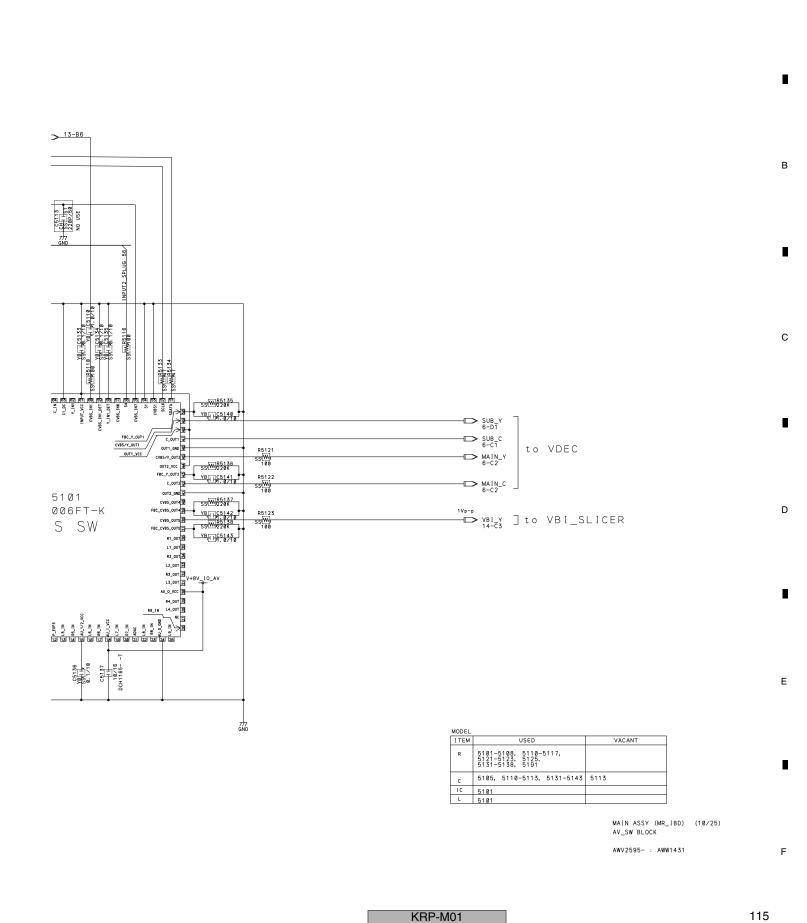
BTX1042- -T V+8V_IO V+8V_IO_AV SDA_AV5 11-D4, 13-D1, 22-B6 SCL_AV5 11-D4, 13-C1, 22-B6 TP5193 from EMMA2 C5113 CH 15 SS 15 22@P/5@ NO USE AV_SW[0:100] INPUT1 SPLUG 73/ INPUT1 V 72/ SIWP 88 V+5V_IO_AV YB| 14.5138 SSIMP100 YBI 11C5139 WI P 071b WI P 071b WI P 1720k SSIMP100 FBC_Y_0 CVBS/Y_OUT YB: 1C5132 BORIVER_VCC E Y_OUTS € c_outs INPUT2_V IC5101 INPUT2_SY SSIW#100 R2S11006FT-K INPUT2_SC CVBS SW ES C_IN4 INPUT3_V SSIW R5105 DCH1201 - TI I C5105 B BG
WIR5106 BY-IN INPUT3_SY INPUT3 SC SSIW#100 © COMP_Y_IN1
COMP_Y_IN2

© S5/P_EXP4

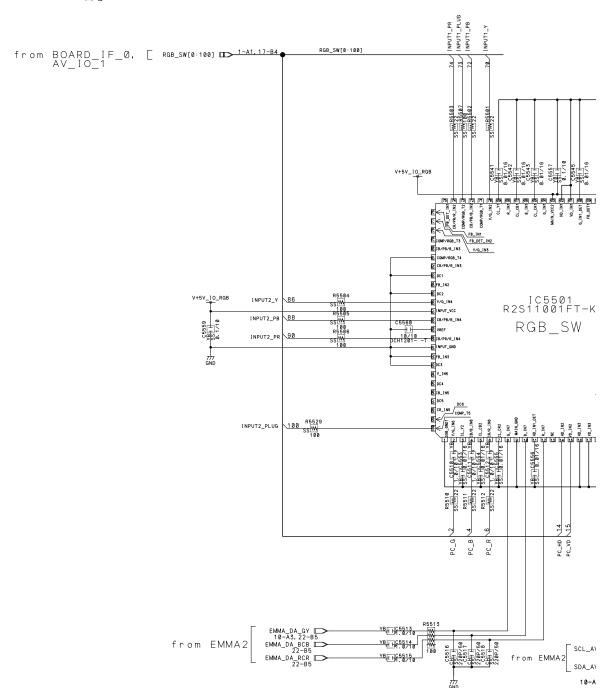
P_EXP1 INPUT3_SPLUG 98 YB: 11C51111 11 FT. 0/10 C5137 10/16 DCH1165-T11-C5 COMP_Y ☐ from RGB_SW

114

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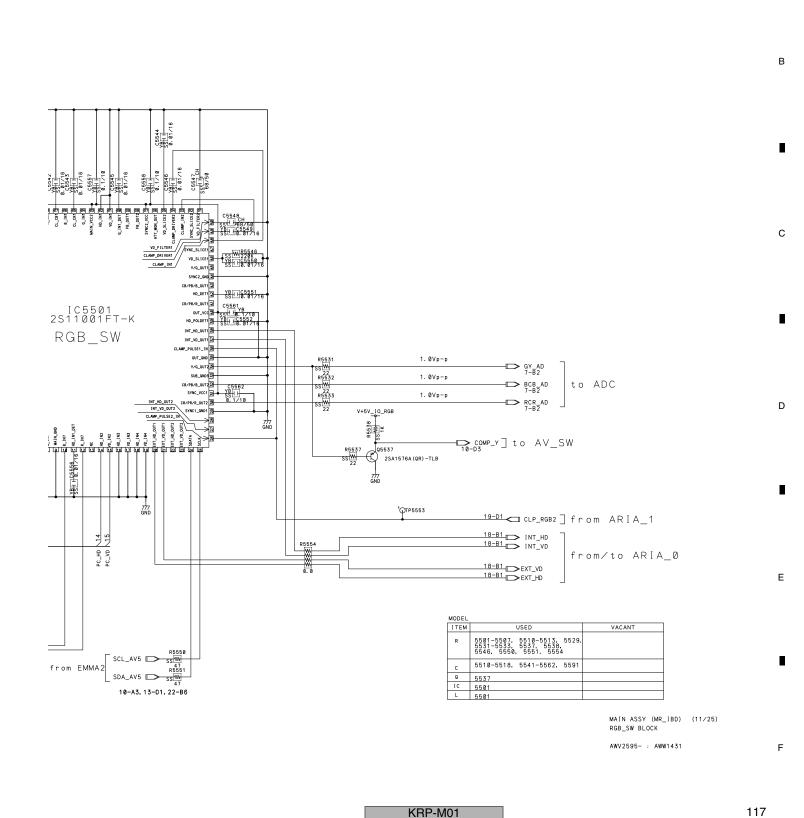
8.11 MAIN BLOCK ASSY (11/24) [RGB_SW BLOCK]



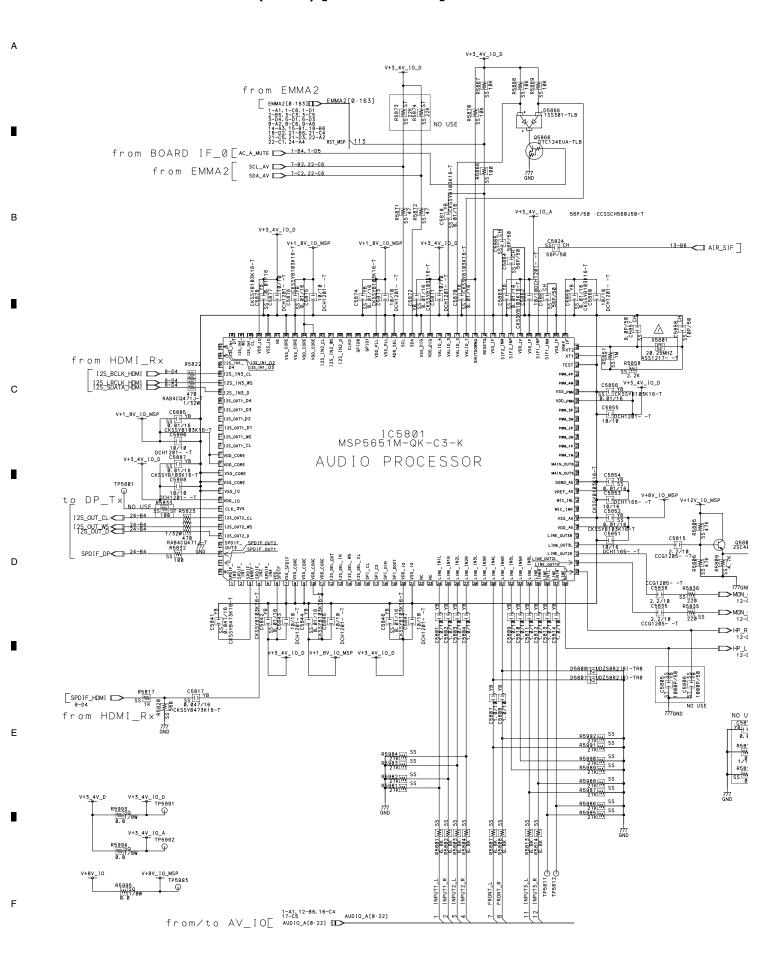
116

KRP-M01

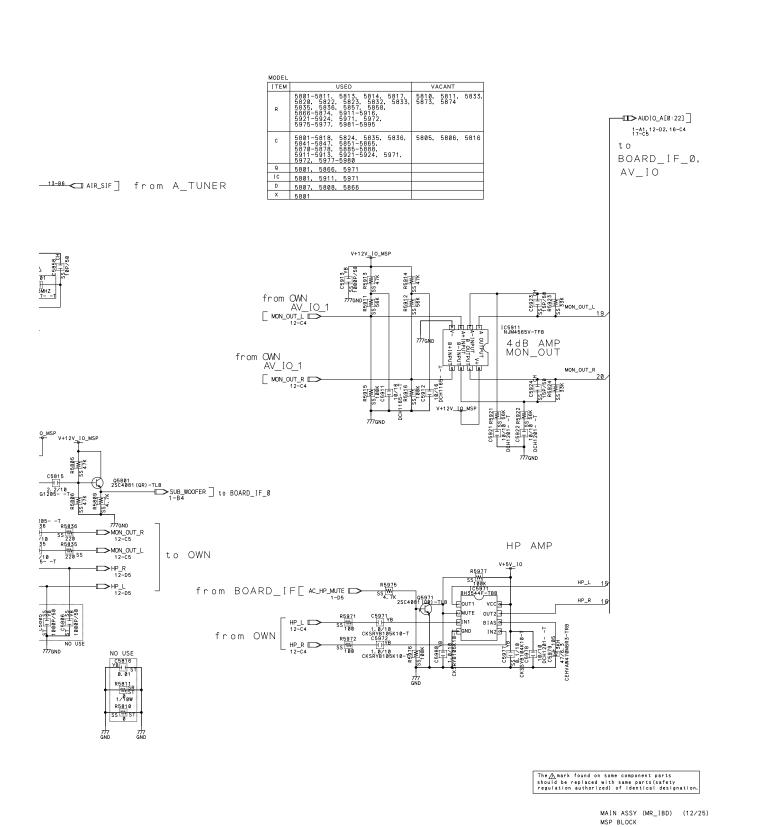
3



8.12 MAIN BLOCK ASSY (12/24) [MSP BLOCK]



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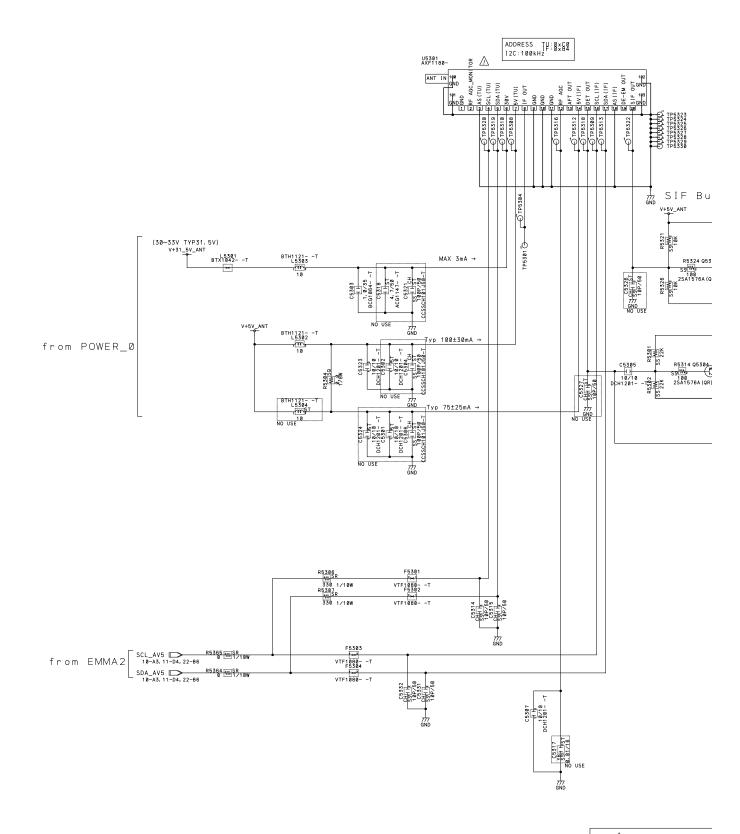
KRP-M01 119

AWV2595- ; AWW1431

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8.13 MAIN BLOCK ASSY (13/24) [A_TUNER BLOCK]



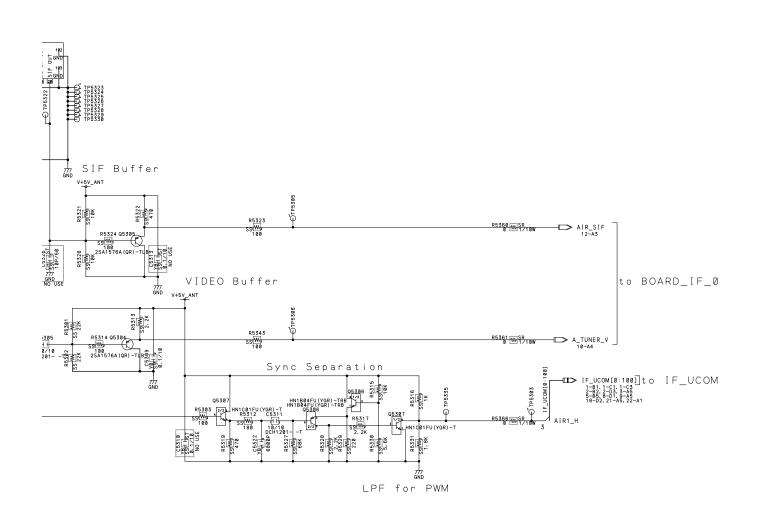
The \bigwedge mark found on some convith same parts (safety reguldesignation.

120

KRP-M01

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MODEL		
ITEM	USED	VACANT
R	5301-5304, 5306, 5307, 5312-5317, 5319, 5321-5324, 5326-5331, 5343, 5360, 5361, 5364-5366	
C	5301-5307, 5309-5311, 5313-5315, 5317, 5318, 5321-5324, 5326, 5327, 5331, 5332	5301, 5302, 5304, 5306, 5310, 5313, 5317, 5318, 5321, 5324, 5326
Q	5304-5307	
F	5301-5304	
L	5301-5304	5304
U	5301	

MAIN ASSY (MR_IBD) (13/25) A_TUNER BLOCK Е

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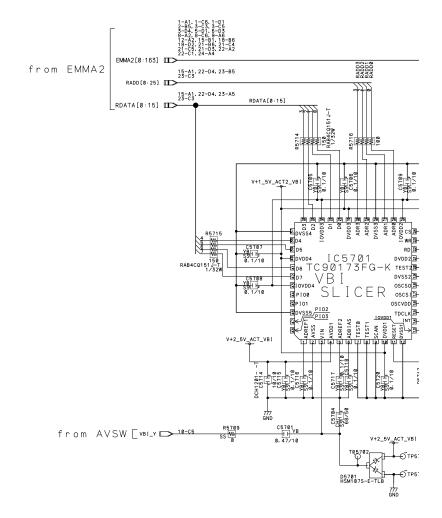
AWV2595- ; AWW1431

mark found on some component parts should be replace me parts (safety regulation authorized) of identical tion

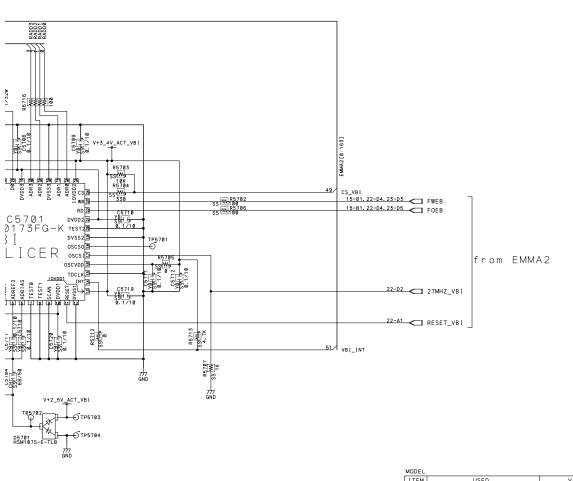
KRP-M01 7 ■

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8.14 MAIN BLOCK ASSY (14/24) [VBI_SLICER BLOCK]



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VACANT I TEM USED 5701-5707, 5709, 5712-5716 5701, 5704-5712, 5714-5720 1 C 5701 D 5701 5701, 5702

MAIN ASSY (MR_IBD) (14/25) VBI_SLICER BLOCK

В

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123

AWV2595- ; AWW1431

8.15 MAIN BLOCK ASSY (15/24) [USB BLOCK]

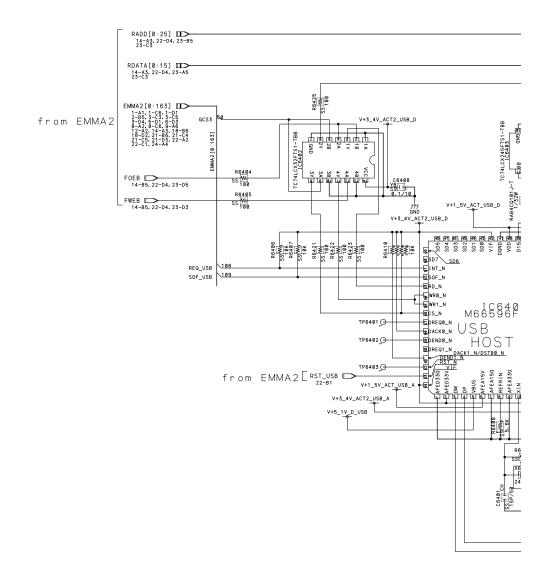
V+5_4V_ACT2

V+5_4V_ACT2_USB

V+3_4V_ACT2_USB_D

Re4830

Re483

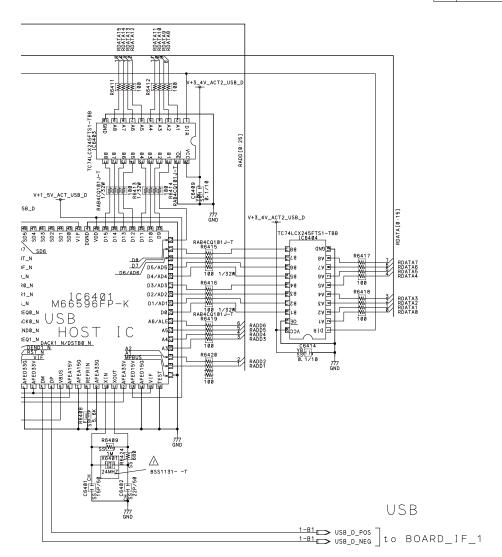


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ITEM	USED	VACANT
R	6401-6425	
С	6401-6414	
10	6401-6404	
L	6401-6403	



MAIN ASSY (MR_IBD) (15/25)

USB BLOCK

AWV2595- : AWW1431

The ∧ mark found on some component parts should be replaced with same parts(safety regulation authorized) of identical designation.

KRP-M01

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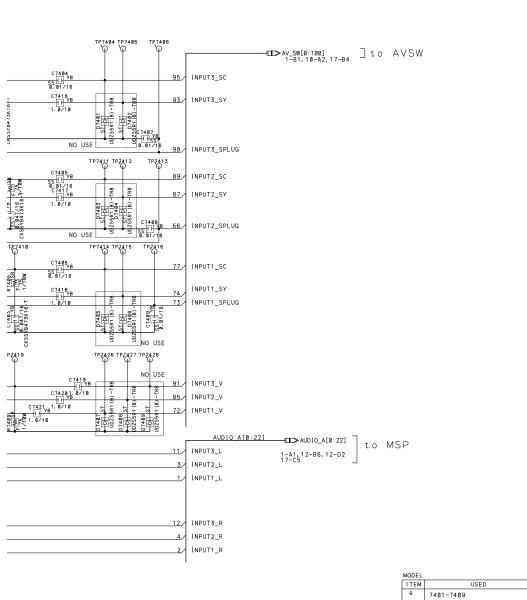
Е

В

TP7402 TP7403 C7404 SS | | YB 0. 01/16 TP7401 C7416_{YB} NO USE JA7402 AKP1280 C7405 SS | | YB 0. 01/16 C7417 | | | YB 1. 0/10 INPUT3 JA7402 AKP1280-NO USE INPUT2 C7406 SS H H YB 0.01/16 C7418 H YB INPUT1 TP7417 TP7418 TP7419 JA7401 AKB1319-3/3 INPUT3_V INPUT2_V INPUT1_V JA7401 AKB1319-2/3 TP7420 TP,7421 INPUT3_L INPUT2_L INPUT1_L JA7401 AKB1319-1/3 TP7423 INPUT3_R INPUT2_R INPUT1_R

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7481-7489

7401-7421 7401-7409

7401, 7402

JA

MAIN ASSY (MR_IBD) (16/25)

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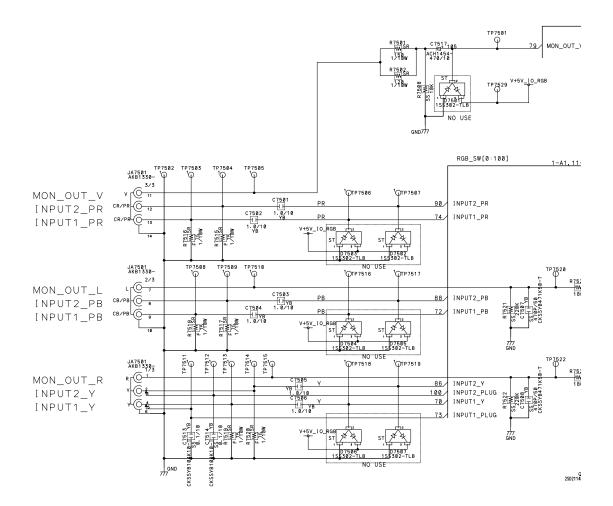
AV_IO_Ø BLOCK

AWV2595- ; AWW1431

KRP-M01 127

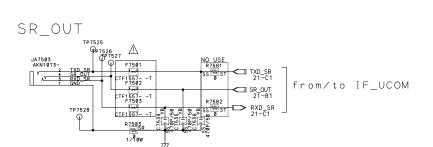
8.17 MAIN BLOCK ASSY (17/24) [AV_IO_1 BLOCK]

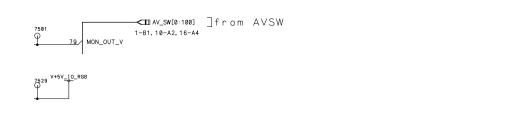
MODEL		
ITEM	USED	VACANT
R	7501-7503, 7508, 7515-7528, 7581, 7582	7581, 7582
С	7501-7508, 7513-7517, 7534-7536	
Q	7502, 7505, 7506	
F	7501-7503	
D	7501-7507	7501-7507
JA	7501, 7503	



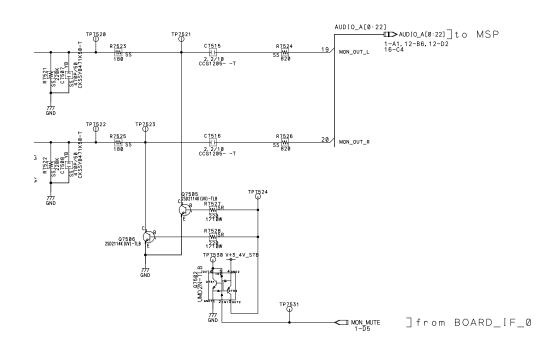
128

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MAIN ASSY (MR_IBD) (17/25) AV_IO_1 BLOCK

В

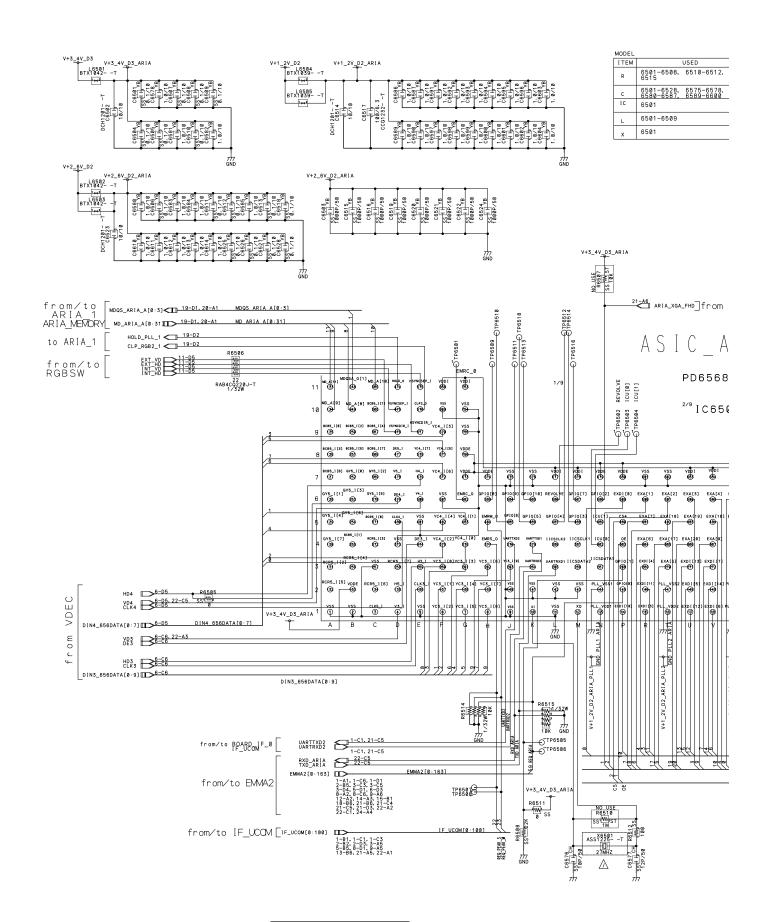
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AWV2595- ; AWW1431

8.18 MAIN BLOCK ASSY (18/24) [ARIA_0 BLOCK]



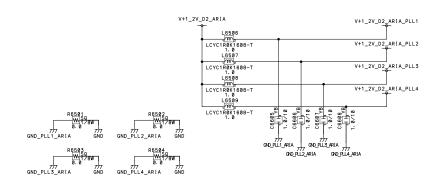
130

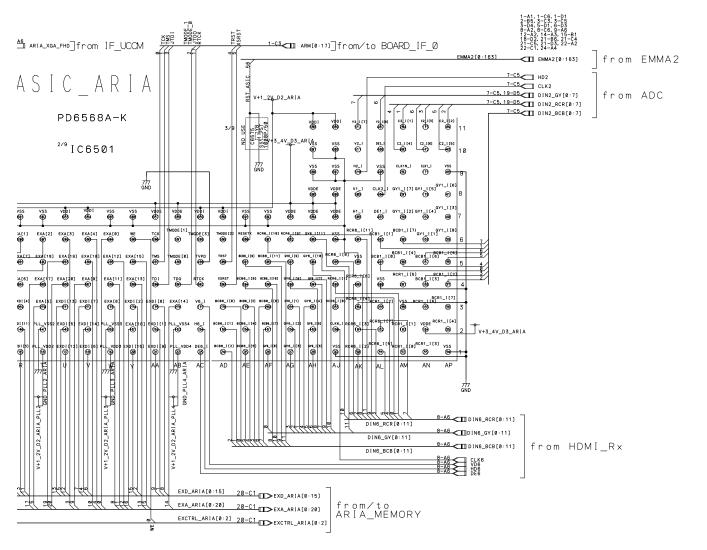
2

KRP-M01



USED	VACANT
3501-6508, 6510-6512, 6514, 3515	6507, 6510
5501-6528, 6575-6578, 5580-6587, 6589-6600	6575
5501	
3501-6509	
3501	





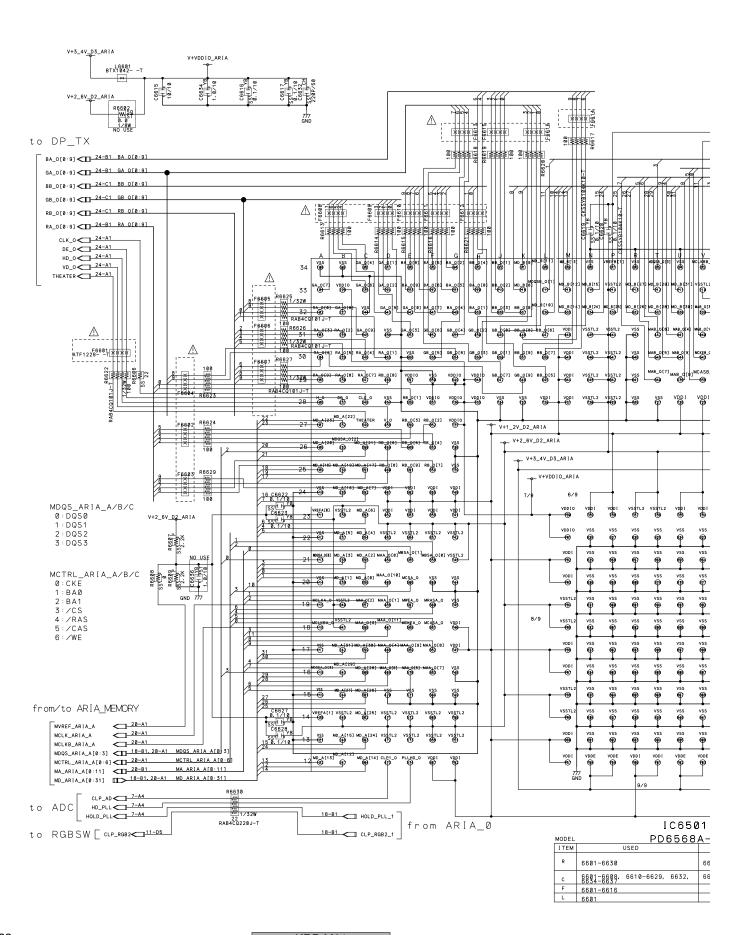
MAIN ASSY (MR_IBD) (18/25) ARIA_0 BLOCK

AWV2595- : AWW1431

12P/50

KRP-M01

8.19 MAIN BLOCK ASSY (19/24) [ARIA_1 BLOCK]



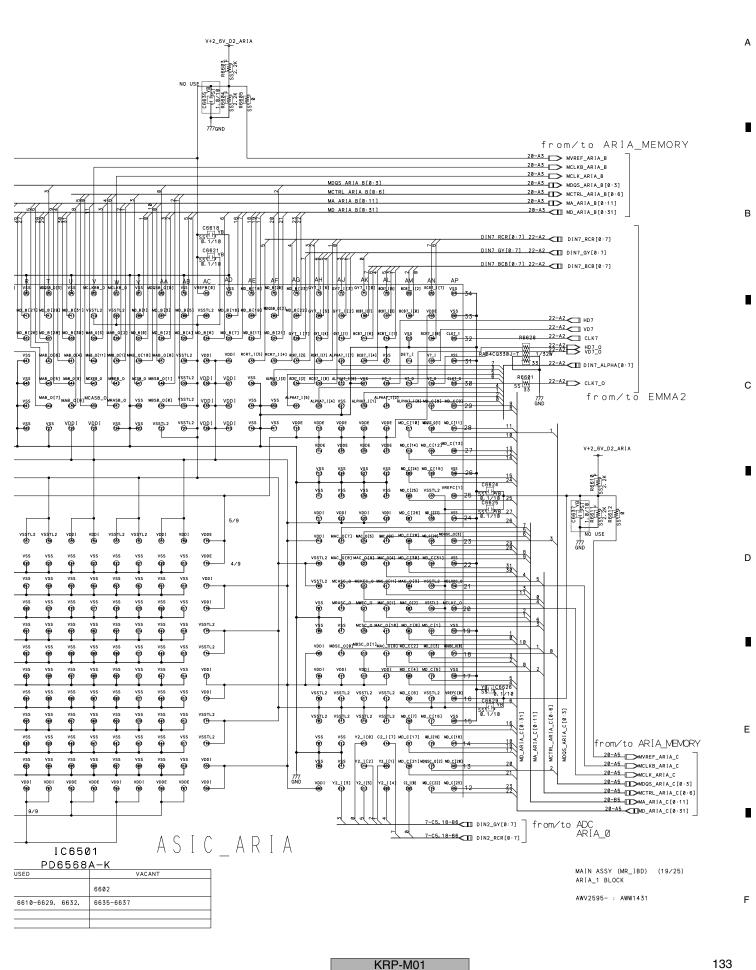
132

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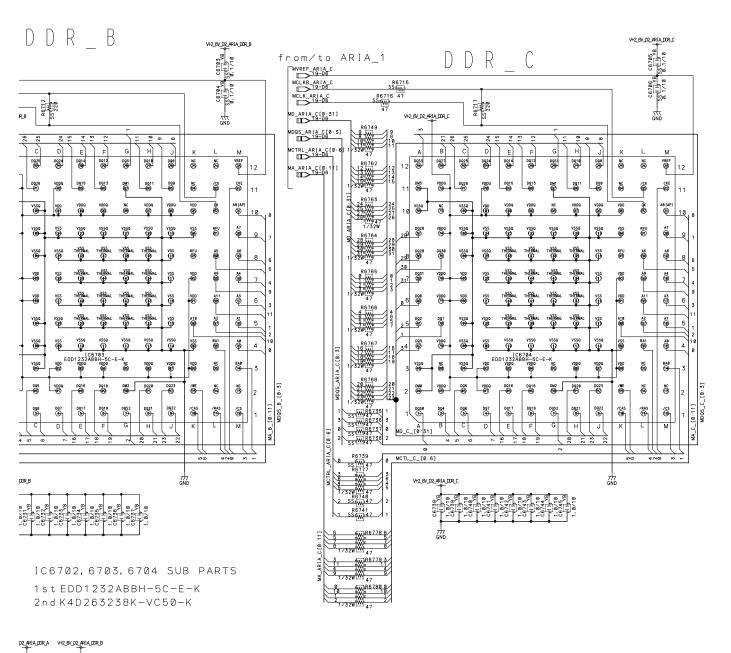


8.20 MAIN BLOCK ASSY (20/24) [ARIA_DDR BLOCK]

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ARIA DDR D D R _ A D D R V+2_6V_D2_ARIA_DDR_A from/to ARIA 1 from/to ARIA_1 MVREF_ARIA_A MCLKB_ARIA_A 19-D1 R6707 SS 220 ICLK_ARIA_A T9-D1 ID_ARIA_A [0:31] ID_18-B1.19-D1 R6711 SSzw.47 SS1M2 220 GND V+2_6V_D2_ARIA_DOR_B DOS_ARIA_A[0:3] 18=81.19-D1 DQS_ARIA_B[0:3] 9 W 47 8 W 1 1 3 W 1 1 7 3 2 W V+2_6V_D2_ARIA_DDR_A :TRL_ARIA_A[0: 471/32 В D c E R6746 н ARIA_A[0:1] В E G J DOSS 0027 (99) ₩ ∰ DQ14 12 W 13 W 15 W 471/32 DOS3 VREF Ø 0027 D025 **8 100** ossi Ø (A) 200 200 NC ⊗ ⊗ ⊗ 12 DM3 99 VDDQ ® DQ26 DQ15 R6756 24 W 4 25 W 27 W 26 W 1/32W R6750 24 W 25 W 27 W 26 W 471/32 DM3 VDDQ 130 DQ26 DQ15 DQ11 009 68 € CKE 11 vsso 1 ® VSSQ (B) vsso. A8 (AP) 10 8 vsso @ R6751 281 W 361 W 315 W 471/32W D928 90 VDDQ 47 @0 0028 9 VDD0 vsso (B) vsso P RFU ∰ DQ29 99 vsso R6758 0 W 4 1 W 2 W 3 W R6752 0 W 1 W 2 W 3 W 471/32V VSSQ vss P RFU 93 **6** 30 _ARIA_B[0:6] MDQS_ARIA_B 89 D03 VDD (B) VDDQ (B) 30 0031 99 VDD (B) **6** VDDQ R6759 4 W 47 6 W 2 5 W 5 7 W 6 900 (49) VDD VDDQ (B) MA_ARIA_B[0:11] R6753 VDDQ VDD (8) vss T ã 6 92 (4) D01 (78) 002 @0 101 18 vsso (P) vss T **Å** 375. 9 W 8 W 19 W 471/32W 26755 R6754 vsso P ₩ ₩ VDD0 VSSQ A8 16 vsso P IC6703 EDD1232ABBH-(VDDQ WDDQ B 003 @ VDDQ (39 vsso ® vss (P) BA1 ∰ NC ® vsso vsso — H-5C-E VDD0 ₩ | C670 | EDD1232ABBH | VDD0 VE | WD00 VE R6761 NC ∰ -K 869 vsso vsso VDDQ 1 vono ® vo: BA8 NC €5 VDDQ O DQ16 DQ1 005 (6) VDD0 DQ16 D028 471/32W DQ18 DN2 DQ23 NC 13 NC Se 0017 (B) 001 (E) 1 R6720 SS W R6729 SS W R6739 SS W R6739 0017 (§) D0222 /CAS /RAS ∕ડ ড ®# (₹) ∞ (3) 007 (1) DQ19 DQS2 1021 (B) 3 WF472 85 WF47 SS 124, 2 186723 SS 18747 MCTL_B_[0:6] MCTL_A_[0:6] 3R6773W-47 3 6/32W-W-5 5 6W-5 5 6W-5 R6769 777 GND V+2_6V_D2_ARIA_DDR_A 2R6738W 47 SS R6725 1R6734W-47 SS#W-R6726 R6774 W 47 :R6770 V+3_4V_D3_AR1A 1/32W R6775 47 EXD_AR[A[0:15] 18-D4 IC6702,670 2R6771 3 1/32W EXA_ARIA[0:20] 18-D4 1 s t EDD123 47 1/32W -PI-K 15 A16 🗟 16 EXCTRL ARIA[0:2]_{18-D4} — A15 A14 BYTE#3 — MA14 BYTE#3 — MA13 VSS 3 2nd K4D263 14 . 13 47 1/32W from ARIA 12 A12 DQ15/A-1 _11 -FA11 DQ7 \$ 10 DQ14 14 ARIA V+2,6V,D2,ARIA,DDR,C V+2,6V,D2,ARIA,DDR,A V+2,6V,D2,ARIA,DDR,B BTX1,04,2--T DQ6 -FIA9 8 -**E** A8 DQ13 13 19 L6702 BTX1042- -T -F A19 DQ5 20 ENC. DQ12 12 R6743 L6703 BTX1042- -T -E WE# DQ4 S 11 10 17 -€A17 9 6 A7 DQ19 6 DQ8 5 **-**88 A5 DQ8 OE -⊠A4 0E# -**№** A3 cs -BiA2 CE#S ARIA FLASH EXA ARIA[0:20] EXCTRL_ARIA[0:2]



CEATUR DCH1281 - T CEATUR CEATUR FINAL CEATUR CE

MODEL		
ITEM	USED	VACANT
R	6705-6707, 6710-6712, 6715-6717, 6720-6741, 6743-6780	6744
С	6701-6708, 6710, 6712, 6714, 6718, 6722-6745	
10	6701-6704	
L	6701-6703	

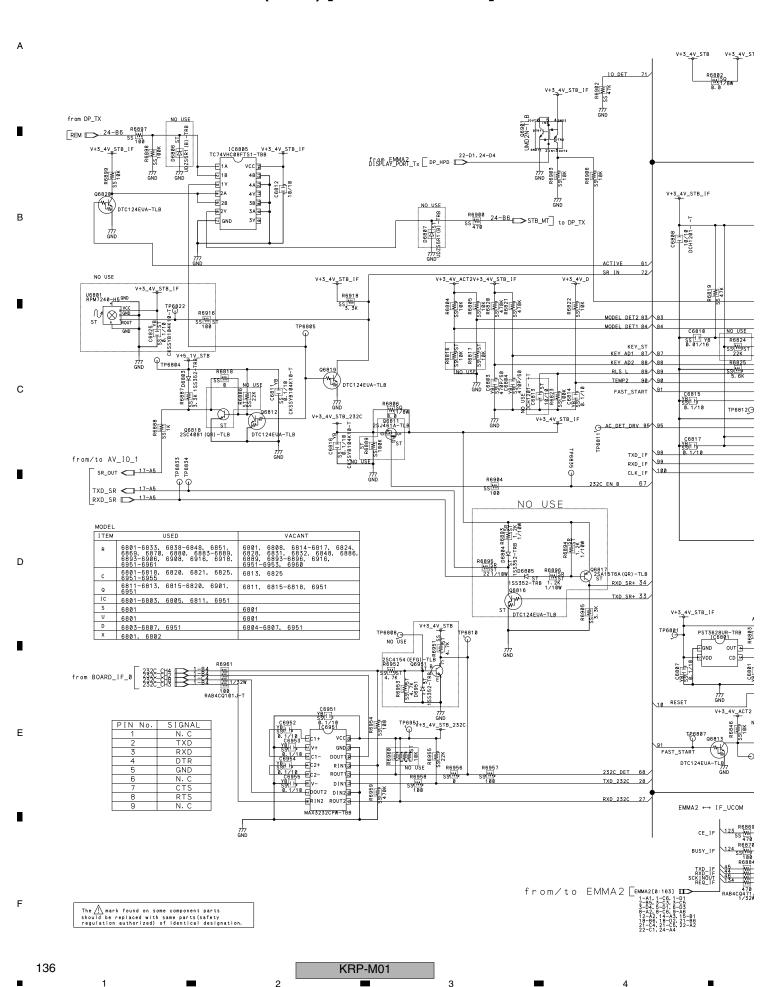
MAIN ASSY (MR_IBD) (20/25) ARIA_MEM BLOCK В

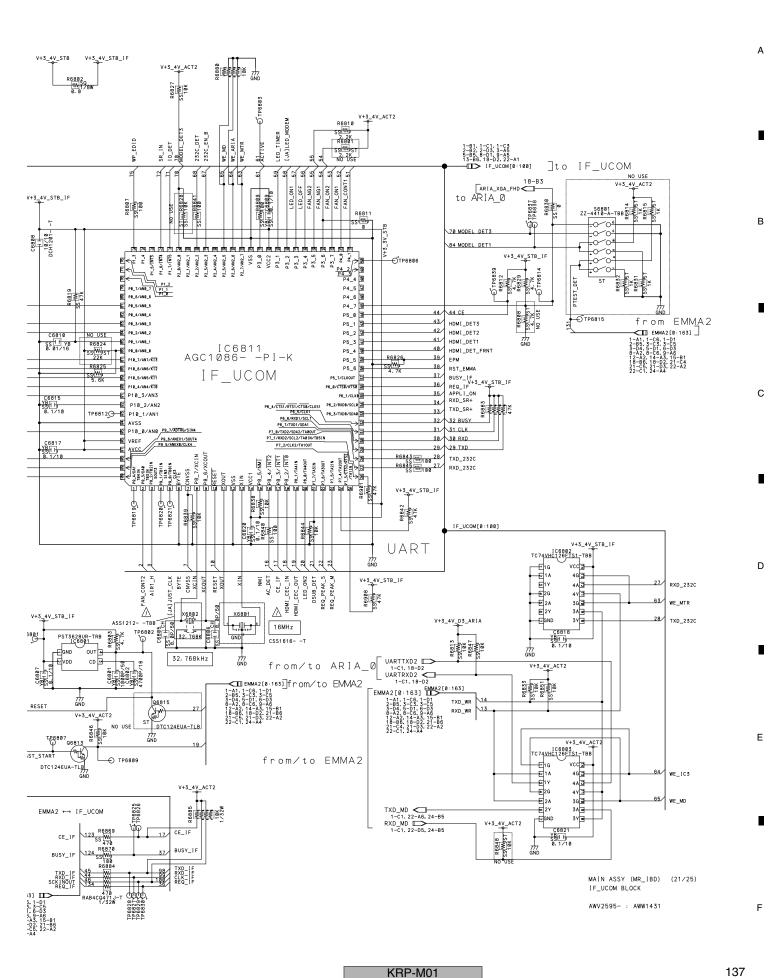
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AWV2595- : AWW1431

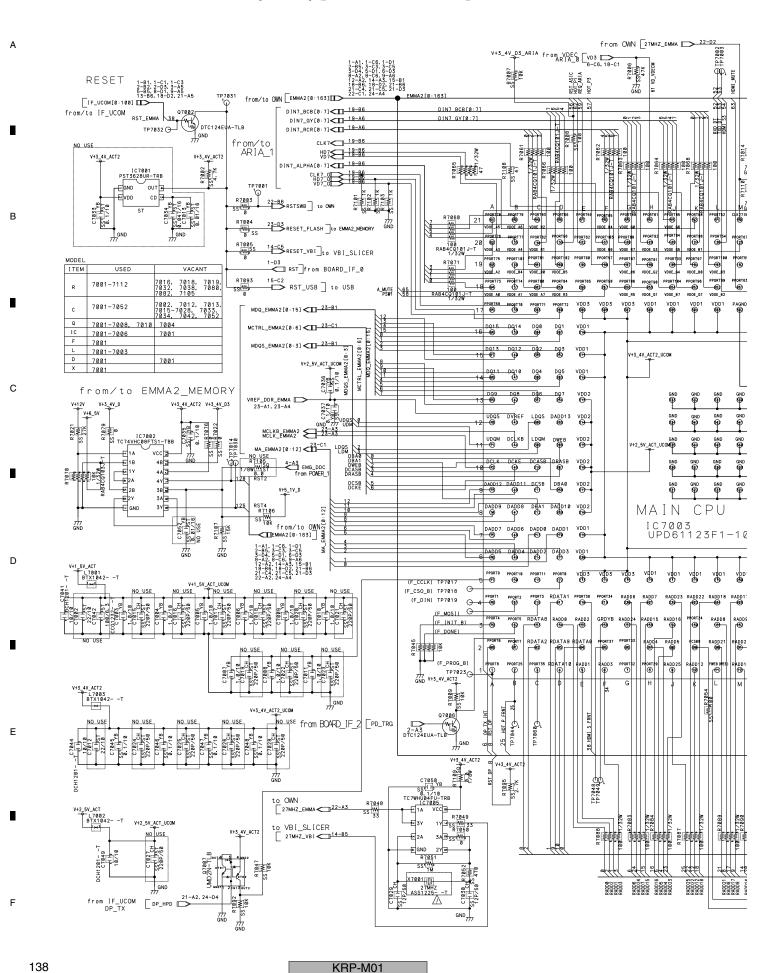
KRP-M01 135

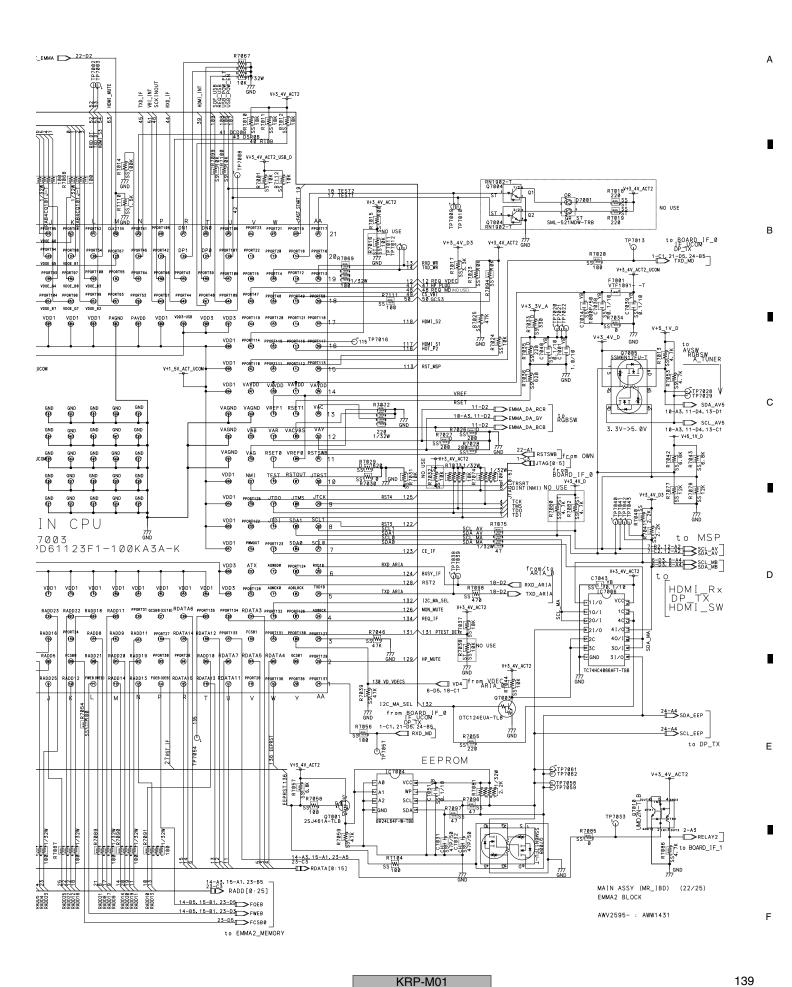
8.21 MAIN BLOCK ASSY (21/24) [IF_UCOM BLOCK]





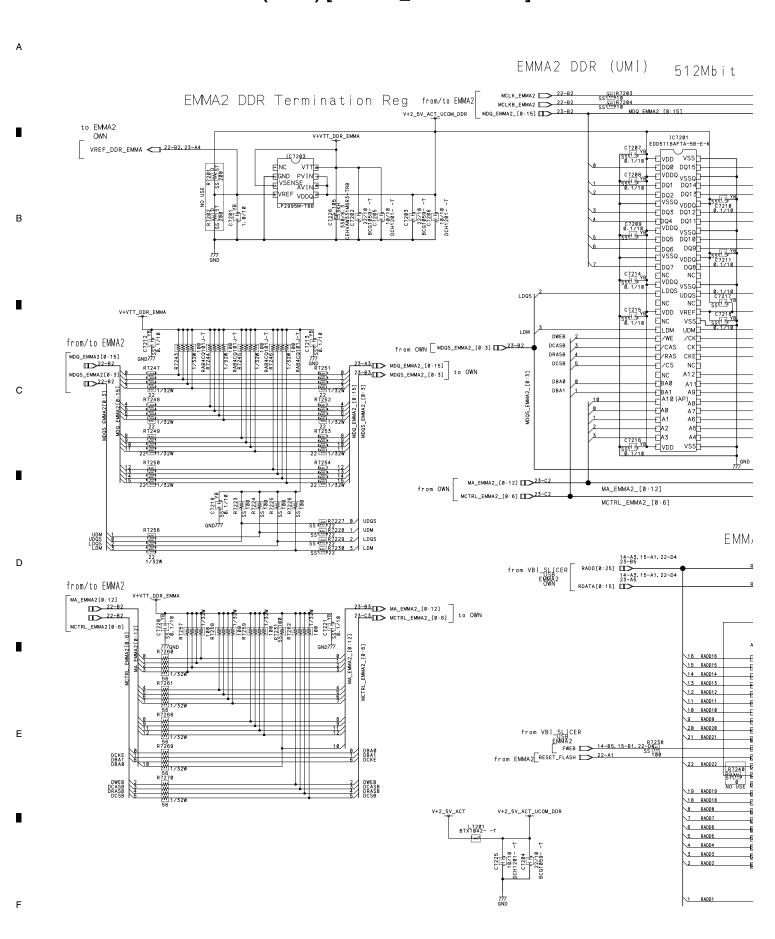
8.22 MAIN BLOCK ASSY (22/24) [EMMA2 BLOCK]



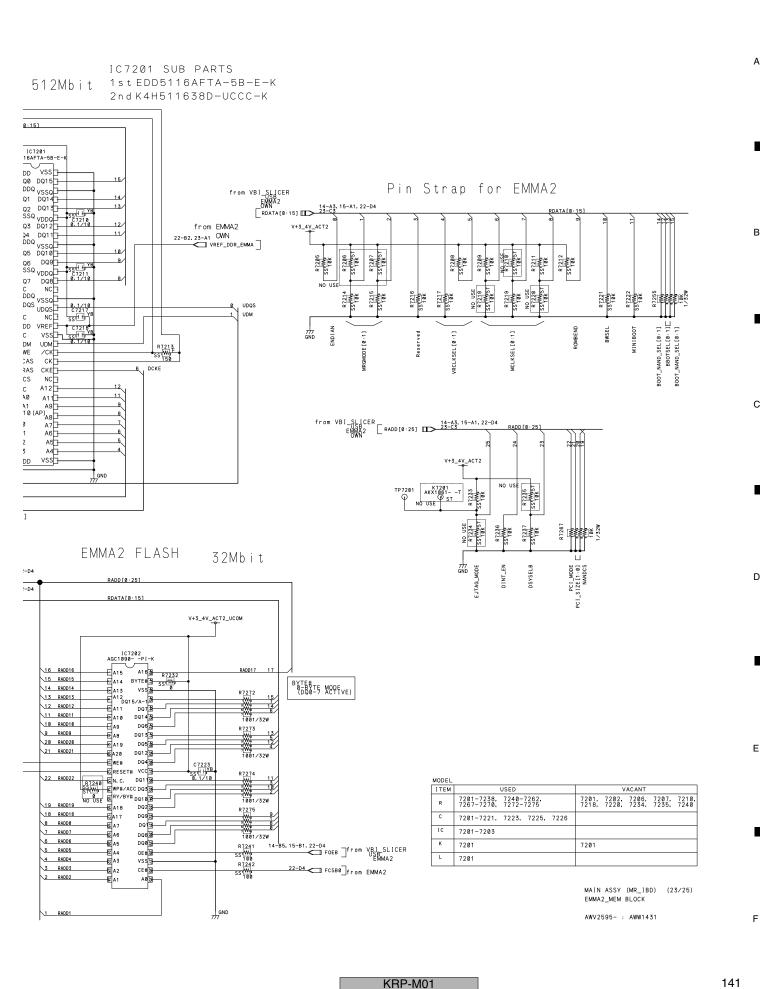


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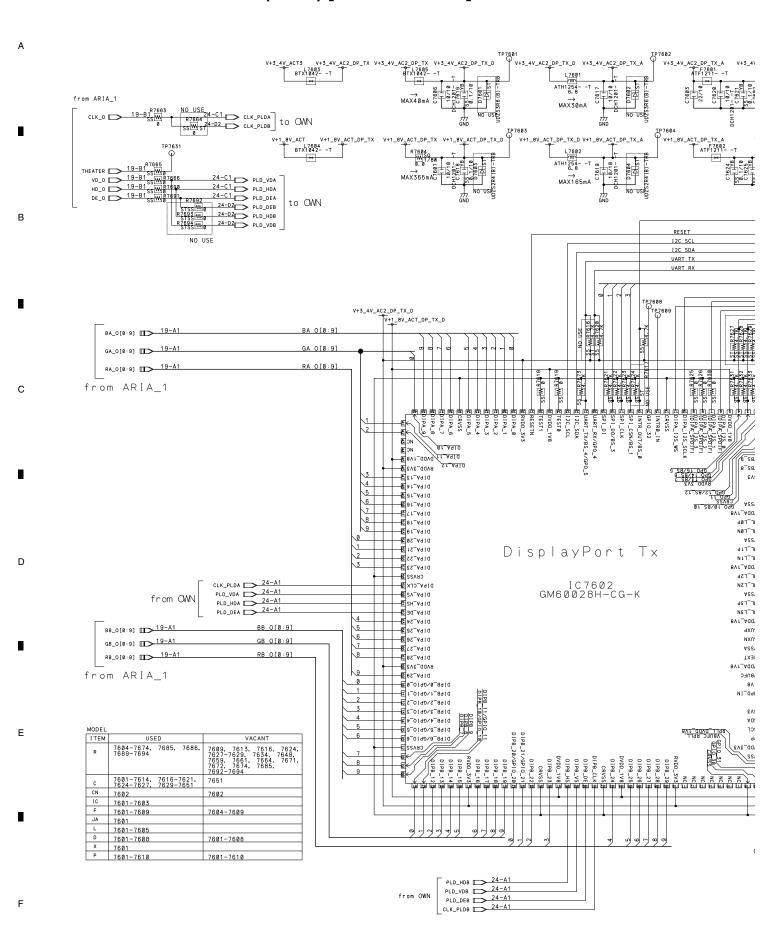
8.23 MAIN BLOCK ASSY (23/24) [EMMA2_MEM BLOCK]



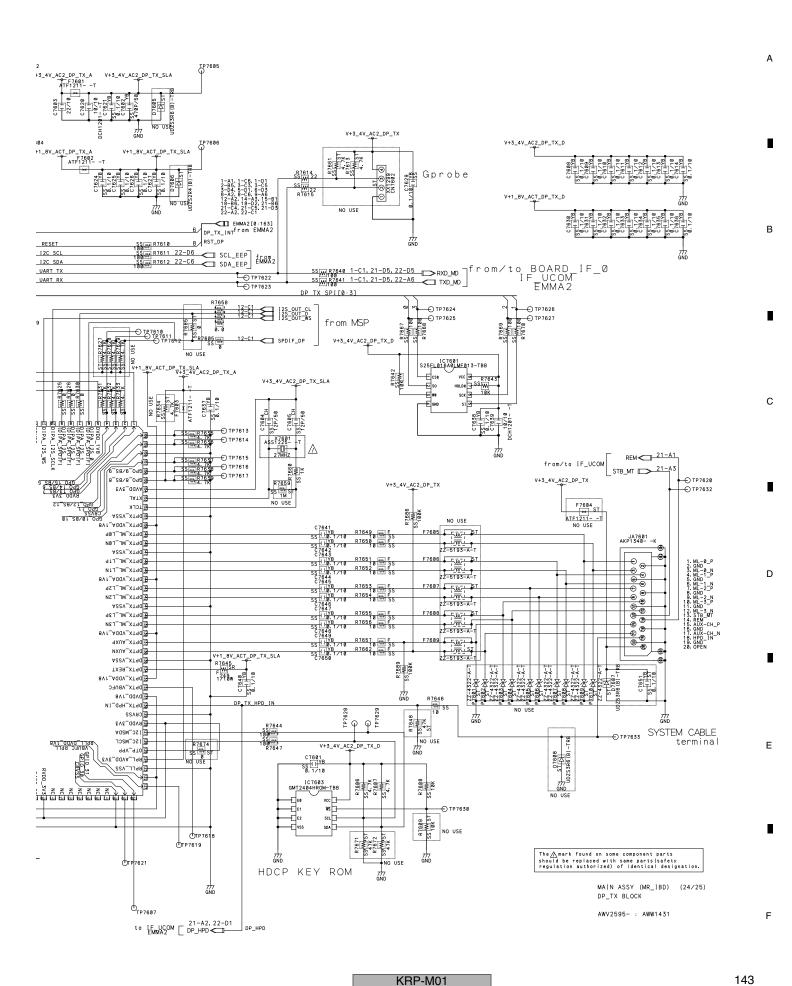
140



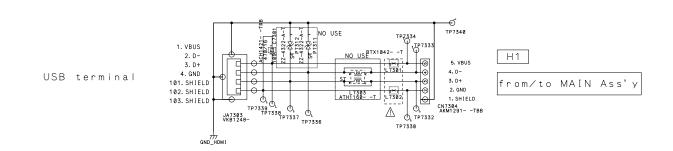
8.24 MAIN BLOCK ASSY (24/24) [DP_TX BLOCK]



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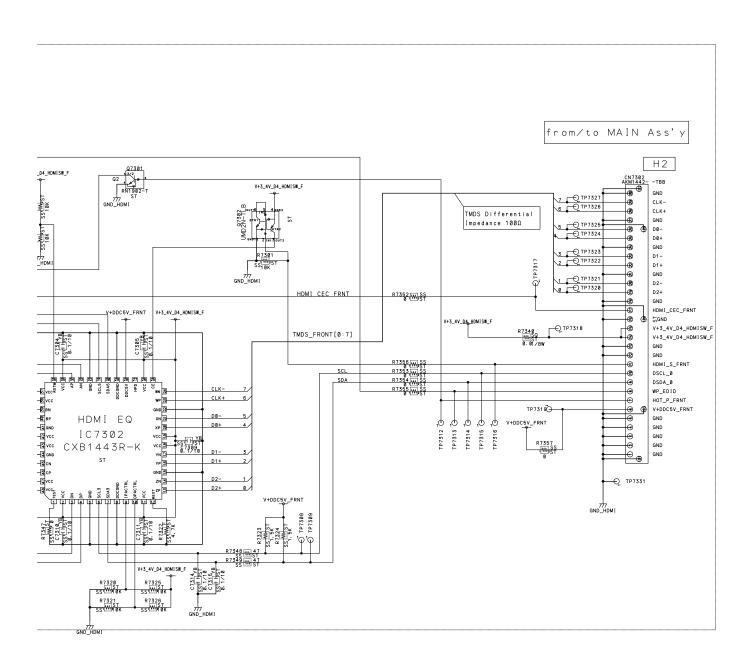
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IN Ass'y

ITEM	USED	VACANT
R	7301-7314, 7320-7326, 7340, 7347-7349, 7352-7357	7301-7314, 7320-7326, 7340, 7347-7349, 7352-7357
С	7301, 7303-7311, 7314, 7315	7303-7311, 7314, 7315
Q	7301, 7302	7301, 7302
10	7301, 7302	7301, 7302
JA	7301, 7303	7301
CN	7302, 7304	7302
L	7301-7303	7303
D	7301	7301
P	7301-7312	7301-7312

MAIN ASSY (MR_IBD) (25/25) FRONT_HDMI_USB BLOCK

AWV2595- : AWW1432 (MR_IBD)

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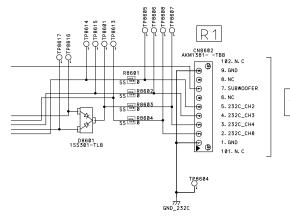
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MAIN_ASS'Y from/to



MODEL		
ITEM	USED	VACANT
R	8601-8606	
с	8604	
Q		
1 C		
S		
JA	8602	
L		
D	8601	
CN	8602, 8603	

EUKUGOU ASSY (MR_IBD) (1/3)

AWW7586-

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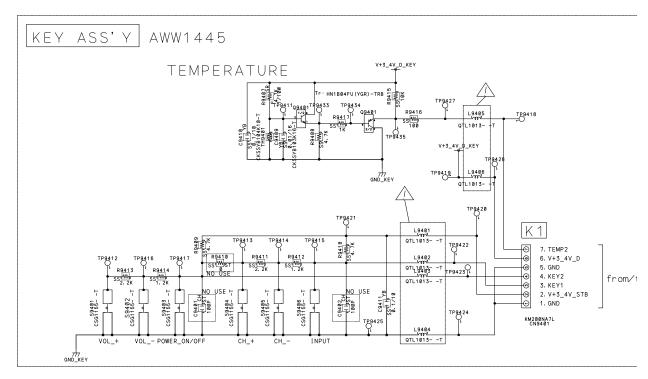
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В

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LED ASS'Y AWW1442 NO USE TP9436 6. LED-R9419 W 65T 1778W TP9426 9407/0211913 - -1 9409/0211913 - -1 9409/0211913 - -1 9419/0211913 - -1 5. LED_MODEM 4. LED_TIMER 3. LED_OFF from MAIN ASS'Y 2. LED_ON 1. LED-TP9432 NO USE L9407 TIMER: EU SLEEP: GC (Orange) (Blue) (Red) gnd_LED_A Chassis GND_LED_B 7/77 GND_LED_A



--- NOTES -CAPACITORS WSR RS1/10SR***J-T SS H YB CKSSYB***K**-T FWSRRS1/10SR***F-T SS RS1/16SS***J-T

The A mark 1 should be rep regulation at

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MAIN ASS'Y FEMP2

/+3_4V_D

3ND

(EY2

(EY1

/+3_4V_STB

3ND from/to MAIN ASS'Y 1L The Amark found on some component parts should be replaced with same parts(safety regulation authorized) of identical designation.

FUKUGQU, ASS'Y (MR_IBD) (2/3)

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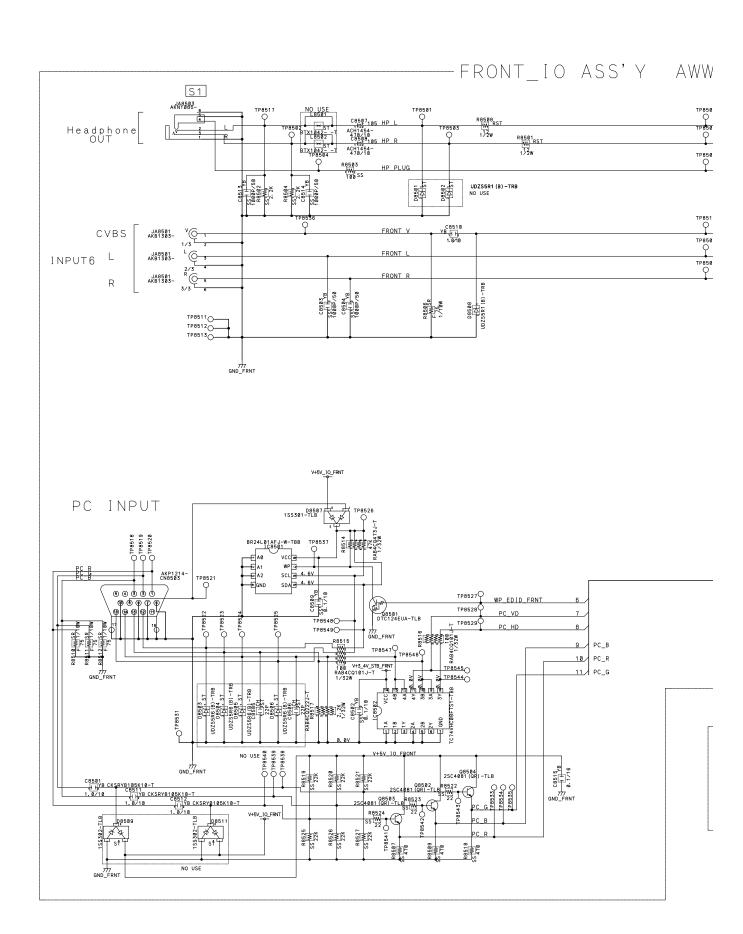
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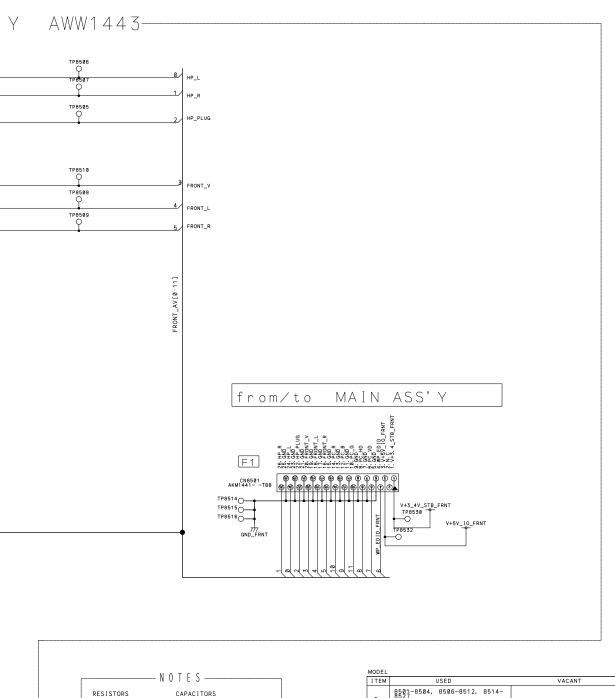
AWV2596-AWW1446



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CAPACITORS SS RS1/16SS***J-T SS H YB CKSSYB***K**-T RST RST1/2SP***J-T HT YB CKSRYB***K**-T F RS1/10SR***F-T RAB4CQ***J-T

MODEL			
ITEM	USED	VACANT	
R	8501-8504, 8506-8512, 8514-		
С	8501-8515	8505, 8506, 8509	
Q	8501-8504		
I C	8501, 8502		
F			
JA	8501, 8503		
L	8501, 8502	8501, 8502	
D	8501-8509, 8511	8501-8506, 8509, 8511	
CN	8501. 8503		

FUKUGOU ASS'Y (MR_IBD) (3/3)

AWW1443

KRP-M01

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8.29 VOLTAGES AND WAVEFORMS [1] VOLTAGES

MAIN BLOCK Assy FRONT_HDM_USB Assy MAIN BLOCK Assy

M13 CN4004 (AKM1276TBB)		Voltage		
NO.	Name		Name	NO.
1	SHIELD	0	SHIELD	1
2	GND	0	GND	2
3	D+	0	D+	3
4	D-	0	D-	4
5	VBUS	5.1	VBUS	5

REAR IO Assy

M16 CN4002 (AKM1378TBB)		Voltage (V)	R1 CN8602 (AKM1381TBB)	
NO.	Name		Name	NO.
1	GND	0	GND	9
2	N.C.	0	N.C.	8
3	SUBWOOFER	0	SUBWOOFER	7
4	N.C.	0	N.C.	6
5	232C_CH2	-5.5	232C_CH2	5
6	232C_CH3	-8.3	232C_CH3	4
7	232C_CH4	0	232C_CH4	3
8	232C_CH8	5.6	232C_CH8	2
9	GND	0	GND	1

В

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MAIN BLOCK Assy

LED Assy

M2 CN4204 (AKW1343TBB)		Voltage (V)	L1 CN9402 (KM200NA6L)	
NO.	Name	(-)	Name	NO.
1	OPEN	0		
2	OPEN	0		
3	TEMP2	2.1		
4	GND	0		
5	KEY1	3.4		
6	GND	0		
7	LED-	0	LED-	6
8	LED_TIMER	3.3/0	LED_TIMER	4
9	LED_ON	2.8/0	LED_ON	2
10	OPEN	0		
11	OPEN	0		
12	LED-	0	LED-	1
13	LED_OFF	3.3/0	LED_OFF	3
14	LED_MODEM(LED-)	0	LED_MODEM	5
15	OPEN	0		
16	V+3_4V_STB	3.4		
17	KEY2	3.4		
18	V+3_4V_D	3.3		
19	OPEN	0		
20	OPEN	0		

MAIN	BLOCK	Assy
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KEY Assy

	M2 CN4204 (AKW1343TBB)	Voltage (V)	K1 CN9401 (KM200NA7L)		
NO.	Name	(*)	Name	NO.	
1	OPEN	0			
2	OPEN	0			
3	TEMP2	2.1	TEMP2	7	
4	GND	0	GND	5	
5	KEY1	3.4	KEY1	3	
6	GND	0	GND	1	
7	LED-	0			
8	LED_TIMER	3.3/0			
9	LED_ON	2.8/0			
10	OPEN	0			
11	OPEN	0			
12	LED-	0			
13	LED_OFF	3.3/0			
14	LED_MODEM(LED-)	0			
15	OPEN	0			
16	V+3_4V_STB	3.4	V+3_4V_STB	2	
17	KEY2	3.4	KEY2	4	
18	V+3_4V_D	3.3	V+3_4V_D	6	
19	OPEN	0			
20	OPEN	0			

MAIN BLOCK Assy

FAN

M31 CN4201 (AKM1276TBB)		Voltage		
NO.	Name	(*)	Name	NO.
1	N.C.	0		
2	FAN_VCC2	7.0/8.9	VCC	П
3	FAN_NEG2	0.1	NG	
4	GND	0	GND	
5	N.C.	0		

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MAIN BLOCK Assy FRONT IO Assy

	M12 CN4003 (AKM1441TBB)	Voltage (V)	F1 CN8501 (AKM1441TBB)	
NO.	Name		Name	NO
1	HP_R	2.1	HP_R	26
2	GND	0	GND	25
3	HP_L	2.1	HP_L	24
4	GND	0	GND	23
5	HP_PLUG	0/3.1	HP_PLUG	22
6	GND	0	GND	21
7	FRONT_V	2.5	FRONT_V	20
8	GND	0	GND	19
9	FRONT_L	-0.2 / 0.2	FRONT_L	18
10	GND	0	GND	17
11	FRONT_R	-0.2 / 0.2	FRONT_R	16
12	GND	0	GND	15
13	PC_R	1.8	PC_R	14
14	GND	0	GND	13
15	PC_B	1.8	PC_B	12
16	GND	0	GND	11
17	PC_G	1.8	PC_G	10
18	GND	0	GND	9
19	PC_HD	0/3.4	PC_HD	8
20	GND	0	GND	7
21	PC_VD	0/3.4	PC_VD	6
22	GND	0	GND	5
23	WP_EDID	0	WP_EDID	4
24	V+5V_IO_FRONT	5.0	V+5V_IO_FRONT	3
25	N.C	0	N.C	2
26	V+3_4V_STB_FRONT	3.4	V+3_4V_STB_FRONT	1

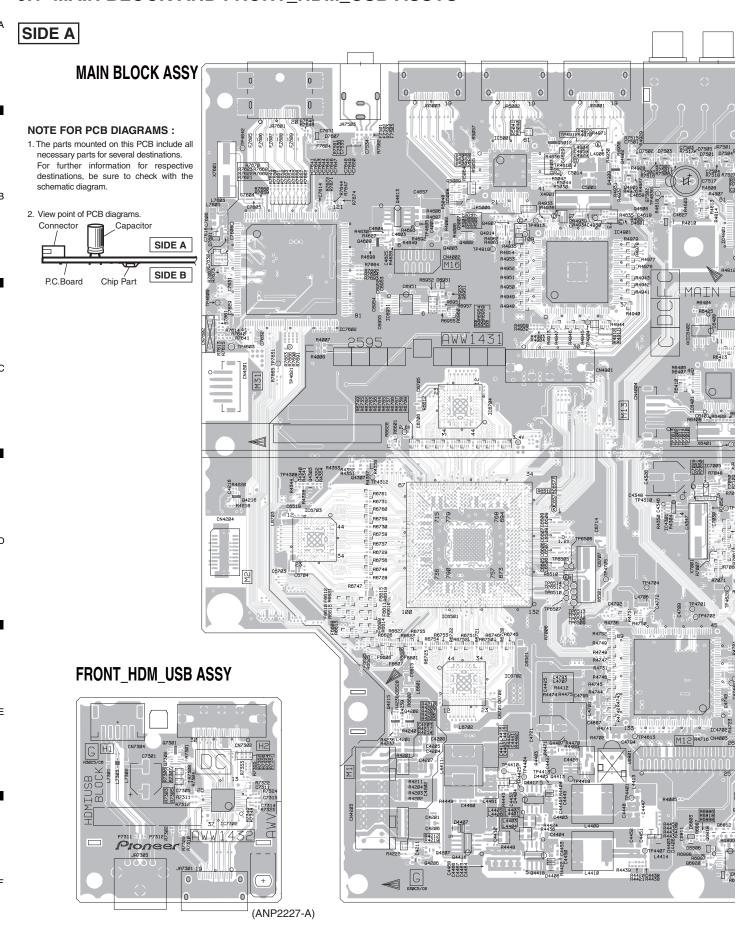
MAIN BLOCK Assy			POWER SUPP	LY L	Jnit
	M1 CN4203 (AKM1440-)	Voltage (V)	P2 (B26B-PNDZ-1	P2 (B26B-PNDZ-1)	
NO.	Name		Name	NO.	
1	V+17V	19.1	V+5_1V_STB	26	4.9
2	V+5_1V_STB	4.9	V+17V	25	19.1
3	GND	0	GND	24	0
4	GND	0	GND	23	0
5	V+12V	13.0	V+12V	22	13.0
6	V+12V	13.0	V+12V	21	13.0
7	GND	0	GND	20	0
8	GND	0	GND	19	0
9	V+6_5V	6.6	V+6_5V	18	6.6
10	V+6_5V	6.6	V+6_5V	17	6.6
11	V+6_5V	6.6	V+6_5V	16	6.6
12	V+6_5V	6.6	V+6_5V	15	6.6
13	GND	0	GND	14	0
14	GND	0	GND	13	0
15	GND	0	GND	12	0
16	GND	0	GND	11	0
17	V+3_4V_STB	3.4	V+3_4V_STB	10	3.4
18	V+3_4V_STB	3.4	V+3_4V_STB	9	3.4
19	V+3_4V_STB	3.4	V+3_4V_STB	8	3.4
20	V+3_4V_STB	3.4	V+3_4V_STB	7	3.4
21	GND	0	V+3_4V_STB	6	3.4
22	V+3_4V_STB	3.4	GND	5	0
23	PD_TRG	0	GND	4	0
24	GND	0	PD_TRG	3	0
25	AC_DET	3.1	RELAY	2	3.1
26	RELAY	3.2	AC_DET	1	3.2

[2] WAVEFORMS

Refer to the section "3.2 DIAGNOSIS FLOWCHART OF FAILURE ANALYSIS" .

KRP-M01

9. PCB CONNECTION DIAGRAM 9.1 MAIN BLOCK AND FRONT_HDM_USB ASSYS



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KRP-M01

000 M51 CN4282 Pioneer ANP2227-A MAIN ASSY 1C6803 1C6802

SIDE A

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В

С

F

(ANP2227-A)

SIDE B

MAIN BLOCK ASSY

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SIDE B

FRONT_HDM_USB ASSY (ANP2227-A) (ANP2227-A)

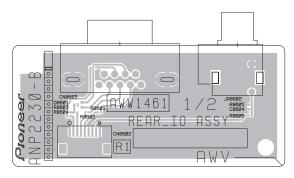
KRP-M01

9.2 REAR IO, FRONT IO, LED AND KEY ASSYS

SIDE A

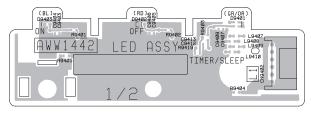
SIDE A

REAR IO ASSY



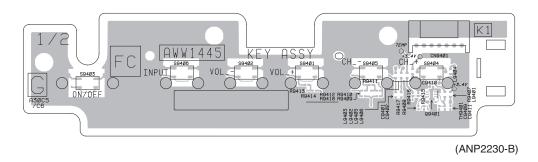
(ANP2230-B)

LED ASSY



(ANP2230-B)

KEY ASSY



FRONT IO ASSY

| Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section | Section |

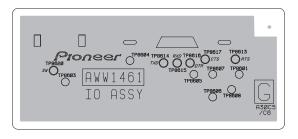
158

KRP-M01

SIDE B

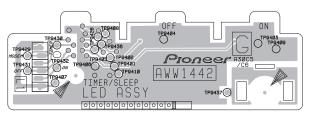
SIDE B

REAR IO ASSY



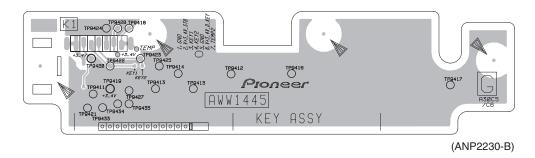
(ANP2230-B)

LED ASSY



(ANP2230-B)

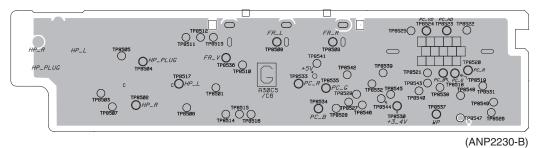
KEY ASSY



FRONT IO ASSY

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10. PCB PARTS LIST

NOTES: • Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.

- The extstyle - When ordering resistors, first convert resistance values into code form as shown in the following examples.

Ex.1 When there are 2 effective digits (any digit apart from 0), such as 560 ohm and 47 k ohm (tolerance is shown by J = 5%, and K = 10%).

Ex.2 When there are 3 effective digits (such as in high precision metal film resistors).

Meaning of the figures and others in the parentheses in the parts list.
 Example) IC 301 is on the point (face A, 91 of x-axis, and 111 of y-axis) of the corresponding PC board.
 IC 301 (A, 91, 111) IC NJM2068V

<u>Mark</u>	No. Description	Part No.
LIS	T OF ASSEMBLIES	
NSP	1MAIN ASSY (GC MR)	AWV2595
	2MAIN BLOCK ASSY (GC MR)	AWW1431
	2FRONT_HDM_USB ASSY (GC MR)	AWW1432
NSP	1FUKUGO ASSY (GC MR)	AWV2596
	2LED ASSY (EU MR)	AWW1442
	2FRONT IO ASSY (EU MR)	AWW1443
	2KEY ASSY (EU MR)	AWW1445
	2REAR IO ASSY (GC MR)	AWW1461
\triangle	1POWER SUPPLY UNIT	AXY1223

Mark No. Description Part No.

Unit Name: MAIN BLOCK ASSY(GC MR) Block Name: BOARD_IF_0 BLOCK(GC)

SEMICONDUCTORS

Q	4001	2SA1576A
Q	4002	UMD2N
Q	4003	2SD2114K
D	4001,4002	1SS352
D	4003-4005	1SS301

MISCELLANEOUS

CN 4001	50P CONNECTOR	AKM1399
CN 4002	FFC CONNECTOR 9P	AKM1378
CN 4003	FFC CONNECTOR 26P	AKM1441
CN 4004	CONNECTOR	AKM1276

RESISTORS

R 4013,4016	RAB4CQ101J
R 4014,4015,4020,4022	RAB4CQ220J
R 4021	RAB4CQ0R0J
R 4023	RAB4CQ220J
Other Resistors	RS1/16SS###J

CAPACITORS

С	4001	DCH1201
С	4003	ACH1421

Block Name: BOARD_IF_1 BLOCK(GC)

SEMICONDUCTORS

Q	4202	RN1902
Q	4206,4207	DTC124EUA
Q	4215	HN1A01FU
Ω	4216	RN2902

MISCELLANEOUS

	L 4201 CHIP BEEDS FILTER	BTX1039
\triangle	L 4205-4207 CHIP BEEDS FILTER	BTX1042
\triangle	F 4201-4205,4207-4209 INDUCTOR	CTF1557
<u> </u>	F 4213-4215 INDUCTOR	CTF1557
	CN 4201 CONNECTOR	AKM1276

RESISTORS

R 4201-4207,4209-4211	RS1/8SQ0R0J
R 4217,4218	RS1/8SQ0R0J
R 4251,4253	RS1/10SR0R0J
R 4252,4254	RS1/10SR102J

ark No. Description	Part No	Mark No. Description	Part No
Ark No. Description Other Resistors	Part No. RS1/16SS###J	Mark No. Description	<u>Part No.</u>
APACITORS	110 17 1000 11 11 10	<u>RESISTORS</u>	
C 4203	CKSSYB102K50	R 4402,4412	RS1/8SQ0R0J
		•	
C 4208	DCH1201	R 4406,4438	RS1/16SS1203D
C 4211	CKSSYB103K16	R 4407,4425-4427,4441	RS1/16SS3302D
C 4216	CKSSYB104K10	R 4421	RS1/16SS5602D
ack Name: DOWER A BLOCK/CO	•	R 4429	RS1/16SS2702D
ock Name: POWER_0 BLOCK(GC	·)	R 4440	RS1/16SS1002D
EMICONDUCTORS		R 4442,4444,4445	RS1/16SS3302D
IC 4301	R5523N001B	Other Resistors	RS1/16SS###J
IC 4302,4309	PQ200WNA1ZPH		
IC 4305,4310	NJM2846DL3-05	<u>CAPACITORS</u>	
IC 4306	BD8903FV	C 4401,4458	CKSRYB104K16
IC 4308	NJM78M12DL1A	C 4403-4406,4409,4410	DCH1165
10 4300	NJIVI7 OIVI 12DL 1A	C 4407,4455,4457	CKSRYB682K50
Q 4303	2SA1576A	C 4411	CCG1232
D 4301,4304,4306,4308	1SS352	C 4414	DCH1201
D 4312	RB521S-40		
D 4316	1SS352	C 4426	CKSSYB104K10
	-	C 4427,4465	CKSRYB105K10
SCELLANEOUS		C 4435	CCSSCH470J50
	DTV4000	C 4436,4439	CKSSYB152K50
L 4301,4302 CHIP BEEDS FILTER	BTX1039		
L 4304 CHIP BEEDS FILTER	BTX1042	C 4437	CCSSCH101J50
L 4305 INDUCTOR	BTH1111		
L 4306 CHIP COIL	BTH1126	C 4438	CCSSCH330J50
	:= :	C 4440	CKSSYB682K25
SISTORS		C 4441	CKSSYB221K50
	D04/0000D0 !	C 4447,4448,4451,4452	BCG1059
R 4304,4305,4308,4316	RS1/8SQ0R0J		
R 4325	RS1/16SS3901F	C 4454	CKSRYB334K10
R 4326	RS1/16SS1003D		B004
R 4327	RS1/16SS2202F	C 4462-4464,4466-4468	BCG1059
R 4340	RS1/16SS2201F		
		Block Name: POWER_2 BLOCK(GC)	
R 4343	RS1/16SS4701F	_	
		<u>SEMICONDUCTORS</u>	
R 4344	RS1/16SS5101F		LT0040751405 0
R 4345	RS1/16SS2701F	IC 4601	LTC3407EMSE-2
R 4346	RS1/16SS1501F	IC 4603,4604,4606	NJM2846DL3-18
Other Resistors	RS1/16SS###J	Q 4601,4618	RSS090P03
		Q 4603,4619	UPA1917TE
APACITORS		Q 4604,4606,4608,4617	2SC4081
C 4301,4305,4308	CKSRYB105K10	,,,	* * *
		Q 4605	DTC124EUA
C 4303	CKSRYB104K25		
C 4304	CKSSYB682K25	Q 4609,4610,4612	RN1902
C 4306,4331	BCG1064	Q 4613	RSS100N03
C 4309,4310	CEHVAW330M25	Q 4614,4615	RTQ045N03
		Q 4616	RTQ040P02
C 4311	ACG1147		1000=-
C 4313,4327	DCH1201	D 4603,4608,4612	1SS352
C 4315,4316,4342,4343	DCH1165	D 4609,4610	RB551V-30
C 4347,4348,4352	CKSSYB104K10		
5 10 11, 10 10, TOOL	ON OF IT I OTHER	MISCELLANEOUS	
		⚠ L 4601 CHIP BEEDS FILTER	BTX1039
	·)		
ock Name: POWER_1 BLOCK(GC		L 4602,4603 CHIP INDUCTOR (2.2 UH)	ATH1244
ock Name: POWER_1 BLOCK(GC			
·		L 4604,4605 CHIP BEEDS FILTER	BTX1042
:MICONDUCTORS	BD8606FV		BTX1042
EMICONDUCTORS IC 4402	BD8606FV N.IM2846DL3-33	L 4604,4605 CHIP BEEDS FILTER	BTX1042
EMICONDUCTORS IC 4402 IC 4404	NJM2846DL3-33	L 4604,4605 CHIP BEEDS FILTER RESISTORS	
EMICONDUCTORS IC 4402 IC 4404 Q 4404-4406	NJM2846DL3-33 DTC124EUA	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607	RS1/8SQ0R0J
EMICONDUCTORS IC 4402 IC 4404 Q 4404-4406 Q 4407,4408	NJM2846DL3-33 DTC124EUA 2SC4081	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607 R 4609,4610,4612-4617	RS1/8SQ0R0J RS1/8SQ0R0J
EMICONDUCTORS IC 4402 IC 4404 Q 4404-4406	NJM2846DL3-33 DTC124EUA	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607 R 4609,4610,4612-4617 R 4649-4652,4655,4683	RS1/8SQ0R0J RS1/8SQ0R0J RS1/8SQ0R0J
EMICONDUCTORS IC 4402 IC 4404 Q 4404-4406 Q 4407,4408	NJM2846DL3-33 DTC124EUA 2SC4081	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607 R 4609,4610,4612-4617 R 4649-4652,4655,4683 R 4659	RS1/8SQ0R0J RS1/8SQ0R0J RS1/8SQ0R0J RS1/16SS1503D
EMICONDUCTORS IC 4402 IC 4404 Q 4404-4406 Q 4407,4408	NJM2846DL3-33 DTC124EUA 2SC4081	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607 R 4609,4610,4612-4617 R 4649-4652,4655,4683	RS1/8SQ0R0J RS1/8SQ0R0J RS1/8SQ0R0J
EMICONDUCTORS IC 4402 IC 4404 Q 4404-4406 Q 4407,4408 Q 4409,4410	NJM2846DL3-33 DTC124EUA 2SC4081 DTA124EUA	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607 R 4609,4610,4612-4617 R 4649-4652,4655,4683 R 4659	RS1/8SQ0R0J RS1/8SQ0R0J RS1/8SQ0R0J RS1/16SS1503D
EMICONDUCTORS IC 4402 IC 4404 Q 4404-4406 Q 4407,4408 Q 4409,4410 Q 4413 Q 4416-4418	NJM2846DL3-33 DTC124EUA 2SC4081 DTA124EUA UPA1917TE SP8M4	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607 R 4609,4610,4612-4617 R 4649-4652,4655,4683 R 4659	RS1/8SQ0R0J RS1/8SQ0R0J RS1/8SQ0R0J RS1/16SS1503D RS1/16SS1003D
EMICONDUCTORS IC 4402 IC 4404 Q 4404-4406 Q 4407,4408 Q 4409,4410 Q 4413 Q 4416-4418 D 4402	NJM2846DL3-33 DTC124EUA 2SC4081 DTA124EUA UPA1917TE SP8M4 1SS352	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607 R 4609,4610,4612-4617 R 4649-4652,4655,4683 R 4659 R 4663 R 4666	RS1/8SQ0R0J RS1/8SQ0R0J RS1/8SQ0R0J RS1/16SS1503D RS1/16SS1003D
EMICONDUCTORS IC 4402 IC 4404 Q 4404-4406 Q 4407,4408 Q 4409,4410 Q 4413 Q 4416-4418	NJM2846DL3-33 DTC124EUA 2SC4081 DTA124EUA UPA1917TE SP8M4	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607 R 4609,4610,4612-4617 R 4649-4652,4655,4683 R 4659 R 4663 R 4666 R 4667	RS1/8SQ0R0J RS1/8SQ0R0J RS1/8SQ0R0J RS1/16SS1503D RS1/16SS1003D RS1/16SS2003D RS1/16SS6202D
EMICONDUCTORS IC 4402 IC 4404 Q 4404-4406 Q 4407,4408 Q 4409,4410 Q 4413 Q 4416-4418 D 4402 D 4405-4407	NJM2846DL3-33 DTC124EUA 2SC4081 DTA124EUA UPA1917TE SP8M4 1SS352	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607 R 4609,4610,4612-4617 R 4649-4652,4655,4683 R 4659 R 4663 R 4666 R 4667 R 4687,4688	RS1/8SQ0R0J RS1/8SQ0R0J RS1/8SQ0R0J RS1/16SS1503D RS1/16SS1003D RS1/16SS2003D RS1/16SS6202D RS1/8SQ0R0J
EMICONDUCTORS IC 4402 IC 4404 Q 4404-4406 Q 4407,4408 Q 4409,4410 Q 4413 Q 4416-4418 D 4402 D 4405-4407	NJM2846DL3-33 DTC124EUA 2SC4081 DTA124EUA UPA1917TE SP8M4 1SS352	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607 R 4609,4610,4612-4617 R 4649-4652,4655,4683 R 4659 R 4663 R 4666 R 4667	RS1/8SQ0R0J RS1/8SQ0R0J RS1/8SQ0R0J RS1/16SS1503D RS1/16SS1003D RS1/16SS2003D RS1/16SS6202D
EMICONDUCTORS IC 4402 IC 4404 Q 4404-4406 Q 4407,4408 Q 4409,4410 Q 4413 Q 4416-4418 D 4402 D 4405-4407	NJM2846DL3-33 DTC124EUA 2SC4081 DTA124EUA UPA1917TE SP8M4 1SS352	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607 R 4609,4610,4612-4617 R 4649-4652,4655,4683 R 4659 R 4663 R 4666 R 4667 R 4687,4688 Other Resistors	RS1/8SQ0R0J RS1/8SQ0R0J RS1/8SQ0R0J RS1/16SS1503D RS1/16SS1003D RS1/16SS2003D RS1/16SS6202D RS1/8SQ0R0J
IC 4404 Q 4404-4406 Q 4407,4408 Q 4409,4410 Q 4413 Q 4416-4418 D 4402 D 4405-4407 ISCELLANEOUS L 4401-4406 CHIP BEEDS FILTER	NJM2846DL3-33 DTC124EUA 2SC4081 DTA124EUA UPA1917TE SP8M4 1SS352 RB060M-30	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607 R 4609,4610,4612-4617 R 4649-4652,4655,4683 R 4659 R 4663 R 4666 R 4667 R 4687,4688	RS1/8SQ0R0J RS1/8SQ0R0J RS1/8SQ0R0J RS1/16SS1503D RS1/16SS1003D RS1/16SS2003D RS1/16SS6202D RS1/8SQ0R0J
EMICONDUCTORS IC 4402 IC 4404 Q 4404-4406 Q 4407,4408 Q 4409,4410 Q 4413 Q 4416-4418 D 4402 D 4405-4407 ISCELLANEOUS	NJM2846DL3-33 DTC124EUA 2SC4081 DTA124EUA UPA1917TE SP8M4 1SS352 RB060M-30	L 4604,4605 CHIP BEEDS FILTER RESISTORS R 4601,4604,4606,4607 R 4609,4610,4612-4617 R 4649-4652,4655,4683 R 4659 R 4663 R 4666 R 4667 R 4687,4688 Other Resistors	RS1/8SQ0R0J RS1/8SQ0R0J RS1/8SQ0R0J RS1/16SS1503D RS1/16SS1003D RS1/16SS2003D RS1/16SS6202D RS1/8SQ0R0J

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	Mark No.	Description	Part No.	Mark	No.	<u>Description</u>	Part No.
	C 4606,4		CKSRYB105K10		C 4802		CKSSYB822K16
	C 4610,4		BCG1059			305,4807,4809	CKSSYB104K10
	C 4616,4		CCSSCH470J50		C 4806,48		CKSSYB473K16
Α	C 4618	011	CKSSYB103K16		C 4811-48	•	CKSSYB104K10
	C 4620,4	645 4648	CCSSCH101J50		C 4818-48	320	DCH1201
	,	626,4634,4649	CKSSYB104K10		0 1010 10	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	50111201
		638,4640,4641	DCH1201	Bloc	k Name:	HDMI_RX BLOCK(GC)	1
	C 4656	,, -	DCH1201	2.00			,
	Disak Name	VDEC DI OCK(CO)		<u>SEM</u>	ICONDU	CTORS	a
	DIOCK Marrie:	VDEC BLOCK(GC)			IC 4901		SII9135ACTU
	SEMICONDI	ICTORS			Q 4902		DTC124EUA
	SEMICONDU IC 4701	OCTORS	HY57V641620FTP-6		Q 4903 Q 4904-49	007 4014	2SC4081 UMD2N
	IC 4701		CM0048BF		Q 4908	707,7314	RN1902
_	Q 4701,4	702	2SA1576A		Q 4300		11111302
В	Q 4701,4	702	20410704		Q 4910		2SA1576A
	MISCELLAN	FOUS			Q 4913		HN1C01FU
		CHIP BEEDS FILTER	BTX1042		D 4901		RB520S-30
	L 4702,4		LCYC6R8K2125				
	,	708 CHIP BEEDS FILTER	BTX1042	MISC	ELLANE	ous	
		INDUCTOR	CTF1557			905 CHIP SOLID INDUCTOR	QTL1013
		CRYSTAL (28.63636 MHz)	ASS1214			07 CHIP BEEDS FILTER	BTX1042
		,			F 4901,49	02 CHIP FERRITE BEADS	ATF1211
	RESISTORS			<u> </u>		RYSTAL (28.322 MHz)	ASS1226
	R 4702,4	703	RS1/8SQ0R0J			, ,	
	R 4710,4		RS1/16SS1500F	RES	ISTORS		
	R 4711,4	721	RS1/16SS2201F		R 4944		RAB4CQ100J
С	R 4712,4	722	RS1/16SS1101F		R 4945-49	954	RAB4CQ680J
	R 4713,4	715,4723	RS1/16SS2701F		R 4976-49	979	ACN1275
					Other Resis	tors	RS1/16SS###J
	R 4714		RS1/16SS1001F				
	R 4726,4	737-4745	RAB4CQ470J	CAP	<u>ACITORS</u>	<u>i</u>	
	R 4746-4		RAB4CQ101J			928,4932,4933	CKSSYB102K50
	Other Resi	stors	RS1/16SS###J		C 4929		CKSSYB103K16
_		_			C 4930,49		CCSSCH8R0D50
	CAPACITORS	-			C 4934,49		CKSSYB104K10
		704-4706,4710	CKSRYB105K10		C 4936,49	41,4946,4951	DCH1201
	C 4702,4		CCSRCH300J50		0 1010 10	AF 40.47 4050	01/00//04041/40
		1709,4712,4718	CKSSYB103K16			945,4947-4950	CKSSYB104K10
D	C 4711		CKSRYB105K10		C 4952-49	900	CKSSYB104K10
-	C 4713,4	7 17	CCSSCH330J50	Diag	l. Nama.	HDML CW BLOCK(CC	`
	C 4714,4	710	CCSSCH680J50	BIOC	k ivame:	HDMI_SW BLOCK(GC)
	C 4715,4		CKSSYB102K50	CEM	ICONDI I	OTORC	
	C 4713,4	710	CKSSYB103K16	<u>SEIVI</u>	ICONDU	CIURS	CVD1444D
	C 4721		CEHVAW101M6R3		IC 5001 IC 5002-50	004	CXB1444R BR24L02FV-W
		1736,4738-4774	CKSSYB104K10		Q 5007-50		UMD2N
	0 1722 1	1700, 1700 1771	oncorpio intro		Q 5011-50		RN1902
	C 4737,4	793-4797	DCH1201		D 5004-50		UDZS6R8(B)
	C 4787		CKSSYB104K10		D 000100	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	0D200110(D)
	C 4792		DCH1165	MISC	ELLANE	ous	
						02 CHIP SOLID INDUCTOR	DTL1041
Е	Block Name:	: ADCC BLOCK(GC)				003 HDMI CONNECTOR	AKP1318
_	OF MOON DU	IOTODO					
	SEMICONDU	<u>ICTORS</u>	AD00051/077 110	RES	<u>ISTORS</u>		
	IC 4801		AD9985KSTZ-110		R 5006		RAB4CQ0R0J
		50110			R 5058		RS1/16SS4701F
	MISCELLAN		DTV4040		Other Resis	tors	RS1/16SS###J
	L 4801,4	802 CHIP BEEDS FILTER	BTX1042				
_	DECICEORC			CAP	ACITORS	<u>i</u>	
	RESISTORS		D04/40000704F		C 5001	227 5000 5040	BCG1059
	R 4804	1000	RS1/16SS2701F			007,5009-5013	CKSSYB104K10
	R 4805-4 R 4809-4		RS1/16SS470J RAB4CQ560J		C 5014	16 5010 5010	DCH1201
	R 4815	דוטו	RAB4CQ103J		U 0015,50	16,5018,5019	CKSSYB104K10
F	Other Resi	stors	RS1/8SQ###J	Rioc	k Nama:	AV_SW BLOCK(GC)	
'	Calor 11001	v		BIUC	n ivaille:	AV_SW BLOCK(GC)	
	CAPACITOR:	<u>s</u>		<u>SEM</u>	ICONDU	CTORS	
	C 4801		CKSSYB823K10		IC 5101		R2S11006FT
	162		L/DD A	101			
_		_	KRP-N	VIO I		_	4
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ark <u>No.</u>	<u>Description</u>	Part No.	Mark No. Description	Part No.
			C 5860,5862,5871,5873	DCH1201
IISCELLANE	OUS		C 5863,5870,5872,5874	CKSSYB103K16
	HIP BEEDS FILTER	BTX1042	C 5875,5877,5886,5888	DCH1201
L 3101 01	III BEEDSTIETEN	D1X10 1 2	C 5876,5878,5885,5887	CKSSYB103K16
CICTORS			C 5913	CKSSYB102K50
SISTORS		DC1/0C00D0 I	0 0910	010010102100
R 5191		RS1/8SQ0R0J	C 5921,5922,5978	DCH1201
Other Resist	ors	RS1/16SS###J	C 5923,5924	CCSSCH150J50
			•	
PACITORS			C 5971,5972,5980	CKSRYB105K10
C 5105,513	31	DCH1201	C 5977	CKSSYB104K10
C 5110-51	12,5138-5143	CKSRYB105K10	C 5979	CEHVAW470M6R3
C 5132-513	36 [°]	CKSSYB104K10		
C 5137		DCH1165	Block Name: A_TUNER BLOCK(GO	;)
ock Name: I	RGB_SW BLOCK(GC	'	SEMICONDUCTORS	
JCK INAIIIE. I	nab_sw block(ac	')	Q 5304,5305	2SA1576A
MICONDUC	CTORS		Q 5306	HN1B04FU
IC 5501	<u> </u>	R2S11001FT	Q 5307	HN1C01FU
Q 5537		2SA1576A	MISCELLANEOUS	
				DTV4040
<u>SCELLANE</u>	<u>ous</u>		L 5301 CHIP BEEDS FILTER	BTX1042
L 5501 CH	HIP BEEDS FILTER	BTX1042	L 5302,5303 CHIP COIL	BTH1121
			F 5301-5304 FERRITE CORE	VTF1080
SISTORS			⚠ U 5301 FRONTEND	AXF1180
R 5513		RAB4CQ101J		
			RESISTORS	
R 5554		RAB4CQOROJ	R 5304	RS1/8SQ0R0J
Other Resist	ors	RS1/16SS###J	R 5306,5307	
			•	RS1/10SR331J
PACITORS			R 5360,5361,5364-5366	RS1/10SR0R0J
C 5510-55		CKSRYB105K10	Other Resistors	RS1/16SS###J
C 5516-55		CCSSCH221J50		
	46,5549-5556	CKSSYB103K16	<u>CAPACITORS</u>	
			C 5303	BCG1064
C 5547,554		CCSSCH680J50	C 5305,5307,5311,5323	DCH1201
U 555/-55	59,5561,5562	CKSSYB104K10	C 5309	CKSSYB104K10
C 5560,559	91	DCH1201	C 5314,5315,5331,5332 C 5322	CCSSCH100D50 CKSRYB682K50
ock Name: I	MSP BLOCK(GC)		Block Name: VBI_SLICER BLOCK(
MICONDUC	CTORS		·	 ,
IC 5801		MSP5651M-QK-C3	<u>SEMICONDUCTORS</u>	
IC 5001		NJM4565V	IC 5701	TC90173FG
			D 5701	HSM107S-E
IC 5971	74	BH3544F	2 3.31	
Q 5801,597	/1	2SC4081	MISCELLANEOUS	
Q 5866		DTC124EUA		DTV4040
_			L 5701,5702 CHIP BEEDS FILTER	BTX1042
D 5807,580	08	UDZS8R2(B)		
D 5866		1SS301	<u>RESISTORS</u>	
			R 5701	RS1/8SQ0R0J
CELLANE	ous		R 5714,5715	RAB4CQ151J
	RYSTAL (20.25 MHz)	ASS1217	R 5716	RAB4CQ101J
V 2001 (1	TO OTAL (20.23 IVII72)	Λυυ1Δ1 <i>1</i>	Other Resistors	RS1/16SS###J
SISTORS			O A DA OLTO TO	
R 5822,582		RAB4CQ471J	CAPACITORS	
R 5993-599	95	RS1/8SQ0R0J	C 5701	CKSRYB474K10
Other Resist	ors	RS1/16SS###J	C 5704	CCSSCH680J50
		-	C 5705-5712,5715-5720	CKSSYB104K10
PACITORS		01/05/25/25/2	C 5714	DCH1201
	04,5807-5814	CKSRYB105K10	Block Name: USB BLOCK(CE)	
C 5815,583		CCG1205	Block Name: USB BLOCK(GE)	
		CKSSYB473K16	0=1110011=110=0	
C 5817,584	42,5844,5846	CKSSYB103K16	<u>SEMICONDUCTORS</u>	
C 5817,584	61.5864.5865	CCSSCH560J50	IC 6401	M66596FP
C 5817,584			IC 6402	TC74LCX32FTS1
C 5817,584 C 5818,584	,,		IC 6403,6404	TC74LCX245FTS1
C 5817,584 C 5818,584 C 5824,586		DCH1201		
C 5817,584 C 5818,584 C 5824,586 C 5843,584	45,5847,5855	DCH1201	10 0 100,0 10 1	101 120/12 101 101
C 5817,584 C 5818,584 C 5824,586 C 5843,584 C 5851,585	45,5847,5855 53,5911,5912	DCH1165		107 1207/2 101101
C 5817,584 C 5818,584 C 5824,586 C 5843,584 C 5851,585 C 5852,585	45,5847,5855	DCH1165 CKSSYB103K16	MISCELLANEOUS	
C 5817,584 C 5818,584 C 5824,586 C 5843,584 C 5851,585	45,5847,5855 53,5911,5912	DCH1165		BTX1042 BSS1131

	1 =	2	3	4
	Mark No. Description	Part No.	Mark No. Description	Part No.
Α	RESISTORS R 6401-6403 R 6408 R 6410 R 6411-6420 Other Resistors	RS1/8SQ0R0J RS1/16SS5601F RAB4CQ103J RAB4CQ101J RS1/16SS###J	CAPACITORS C 6501,6504-6513,6518 C 6502,6514,6523 C 6503,6515,6516 C 6517 C 6519-6522,6524	CKSSYB104K10 DCH1201 CKSSYB102K50 CCG1232 CKSSYB102K50
•	CAPACITORS C 6401 C 6402 C 6403-6410,6412,6414 C 6411,6413	CCSSCH150J50 CCSSCH220J50 CKSSYB104K10 DCH1201	C 6525-6528 C 6576 C 6577 C 6578,6580-6587 C 6589-6608,6610-6614	CKSSYB104K10 CCSSCH100D50 CCSSCH120J50 CKSRYB105K10 CKSRYB105K10
	Block_Name: AV_IO_0 BLOCK(GE)		Block Name: ARIA_1 BLOCK(GC)	
В	MISCELLANEOUS JA 7401 9P PIN JACK JA 7402 3P 4PIN MINIDIN (S)	AKB1319 AKP1280	MISCELLANEOUS L 6601 CHIP BEEDS FILTER ⚠ F 6601-6616 FERRITE BEADS ARRAY	BTX1042 ATF1228
	RESISTORS All Resistors CAPACITORS C 7401-7403 C 7404-7409	RS1/10SR####F CKSSYB473K16 CKSSYB103K16	RESISTORS R 6603,6604,6607 R 6609-6611 R 6613-6627,6629 R 6628 R 6630	RS1/16SS2201F RS1/16SS2201F RAB4CQ101J RAB4CQ330J RAB4CQ220J
	C 7410-7415 C 7416-7421	CKSSYB102K50 CKSRYB105K10	Other Resistors	RS1/16SS###J
С	Block Name: AV_IO_1 BLOCK(GC) SEMICONDUCTORS Q 7502 Q 7505,7506	UMD2N 2SD2114K	CAPACITORS C 6615 C 6616-6629 C 6632 C 6634	DCH1201 CKSSYB104K10 CCSSCH221J50 CKSRYB105K10
	MISCELLANEOUS		Block Name: ARIA_DDR BLOCK(GC)	
	⚠ F 7501-7503 INDUCTOR JA 7501 9P PIN JACK JA 7503 MINI JACK (4P)	CTF1557 AKB1330 AKN1073	SEMICONDUCTORS NSP IC 6701	AGC1091 EDD1232ABBH-5C-E
D	RESISTORS R 7501 R 7503 R 7515-7520 R 7527,7528 Other Resistors	RS1/10SR151J RS1/10SR0R0J RS1/10SR75R0F RS1/10SR221J RS1/16SS###J	MISCELLANEOUS L 6701-6703 CHIP BEEDS FILTER RESISTORS R 6745-6780	BTX1042 RAB4CQ470J
		1131/1033###3	Other Resistors	RS1/16SS###J
_	CAPACITORS C 7501-7506 C 7507,7508,7534-7536 C 7513,7514 C 7515,7516 C 7517	CKSRYB105K10 CKSSYB471K50 CKSSYB104K10 CCG1205 ACH1454	CAPACITORS C 6701-6707 C 6708,6710,6712,6714 C 6718 C 6722-6745	CKSSYB104K10 DCH1201 CKSSYB103K16 CKSRYB105K10
E	Block Name: ARIA_0 BLOCK(GC)		Block Name: IF_UCOM BLOCK(GC)	
	SEMICONDUCTORS IC 6501	PD6568A	SEMICONDUCTORS IC 6801 IC 6802,6803	PST3628UR TC74VHC126FTS1
•	MISCELLANEOUS L 6501-6503 CHIP BEEDS FILTER L 6504,6505 CHIP BEEDS FILTER L 6506-6509 INDUCTOR ⚠ X 6501 CRYSTAL (27 MHz)	BTX1042 BTX1039 LCYC1R0K1608 ASS1225	IC 6805 NSP IC 6811 IC 6951 Q 6812,6813,6819,6820	TC74VHC08FTS1 AGC1086 MAX3232CPW DTC124EUA
	· ,	1,001220	Q 6901 D 6803	UMD2N 1SS352
F	RESISTORS R 6501-6504 R 6506 R 6514,6515 Other Resistors	RS1/8SQ0R0J RAB4CQ220J RAB4CQ103J RS1/16SS###J	MISCELLANEOUS ⚠ X 6801 CERAMIC OSCILLATOR ⚠ X 6802 CRYSTAL OSCILLATOR	CSS1616 ASS1212
•	164 1 ■	2 KRP-M01	3	4

rk No. Description	<u>Part No.</u>	Mark No. Description	<u>Part No.</u>
SISTORS	DC1/9C00D0 I	Block Name: EMMA2_MEM BLOCK	(GC)
R 6802,6806	RS1/8SQ0R0J	OFMICONDUCTORS	
R 6880,6885	RAB4CQ103J	<u>SEMICONDUCTORS</u>	
R 6883	RAB4CQ473J	IC 7201	EDD5116AFTA-5B-E
R 6884	RAB4CQ471J	NSP IC 7202	AGC1090
R 6961	RAB4CQ101J	IC 7203	LP2995M
Other Resistors	RS1/16SS###J	MISCELLANEOUS L 7201 CHIP BEEDS FILTER	BTX1042
PACITORS	OVCCVD100VE0		
C 6801	CKSSYB102K50	<u>RESISTORS</u>	
C 6802	CKSSYB472K16	R 7213	RS1/16SS1500F
C 6803,6804	CKSSYB471K50	R 7243-7246,7257-7259	RAB4CQ101J
C 6805,6806	CCSSCH8R0D50	R 7247-7254,7256	RAB4CQ220J
C 6807,6809,6811	CKSSYB104K10	R 7255,7267	RAB4CQ103J
		R 7260,7261,7268-7270	RAB4CQ560J
C 6808,6812	DCH1201	, ,	
C 6810	CKSSYB103K16	R 7262,7272-7275	RAB4CQ101J
C 6814-6818,6820,6821	CKSSYB104K10	Other Resistors	RS1/16SS###J
C 6951-6955	CKSSYB104K10		110 1/ 1000###U
ck Name: EMMA2 BL OCK/CC\		<u>CAPACITORS</u>	01(05)(5.155)
ck Name: EMMA2 BLOCK(GC)		C 7201	CKSRYB105K10
MOONDHOTO TO		C 7202-7204	BCG1059
MICONDUCTORS		C 7205,7206,7225	DCH1201
IC 7002	TC74VHC08FTS1	C 7207-7221,7223	CKSSYB104K10
IC 7003	UPD61123F1-100KA3A	C 7226	CEHVAW331M6R3
IC 7004	BR24L64F-W	-	
IC 7005	TC7WHU04FU	Block Name: DP_TX BLOCK	
IC 7006	TC74HC4066AFT	SEMICONDUCTORS	
Q 7001	2SJ461A	IC 7601	COREL D16 VOL MED10
Q 7002,7003,7006	DTC124EUA		S25FL016A0LMF013
Q 7005,7008	SSM6N17FU	IC 7602	GM60028H-CG
Q 7007,7010	UMD2N	IC 7603	GMT2404HROM
		MISCELLANEOUS	
<u>CELLANEOUS</u>			ATU1054
L 7001-7003 CHIP BEEDS FILTER	BTX1042	L 7601,7602 CHIP INDUCTOR	ATH1254
F 7001 FERRITE CORE	VTF1091	L 7603-7605 CHIP BEEDS FILTER	BTX1042
X 7001 CRYSTAL (27 MHz)	ASS1225	F 7601-7603 CHIP FERRITE BEADS	ATF1211
A /UUI UNISIAL (Z/ WITZ)	A001220	JA 7601 DP CONNECTOR	AKP1340
SISTORS		⚠ X 7601 CRYSTAL (27 MHz)	ASS1225
R 7026-7028	RS1/16SS2000D	DECICTORS	
R 7029.7036	RS1/16SS6200D	RESISTORS	DO4/000005
,		R 7604	RS1/8SQ0R0J
R 7033	RS1/16SS3300D	R 7645	RS1/10SR2490F
R 7035	RS1/16SS2200D	R 7649-7657,7662	RS1/16SS10R0F
R 7045,7067,7070,7073	RAB4CQ103J	R 7658	RAB4CQ0R0J
		Other Resistors	RS1/16SS###J
R 7060-7064,7066,7068	RAB4CQ101J		· · ·
R 7065,7075	RAB4CQ470J	<u>CAPACITORS</u>	
R 7069,7071,7083,7084	RAB4CQ101J	C 7601,7608-7614,7616	CKSSYB104K10
R 7072	RAB4CQ221J		
R 7074	RAB4CQ103J	C 7602	CKSSYB471K50
1911	10 10 00 1000	C 7603	BCG1059
R 7081	RAB4CQ222J	C 7604,7605	CCSSCH120J50
		C 7606,7607,7617,7619	DCH1201
R 7087-7091	RAB4CQ101J		
R 7109	RS1/8SQ0R0J	C 7618,7621,7624-7627	CKSSYB104K10
Other Resistors	RS1/16SS###J	C 7620,7639	DCH1201
		C 7629-7638,7640-7650	CKSSYB104K10
PACITORS	01/00/05 1051/10		
C 7001,7003-7011	CKSRYB105K10	Unit Name: FRONT_HDM_US	B ASSY(GC MR)
C 7014	CKSSYB102K50	Jim Hame. I Holff_Hbw_03	S ASSI (GO IVIII)
C 7029,7030	CCSSCH120J50		
C 7031,7032	CCSSCH470J50	MISCELLANEOUS	
C 7035-7040,7043	CKSSYB104K10	⚠ L 7301,7302 CHIP BEEDS FILTER	BTX1042
•	•	JA 7303 USB CONNECTOR	VKB1248
	DCH1201	CN7304 CONNECTOR	AKM1291
C 7041.7044.7049			
C 7041,7044,7049 C 7045-7048 7050 7051			
C 7041,7044,7049 C 7045-7048,7050,7051	CKSSYB104K10	CAPACITORS	
		CAPACITORS C 7301	ACH1421

2 3 Mark No. **Description** Part No. Mark No. **Description** Part No. C 9410,9411 CKSSYB104K10 **Unit Name: LED ASSY(EU MR) Unit Name: REAR IO ASSY(GC MR) SEMICONDUCTORS SEMICONDUCTORS** D 9401 SML-521MDW D 8601 **1SS301** D 9402 TLRV1022 D 9403 SMLE12BC7T(NP) **MISCELLANEOUS** VKB1159 JA 8602 JACK **MISCELLANEOUS** CN 8602 FFC CONNECTOR 9P RA AKM1381 L 9408-9410 CHIP SOLID INDUCTOR CN 8603 9P D-SUB SOCKET AKP1213 QTL1013 KM200NA6L CN 9402 L-PLUG (6P) RESISTORS **RESISTORS** RS1/16SS###J All Resistors All Resistors RS1/10SR###J **CAPACITORS CAPACITORS** C 8604 CKSSYB471K50 C 9404,9406,9407 CKSSYB103K16 Unit Name: FRONT IO ASSY(EU MR) **SEMICONDUCTORS** IC 8501 BR24L01AFJ-W IC 8502 TC74VHC08FTS1 Q 8501 DTC124EUA Q 8502-8504 2SC4081 D 8507 1SS301

UDZS5R1(B)

D 8508

MISCELLANEOUS

В

 JA 8501
 PIN JACK (3P)
 AKB1303

 JA 8503
 MINI JACK
 AKN1085

 CN 8501
 FFC CONNECTOR 26P
 AKM1441

 CN 8503
 15P D-SUB SOCKET
 AKP1214

 RESISTORS

 R
 8501,8508
 RST1/2SP120J

 R
 8506,8510-8512
 RS1/10SR75R0F

 R
 8514
 RAB4CQ473J

 R
 8515,8516
 RAB4CQ101J

 R
 8517
 RAB4CQ222J

Other Resistors RS1/16SS###J

CAPACITORS

C 8501,8510-8512 CKSRYB105K10

C 8502,8509 CKSSYB104K10
C 8503,8504,8513,8514 CKSSYB102K50
C 8507,8508 ACH1454
C 8515 CKSRYB104K16

Unit Name: KEY ASSY(EU MR)

SEMICONDUCTORS
Q 9401

Q 9401 HN1B04FU TH 9401 TH05-3H103F

MISCELLANEOUS

L 9401-9406 CHIP SOLID INDUCTOR QTL1013
 S 9401-9406 PUSH SWITCH CSG1155
 CN9401 L-PLUG (7P) KM200NA7L

RESISTORS

R 9407 RS1/10SR4701F

Other Resistors RS1/16SS###J

CAPACITORS

C 9409 CKSSYB103K16





KRP-M01

ORDER NO. ARP3509

KRP-M01

THIS MANUAL IS APPLICABLE TO THE FOLLOWING MODEL(S) AND TYPE(S).

Model	Туре	Power Requirement	Remarks
KRP-M01	WYSIXK5	AC 220 V to 240 V	
KRP-M01	WYSXJ5	AC 220 V to 240 V	

This service manual should be used together with the following manual(s).

Model No.	Order No.	Remarks
KRP-M01	ARP3508	EXPLODED VIEWS, BLOCK DIAGLAM, ADJUSTMENT, etc



PIONEER CORPORATION 4-1, Meguro 1-chome, Meguro-ku, Tokyo 153-8654, Japan PIONEER ELECTRONICS (USA) INC. P.O. Box 1760, Long Beach, CA 90801-1760, U.S.A. PIONEER EUROPE NV Haven 1087, Keetberglaan 1, 9120 Melsele, Belgium PIONEER ELECTRONICS ASIACENTRE PTE. LTD. 253 Alexandra Road, #04-01, Singapore 159936 © PIONEER CORPORATION 2008

In this manual, procedures that must be performed during repairs are marked with the below symbol. Please be sure to confirm and follow these procedures.

1. Product safety



В

Please conform to product regulations (such as safety and radiation regulations), and maintain a safe servicing environment by following the safety instructions described in this manual.

① Use specified parts for repair.

Use genuine parts. Be sure to use important parts for safety.

Do not perform modifications without proper instructions.

Please follow the specified safety methods when modification(addition/change of parts) is required due to interferences such as radio/TV interference and foreign noise.

3 Make sure the soldering of repaired locations is properly performed.

When you solder while repairing, please be sure that there are no cold solder and other debris. Soldering should be finished with the proper quantity. (Refer to the example)

4 Make sure the screws are tightly fastened.

Please be sure that all screws are fastened, and that there are no loose screws.

5 Make sure each connectors are correctly inserted.

Please be sure that all connectors are inserted, and that there are no imperfect insertion.

6 Make sure the wiring cables are set to their original state.

Please replace the wiring and cables to the original state after repairs. In addition, be sure that there are no pinched wires, etc.

Make sure screws and soldering scraps do not remain inside the product.

Please check that neither solder debris nor screws remain inside the product.

® There should be no semi-broken wires, scratches, melting, etc. on the coating of the power cord.

Damaged power cords may lead to fire accidents, so please be sure that there are no damages. If you find a damaged power cord, please exchange it with a suitable one.

9 There should be no spark traces or similar marks on the power plug.

When spark traces or similar marks are found on the power supply plug, please check the connection and advise on secure connections and suitable usage. Please exchange the power cord if necessary.

10 Safe environment should be secured during servicing.

When you perform repairs, please pay attention to static electricity, furniture, household articles, etc. in order to prevent injuries. Please pay attention to your surroundings and repair safely.

2. Adjustments



To keep the original performance of the products, optimum adjustments and confirmation of characteristics within specification. Adjustments should be performed in accordance with the procedures/instructions described in this manual.

3. Lubricants, Glues, and Replacement parts



Ε

Use grease and adhesives that are equal to the specified substance. Make sure the proper amount is applied.

4. Cleaning



For parts that require cleaning, such as optical pickups, tape deck heads, lenses and mirrors used in projection monitors, proper cleaning should be performed to restore their performances.

5. Shipping mode and Shipping screws



To protect products from damages or failures during transit, the shipping mode should be set or the shipping screws should be installed before shipment. Please be sure to follow this method especially if it is specified in this manual.

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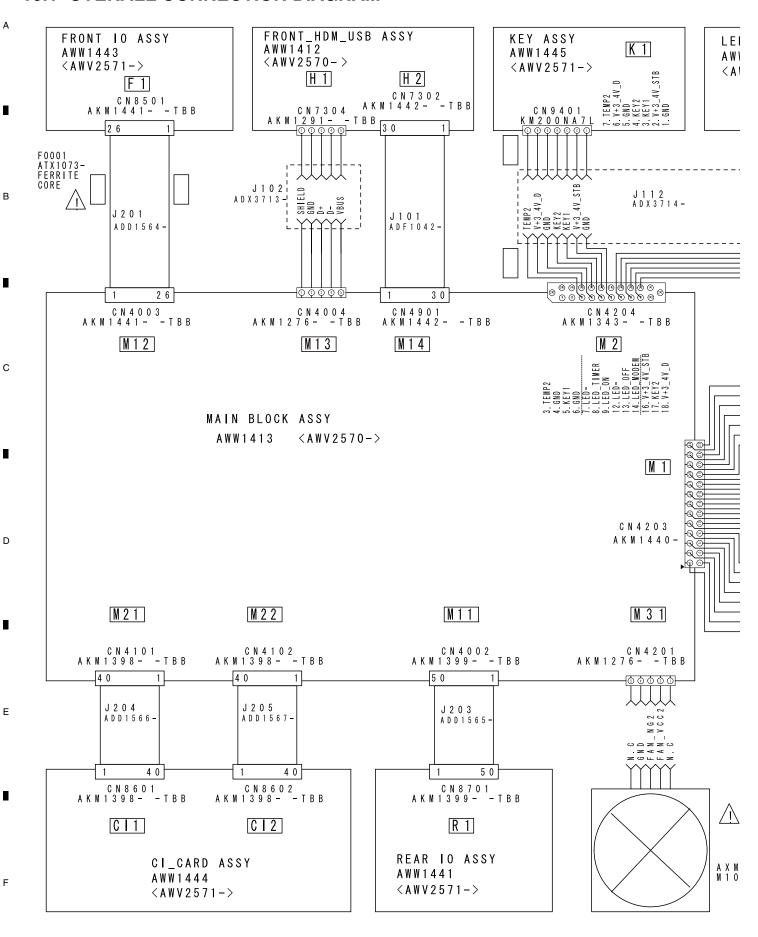
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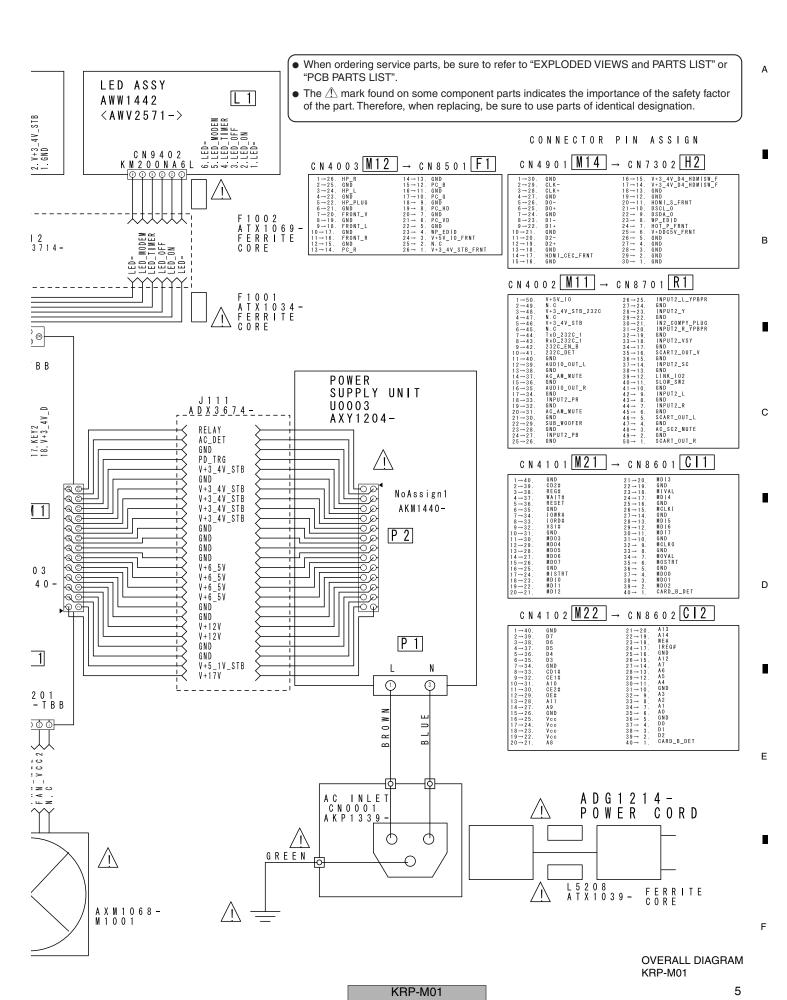
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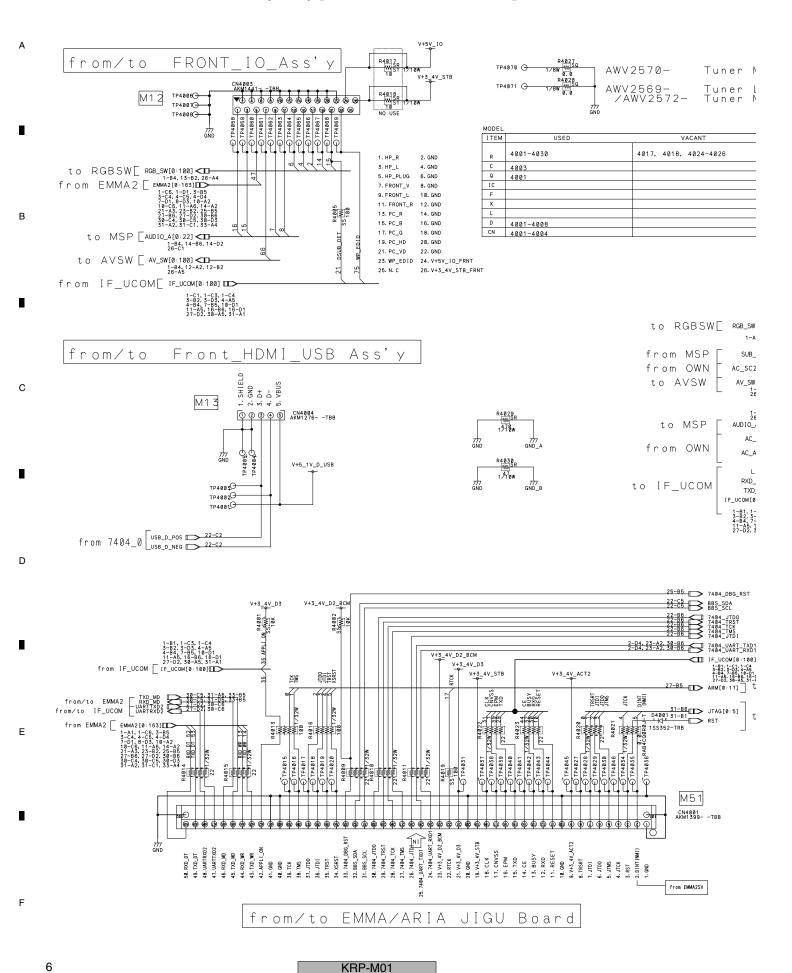
10. SCHEMATIC DIAGRAM 10.1 OVERALL CONNECTION DIAGRAM

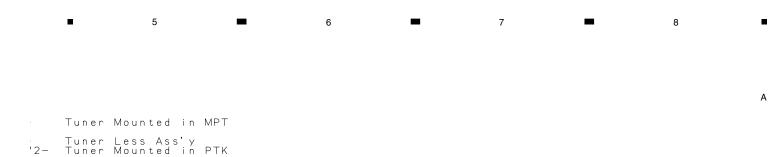


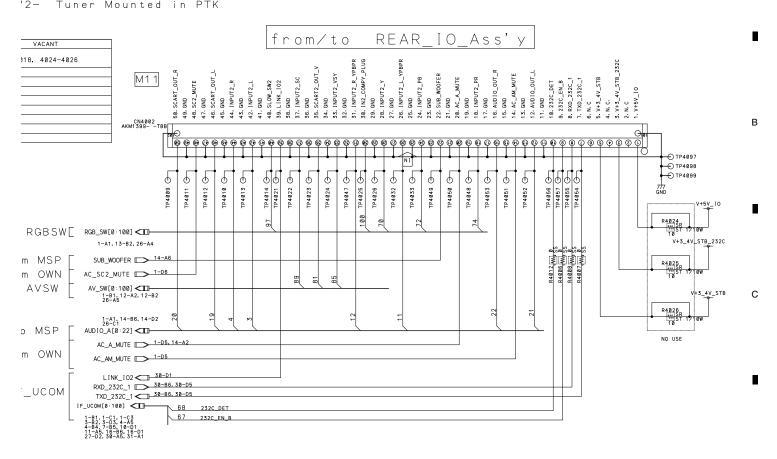
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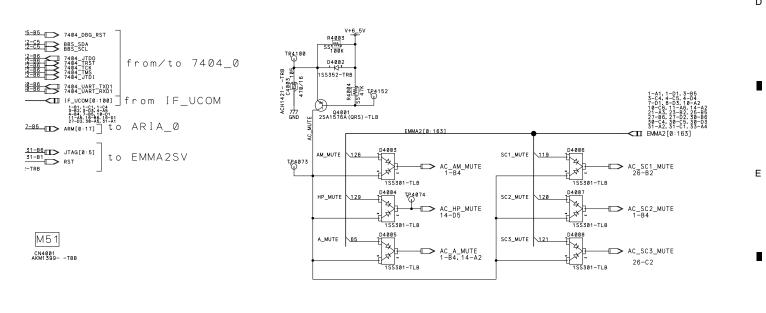


10.2 MAIN BLOCK ASSY (1/33) [BOARD_IF_0 BLOCK]









from EMMA2SV MAIN ASSY (MR_EU) BOARD_IF_0 BLOCK

AWV2578- : AWW1413

10.3 MAIN BLOCK ASSY (2/33) [BOARD_IF_1 BLOCK]

Α

В

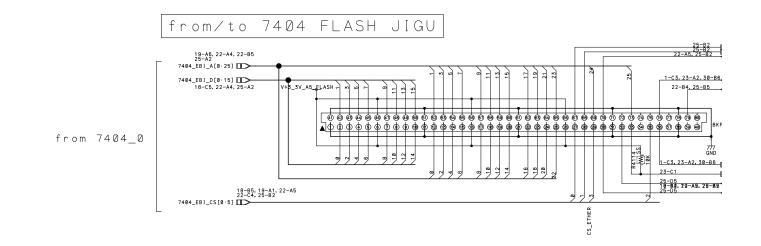
С

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8

from/to CI_CARD_Ass'y M21 TP4160 🔾 TP4161 (-) **a** 40. GND 40. GND ____ TP4101 67. 39. CD2B# 39. D7 → TP4102 38. REG#_B 61 38. D6 → TP4103 → TP4107 59 37. WAITB# 37. D5 58 36. RESETB 36. D4 35. GND 35. D3 → TP4108 → TP4109 → TP4110 34 IOWR# R 34 GND 44 33. | ORD#_B 33. CD1B# 43 32. VS1#_B 32. CE1B# 31. GND 31. A10 37 30. MDOB3 30. CE2B# <u>→ TP4112</u> 38 29. MDOB4 29. OE#_B → TP4113 → TP4114 → TP4115 39 28. MDOB5 28. A11 40 27. MDOB6 27. A9 26. MDOB 7 41, 26. GND 25. GND 25. Vcc 46 24. MISTRTB 24. Vcc → TP4117 23. MD | B0 47/ 23. Vcc → TP4118 → TP4119 → TP4120 48 22. MD | B1 22. Vcc 49, 21. MD | B2 21. A8 (1) 20. MD | B3 50 20. A13 19. GND 19. A14 19. 18. MIVALB 18. WE#_B → TP4122 53 17. MD | B4 17. IREQB# 16. GND 16. GND 20 15. MCLKIB 15. A12 14. GND ூ 14. A7 13. MD | B5 13. A6 → TP4142 → TP4143 55. 12. MD [B 6 12. A5 56, 11. MD | B7 11. A4 10. GND 10. GND 57 9. MCLKOB 9. A3 8. GND 8. A2 <u>→ TP4145</u> 7. MOVALB 7. A1 → TP4146 63 6. MOSTRTB 6. AØ 5. GND 5. GND 4. MDOBØ 4. DØ → TP4148 → TP4149 65, 3. MDOB1 3. D1 66 2. MDOB2 2. D2 1. CARD_B_DET 1. CARD_B_DET <u>a</u>

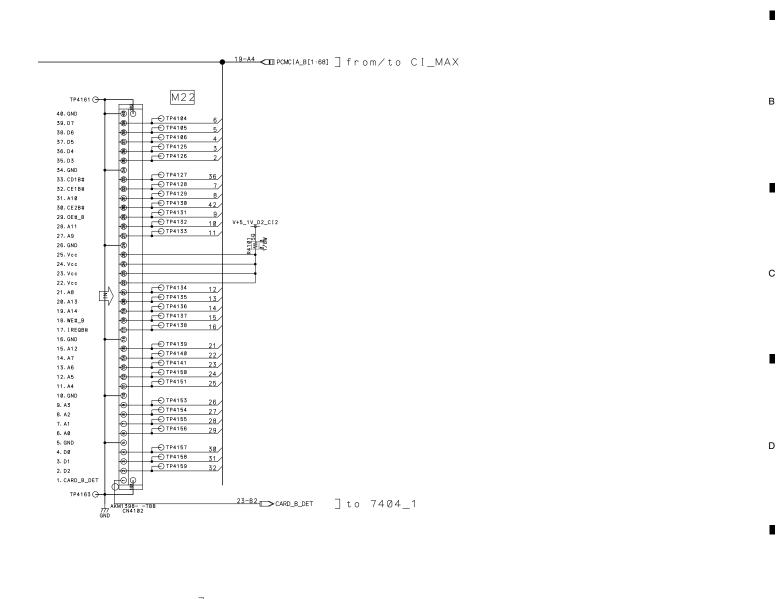
TP4163 🔾



KRP-M01

TP4162 ⊝

AKM1398- -TBB 777 CN4101 GND



> MAIN ASSY (MR_EU) (02/34) BOARD_IF_1 BLOCK

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KRP-M01

MODEL

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25-B2 25-B2 22-A5, 25-B2 22-A5, 25-B2 7404_EBI_WE0

1-C3, 23-A2, 30-B6 7404_UART_TXD1 22-B4, 25-B5 RESET_7404

1-C3, 23-A2, 30-B6 7404_UART_RXD1

25-D5 18-88.29-A9.28-85 25-D5 7404_EBI_RD

9999999

23-C1

M52

CN4104 BKP1159- -TBB

EXT_DBG_INT

from 7404_0

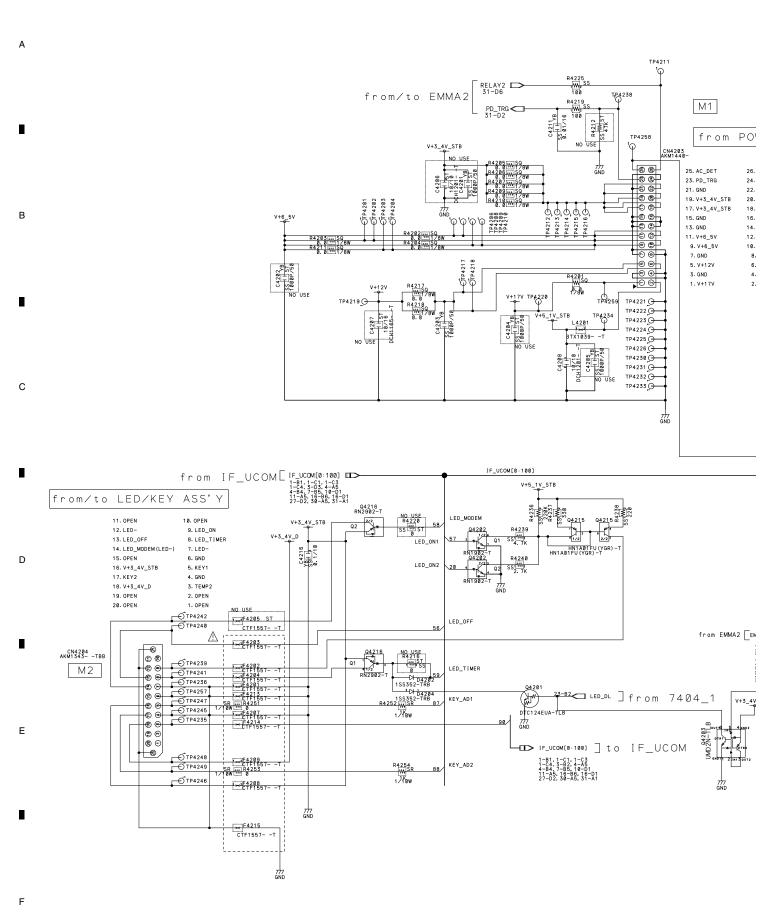
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-

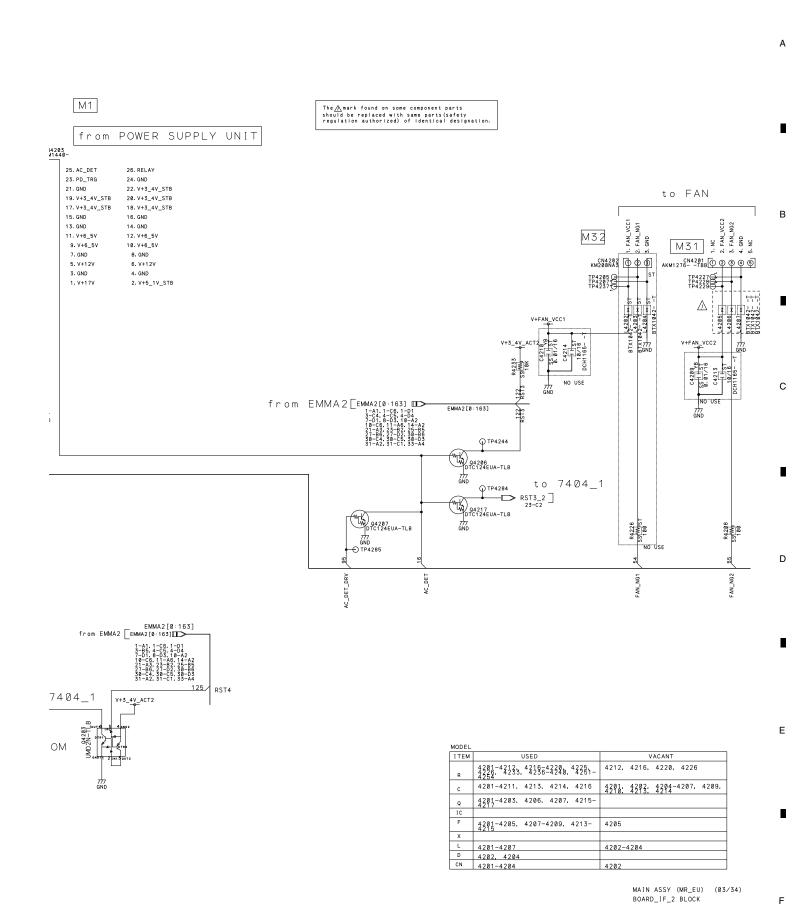
10.4 MAIN BLOCK ASSY (3/33) [BOARD_IF_2 BLOCK]



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KRP-M01

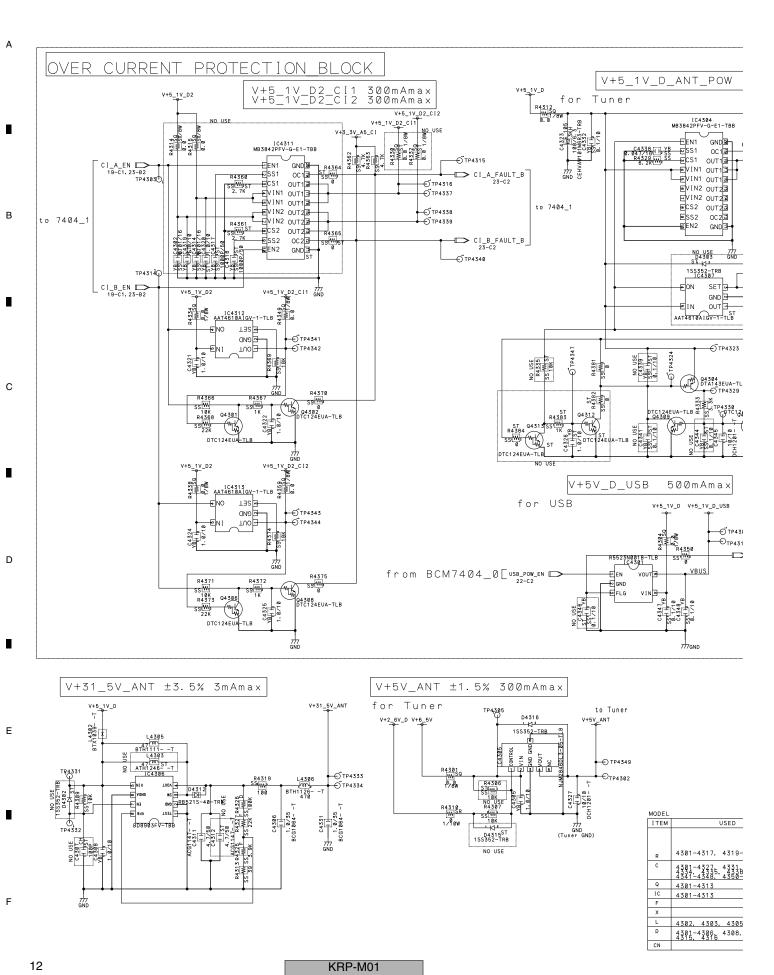
-

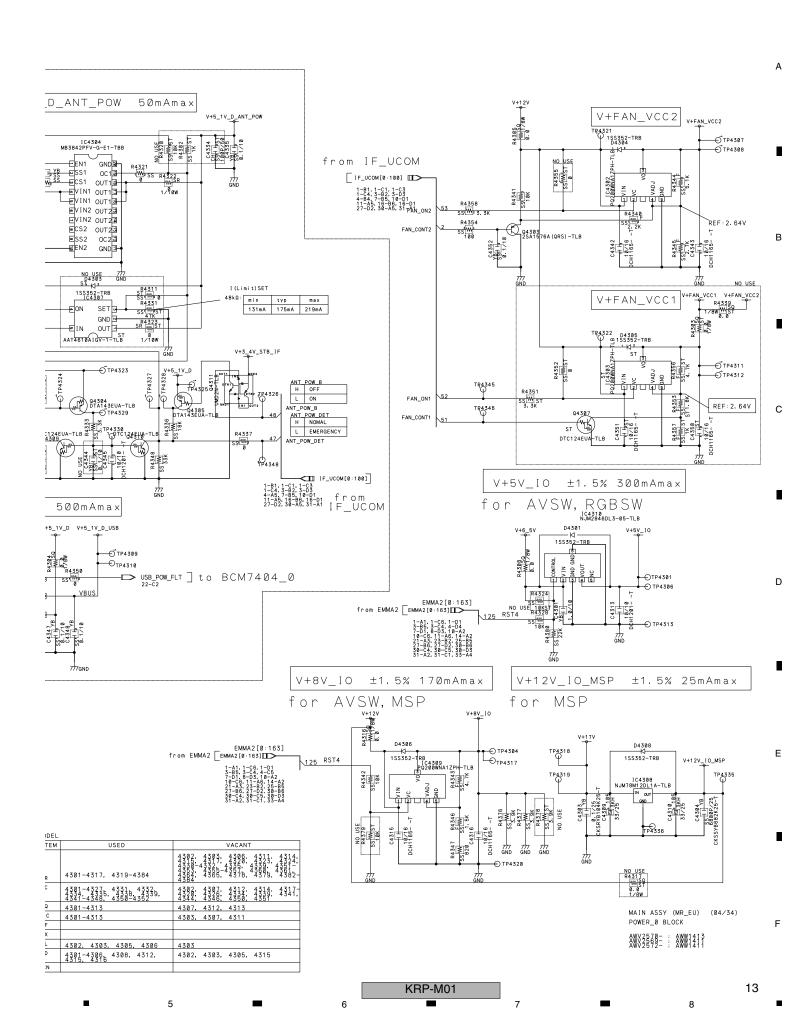


KRP-M01 11 8

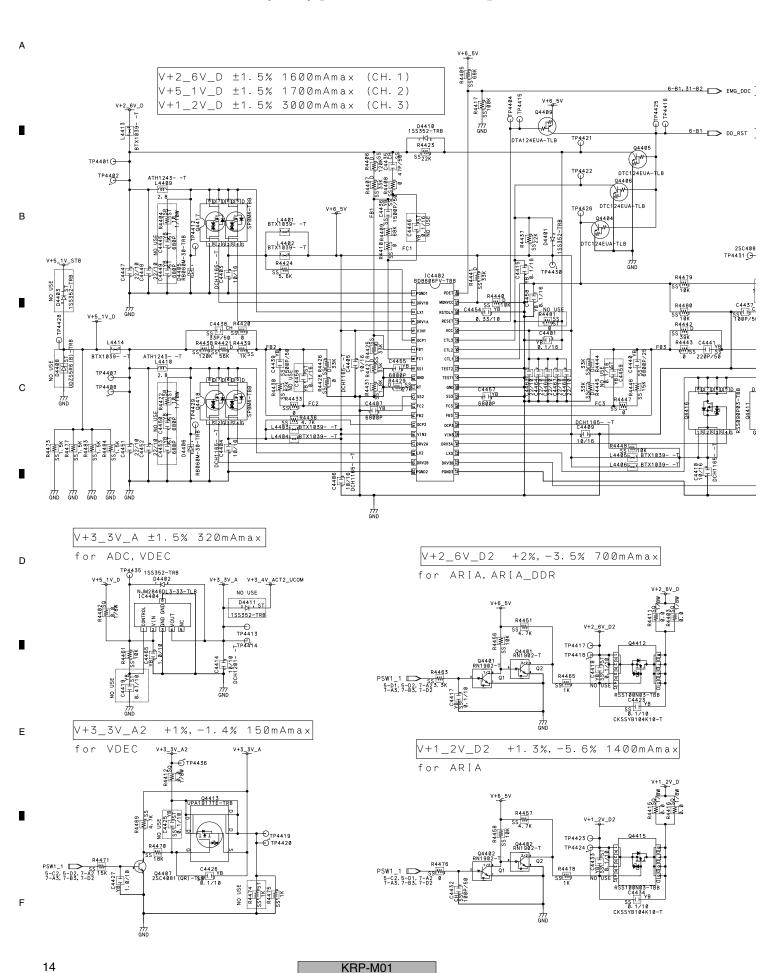
AWV2578- : AWW1413

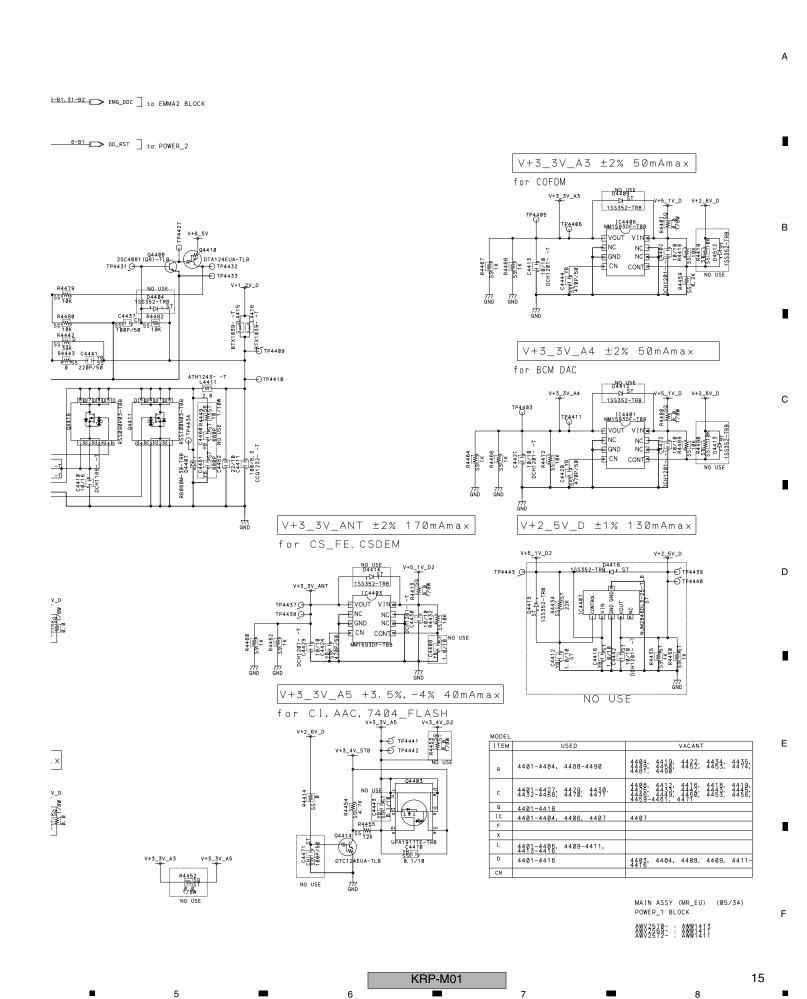
10.5 MAIN BLOCK ASSY (4/33) [POWER_0 BLOCK]



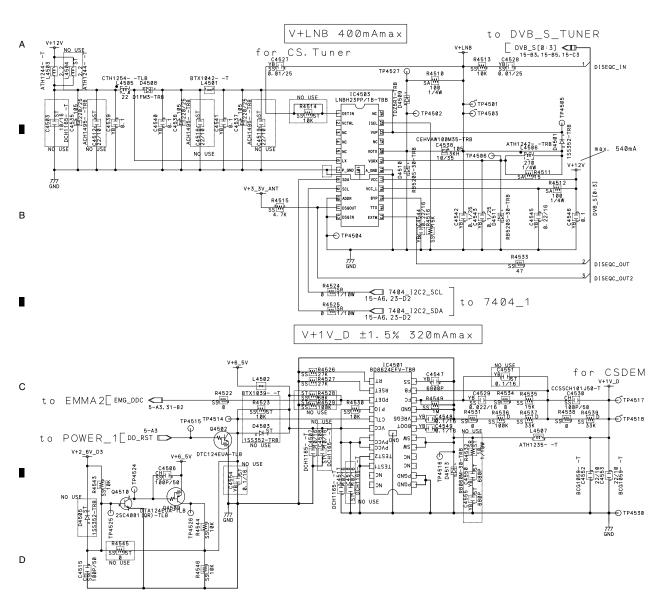


10.6 MAIN BLOCK ASSY (5/33) [POWER_1 BLOCK]

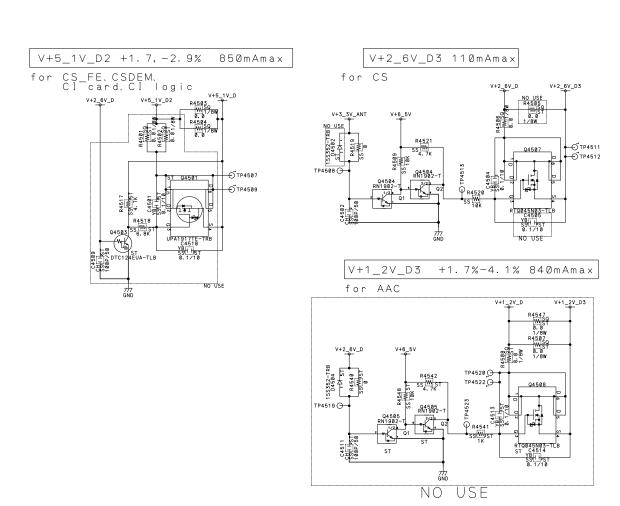




10.7 MAIN BLOCK ASSY (6/33) [POWER_2 BLOCK]



16



MODEL		
ITEM	USED	VACANT
R	4501-4549	4501, 4502, 4505, 4507, 4508, 4512, 4512, 4548, 4548, 4545, 4545,
с	4501-4519, 4527-4530, 4535-4555,	4501: 4503: 4505: 4508-45151.
Q	4501-4505, 4507-4510	4501, 4503, 4505, 4508
1C	4501, 4503	
F		
X		
L	4501-4507	4504
D	4501-4505, 4508-4513	4502-4505, 4512
CN		

MAIN ASSY (MR_EU) (06/34) POWER_2 BLOCK

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В

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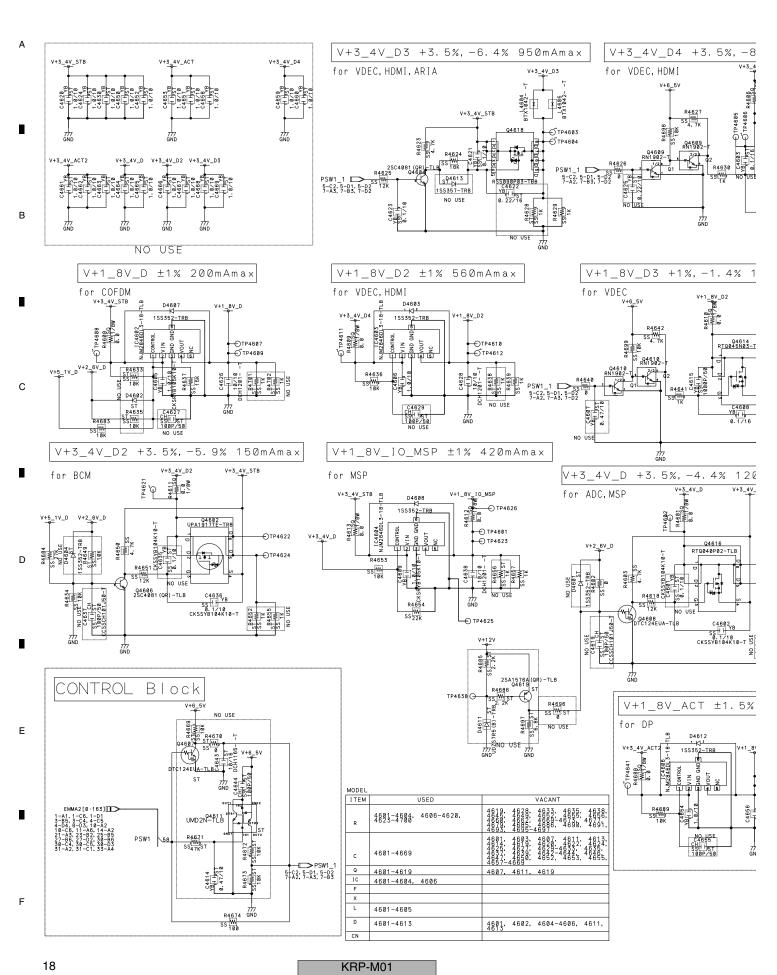
Е

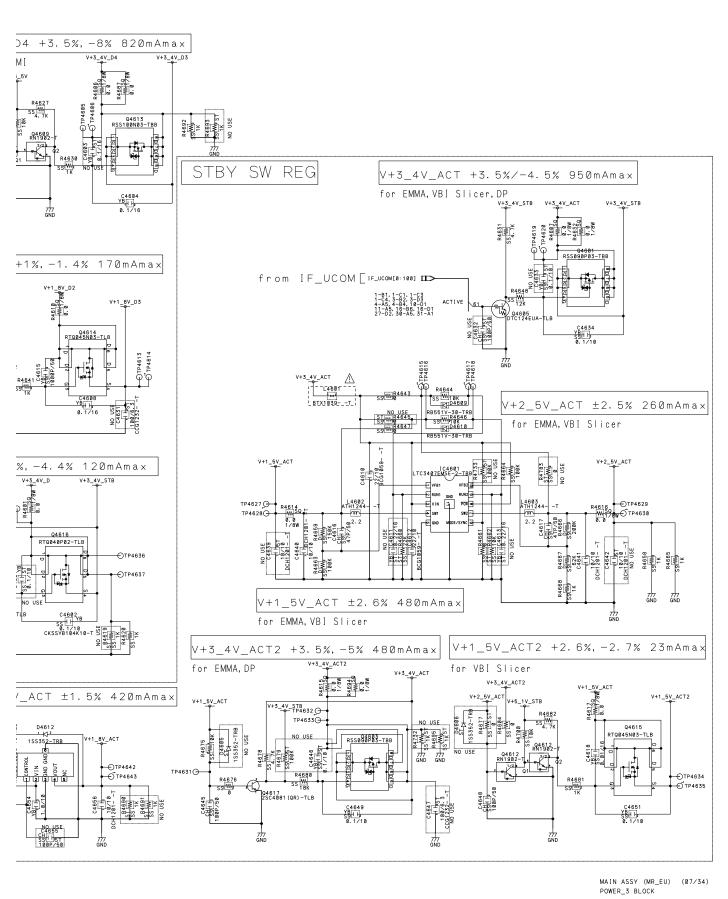
AWV2578- : AWW1413

KRP-M01 17

■ 6 **■** 7

10.8 MAIN BLOCK ASSY (7/33) [POWER_3 BLOCK]



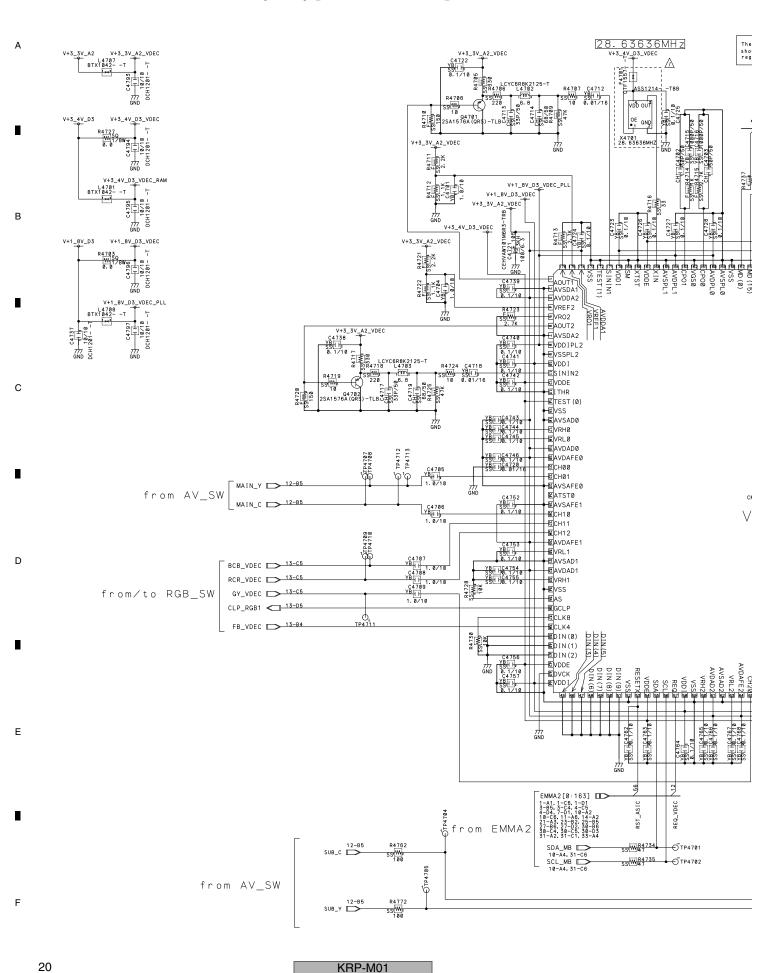


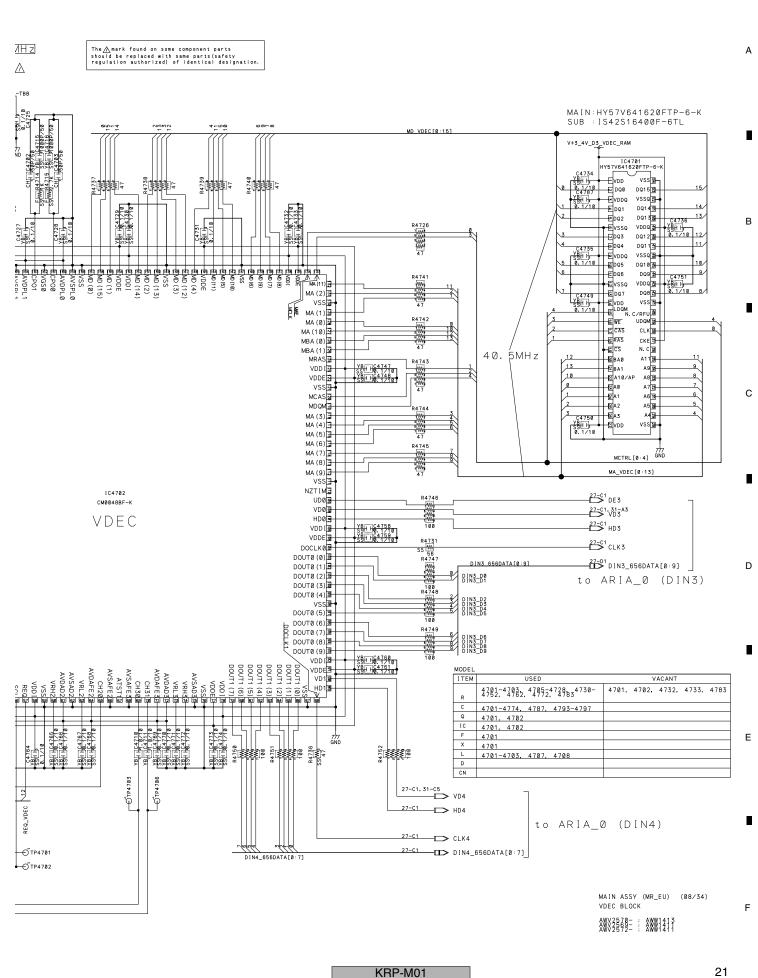
AWY2578- : AWW1411

19

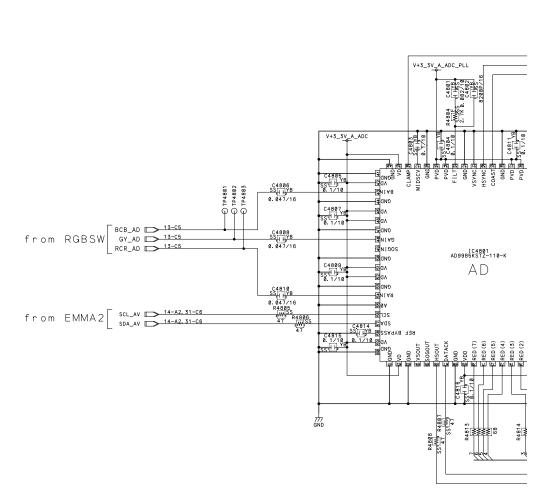
Ε

10.9 MAIN BLOCK ASSY (8/33) [VDEC BLOCK]

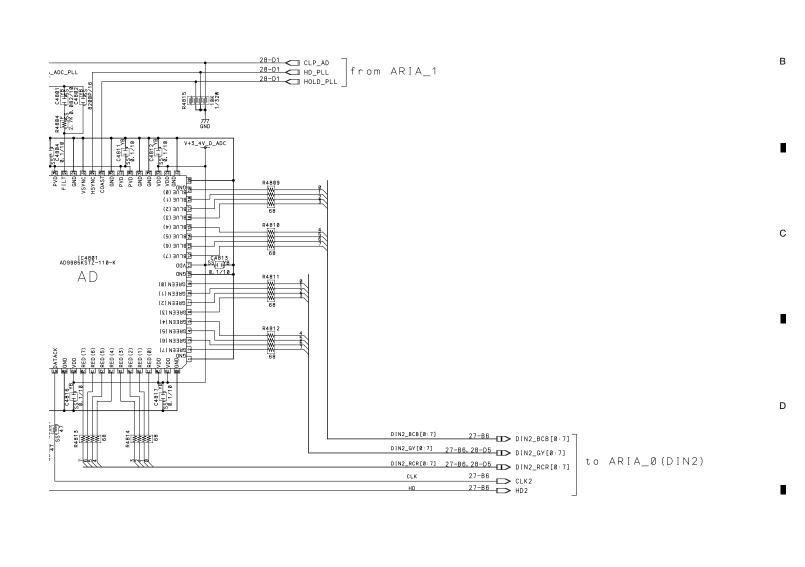




10.10 MAIN BLOCK ASSY (9/33) [ADC BLOCK]



22



MAIN ASSY (MR_EU) (09/34) ADC BLOCK

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AWV2578- : AWW1413

VACANT

KRP-M01

USED

4803-4815 4801-4820

4801, 4802

4801

MODEL

Q IC F

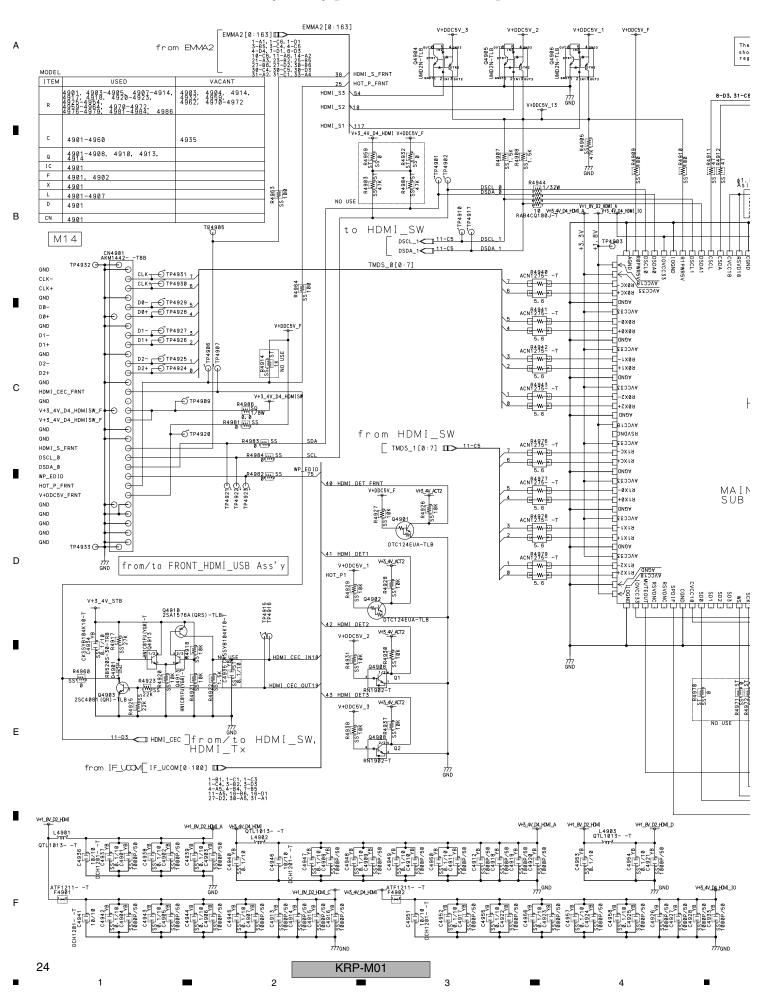
X L D

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10.11 MAIN BLOCK ASSY (10/33) [HDMI_RX BLOCK]



The ∧ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation. 8-D3, 31-C6 \$CL_MB | from EMMA RAB4CQ680J-T R4945 R4945 WW WW WW 1 / 32W 68 R4913 SS W SS W 88 SS W 8491 SS W 8491 V+1_8V_D2_HDMI_C ø1.0 Aside 27-D5 CLK6 27-D5 DIN6_BCB[0:11] 27-D5 DIN6_GY[0:11]
27-D5 DIN6_RCR[0:11] DIN6_GY[0:11] to Aria_0(DIN6) HSYNC

HCGND

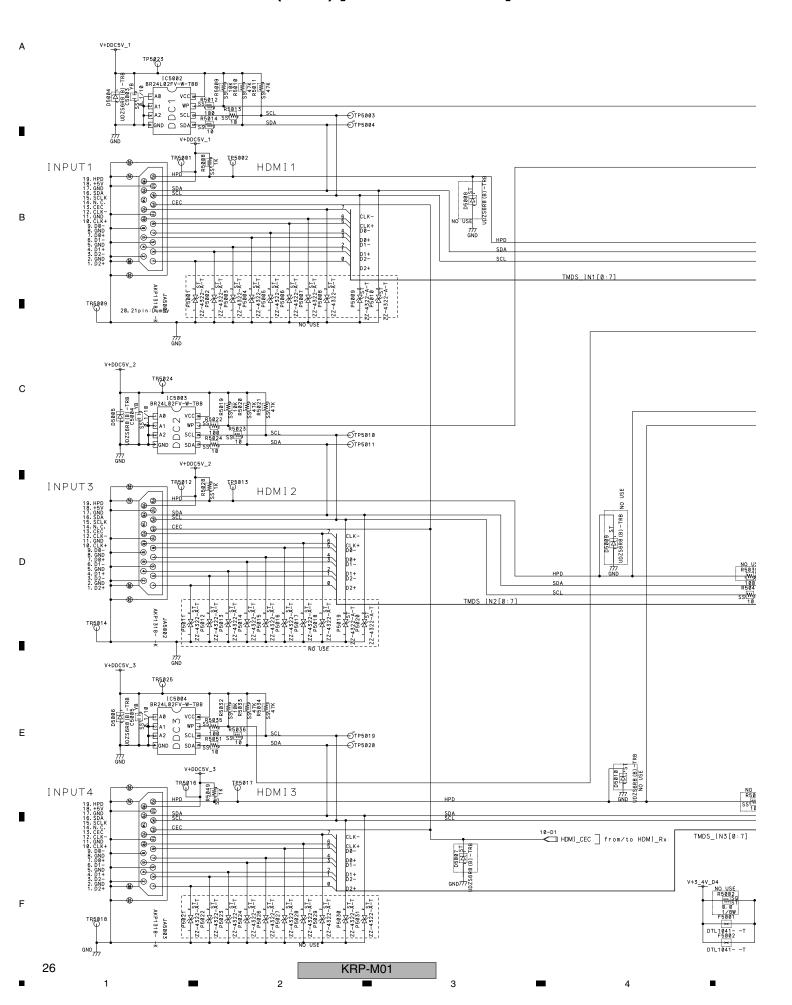
HCGND

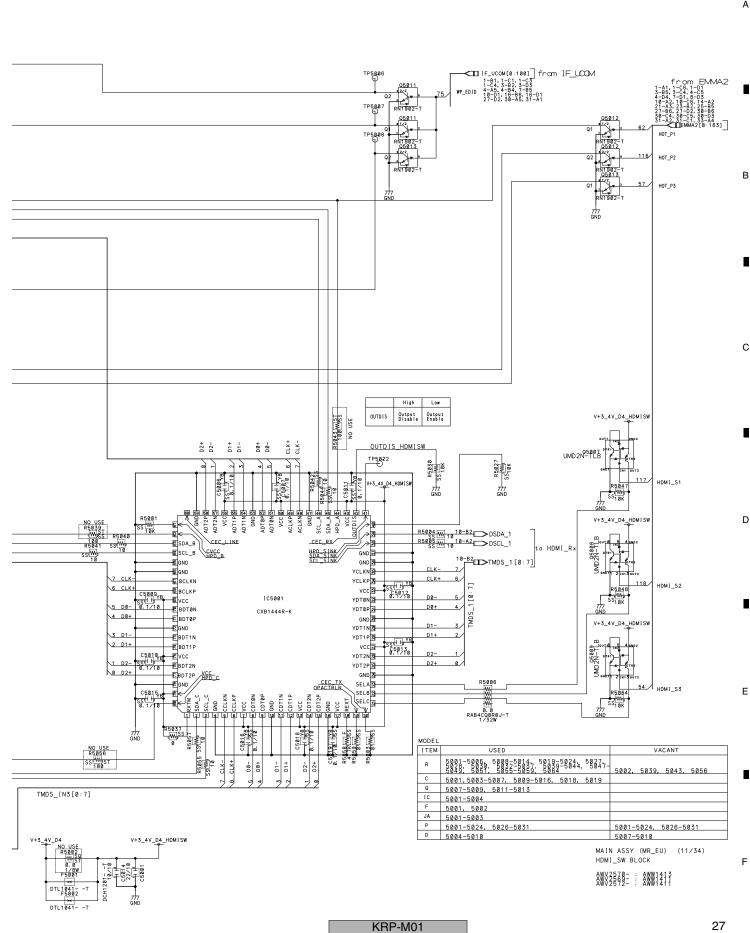
HCGND

HCCGND

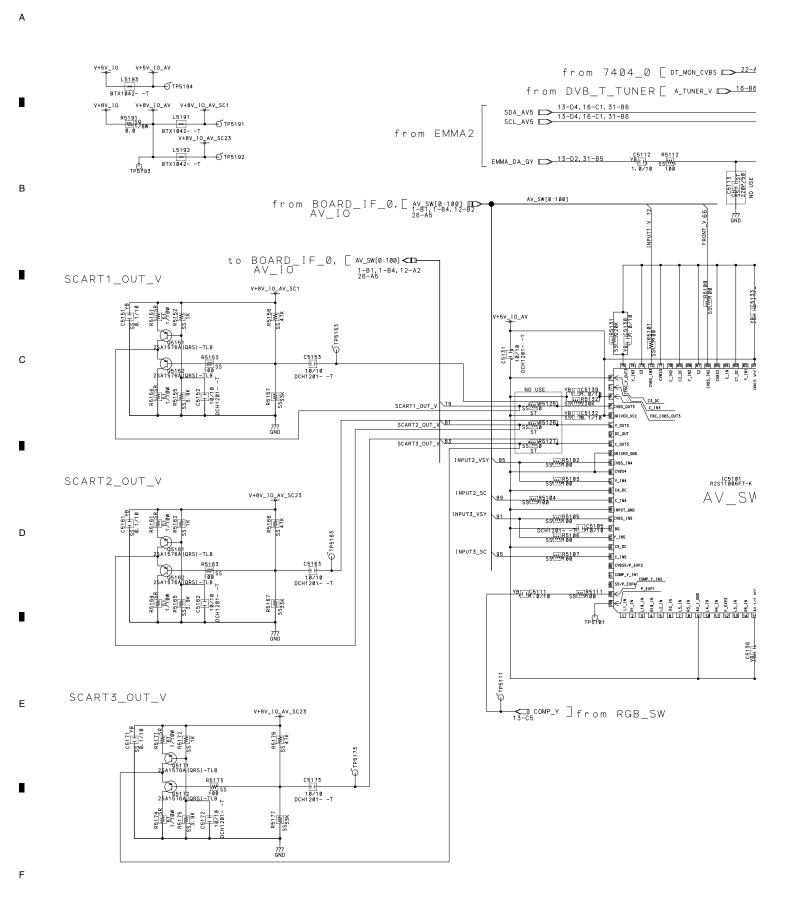
HC TOGND BA2_PLK[0:11] GA2_PLK[0:11] Гсеир RA2_PLK[0:11] RAB4CQ880J-T 1013 +101+ 10ACC22 -П осив R4948 5 1107 810 JONO T R4949 Сеив HDMI_RX 020 7.W91/32W RAB4CQ880J-T 9 120 ZZO R4950 IC4901 SII9135ACTU-K 052 TP4911 10vcc *:W# 1W#1/32W RAB4CQ880J-T R4951 φι -Посив +20<u>1</u> 4025 777 GND 920 RAB4CQ880J-T R4952 Win Win Win Win Win RAB4CQ880J-T RAB653 MAIN SII9135ACTU-K SUB SII9135CTU-K #ICVCC18 Сеир 620 020 10ACC22 Посир 9/ 033 1W1/32W RAB4CQ880J-T TP4935 V+3_4V_D4_HDMI 2€ Q' UMD2N-TLB TR4908 EMMA2[0:163] | from EMMA2 HDMI_INT R4978 TR4918 ASS1226--1 28. 322MHZ R4936 SS W2 RST_ASIC TP4937 NO USE V+3_4V_D4_HDMI Е V+3_4V_D4_HDMI UMD2N-TLB HDMI_MUTE R4962 SS West NO USE 14-B1 | 12S_BCLK_HDMI 14-B1 | 2S_LRCLK_HDM| to MSP 4961 SPD IF_HDMI V+3_4V_D4_HDMI V+3_4V_D4_HDMI_REG L4905 QTL1013- -T V+1_8V_D2_V+1_8V_D2_HDMI L4907 BTX1042--T MAIN ASSY (MR_EU) (10/34) HDMI_RX BLOCK KRP-M01 25

10.12 MAIN BLOCK ASSY (11/33) [HDMI_SW BLOCK]





10.13 MAIN BLOCK ASSY (12/33) [AV_SW BLOCK]

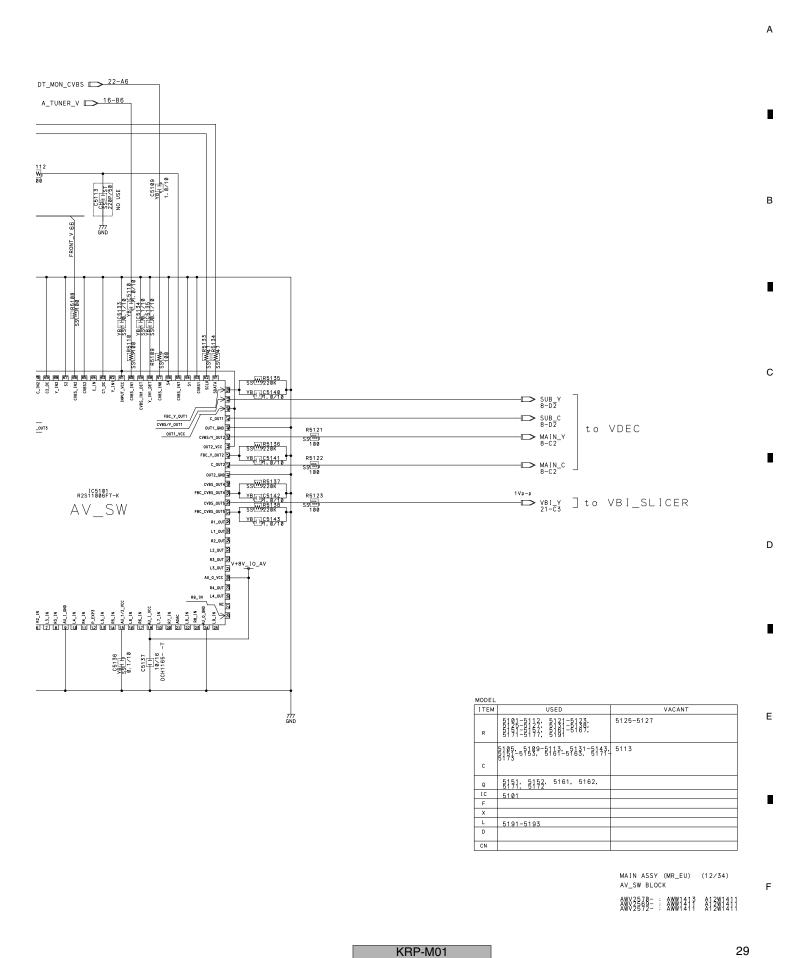


28

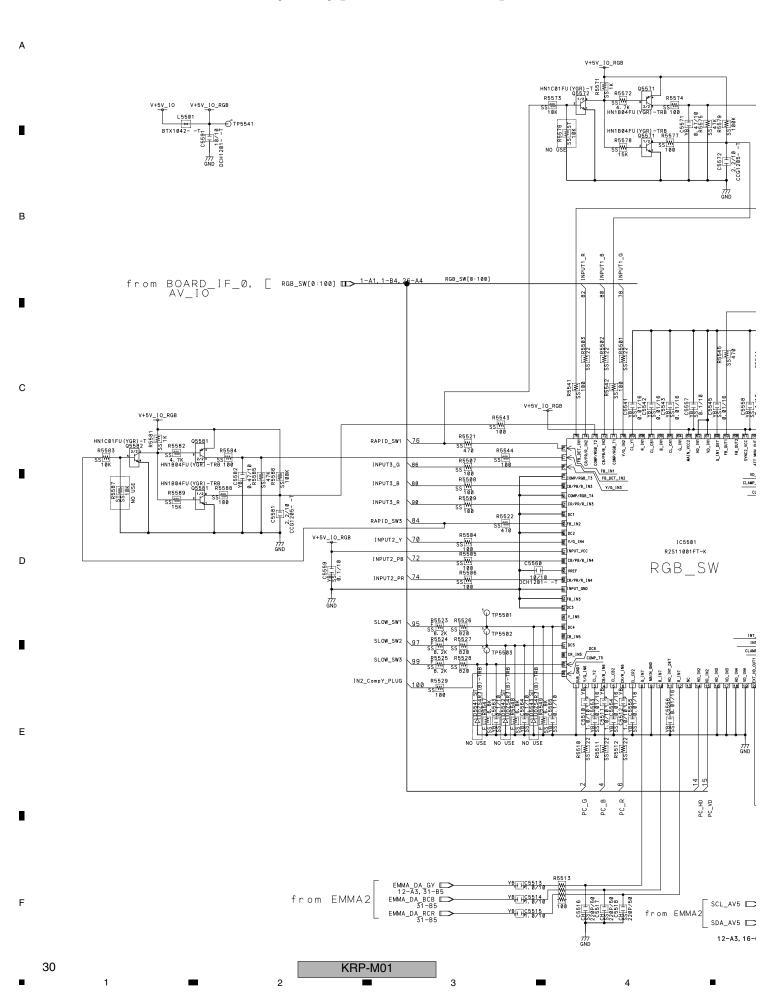
2

KRP-M01

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10.14 MAIN BLOCK ASSY (13/33) [RGB_SW BLOCK]

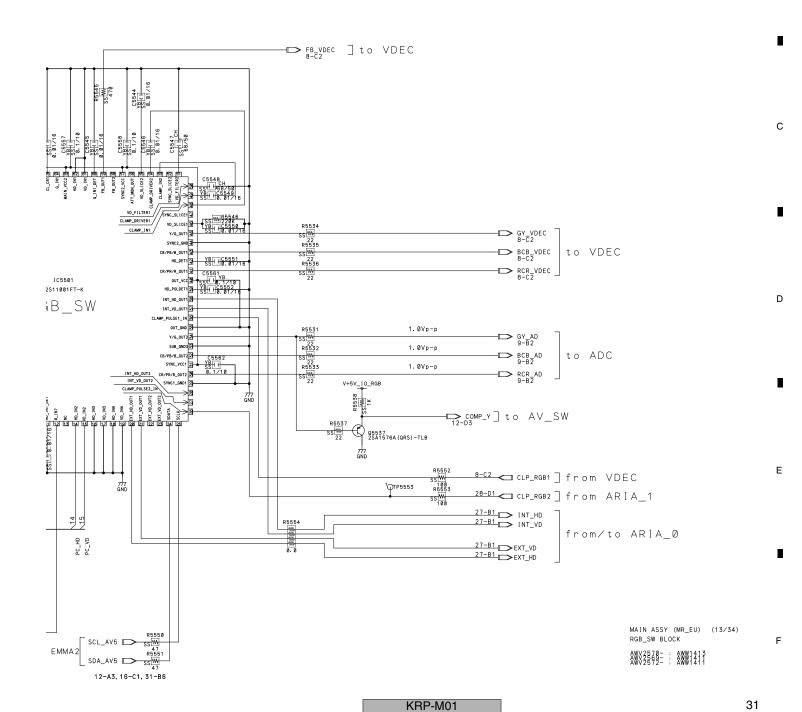




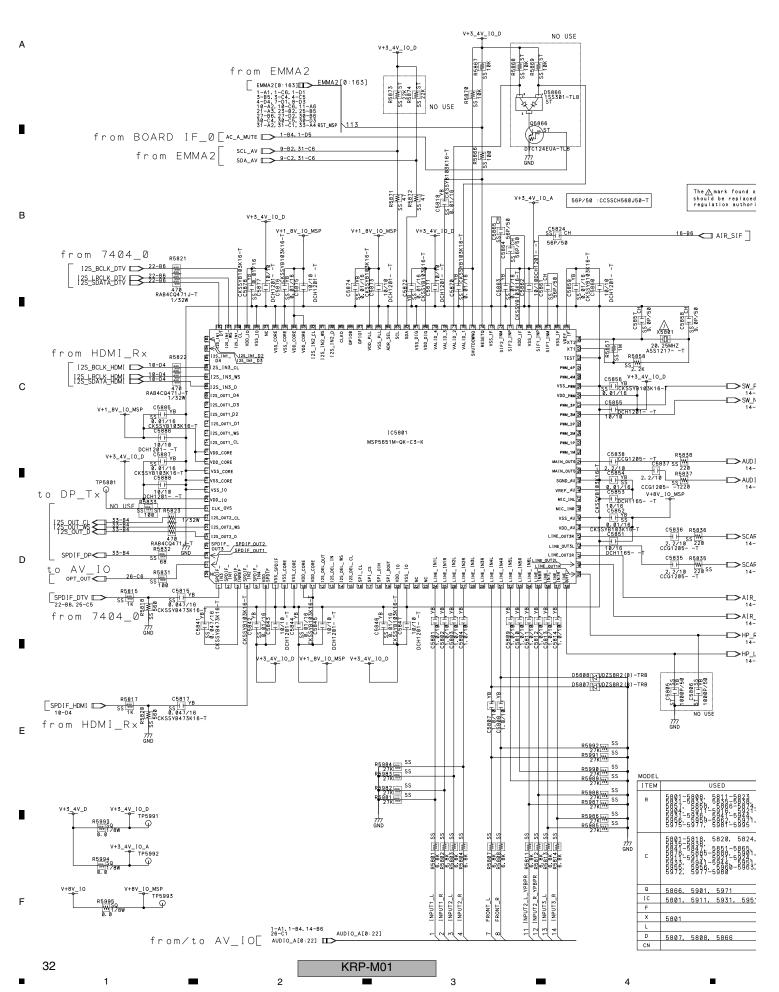
5574				
100 100 577	8.47/18 R5575	85 W 47K R5579	55 WE 100K	
577 > M		C5572	ccG1205T	
		77 Gi		

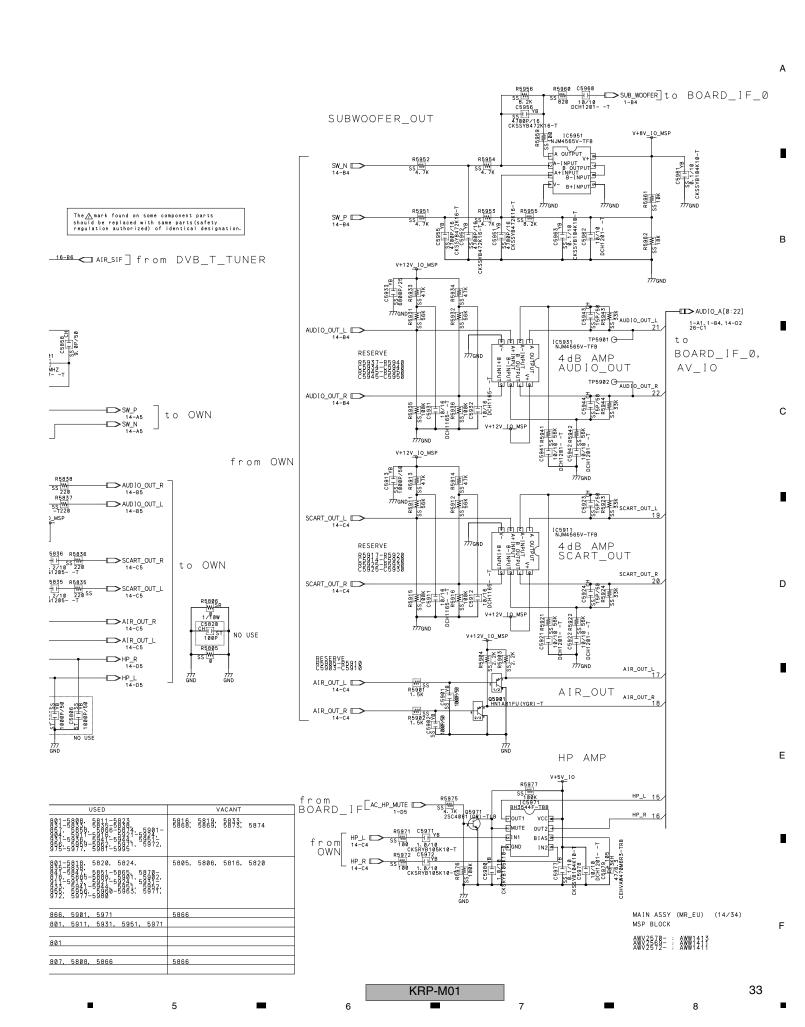
MODEL		
ITEM	USED	VACANT
R	5501-55131-5521-55291-5579. 5581-5589	5576, 5587
С	5510-5518, 5541-5565, 5571, 5572, 5581, 5582, 5591	
Q	5537, 5571, 5572, 5581, 5582	
1 C	5501	
F		
Х		
L	5501	
D	5541-5543	5541-5543
CN		

В

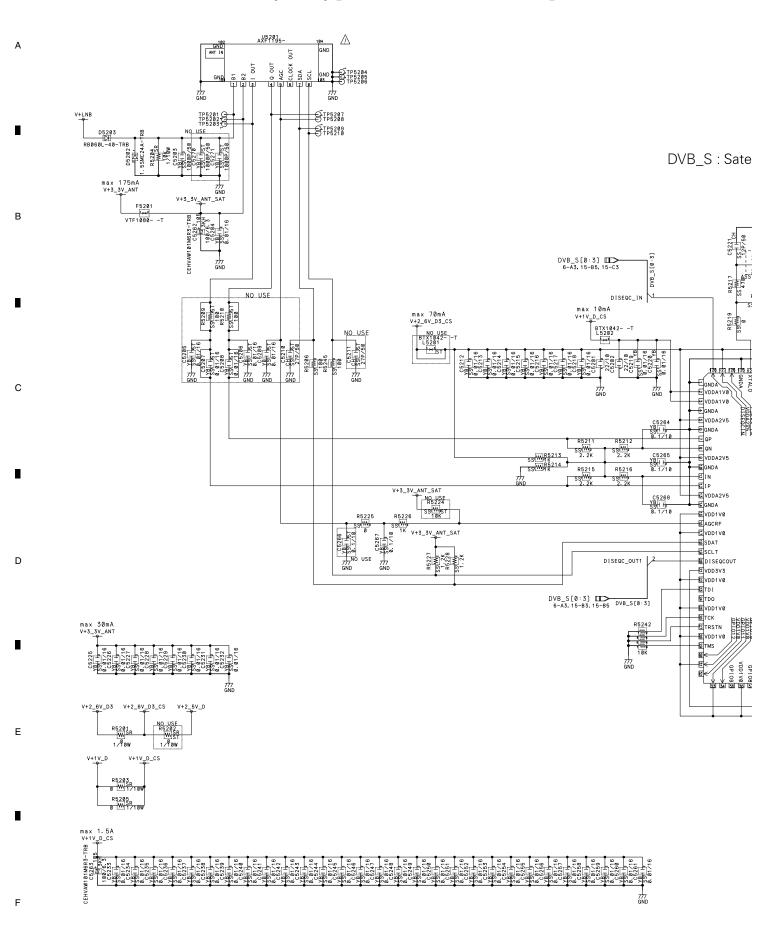


10.15 MAIN BLOCK ASSY (14/33) [MSP BLOCK]

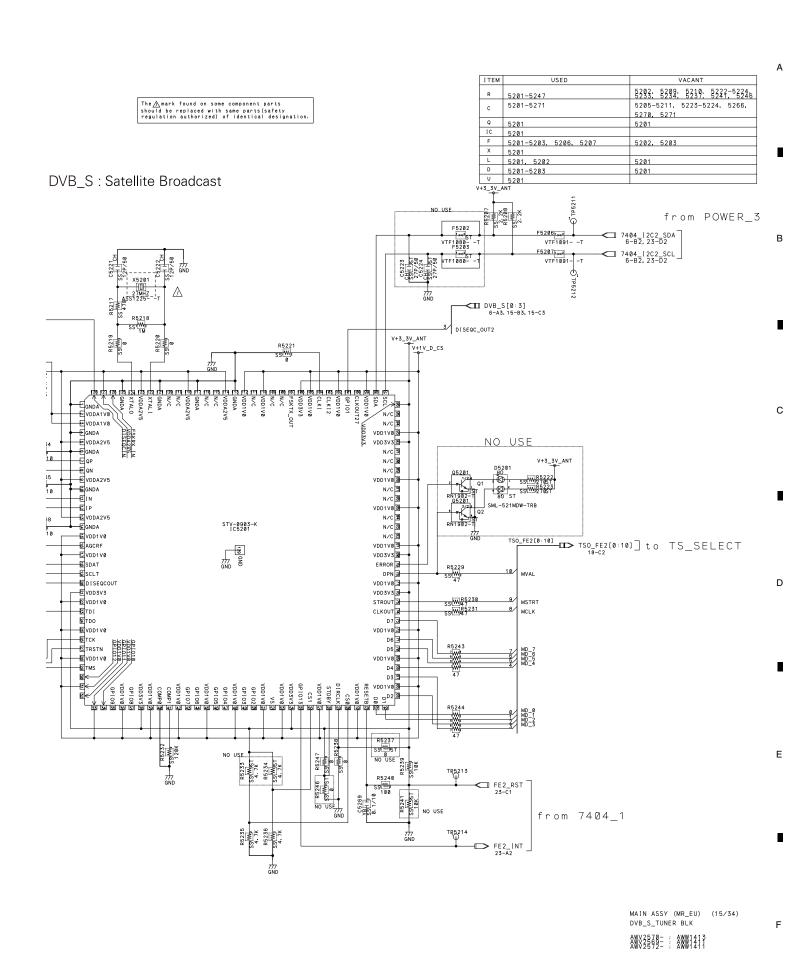




10.16 MAIN BLOCK ASSY (15/33) [DVB_S_TUNER BLOCK]



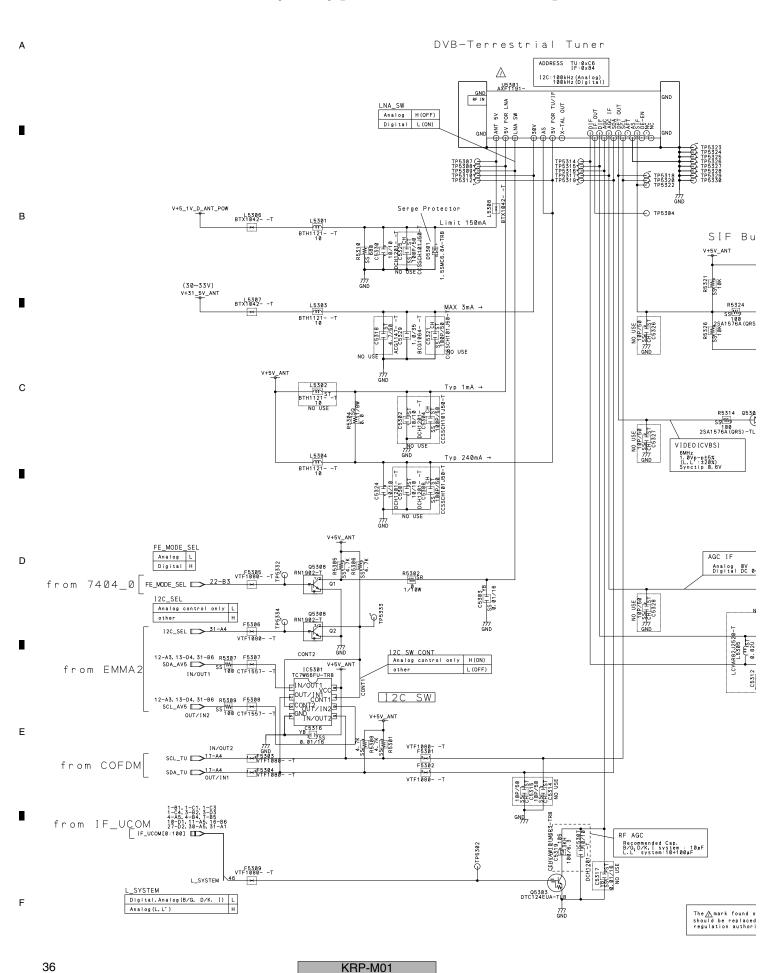
34

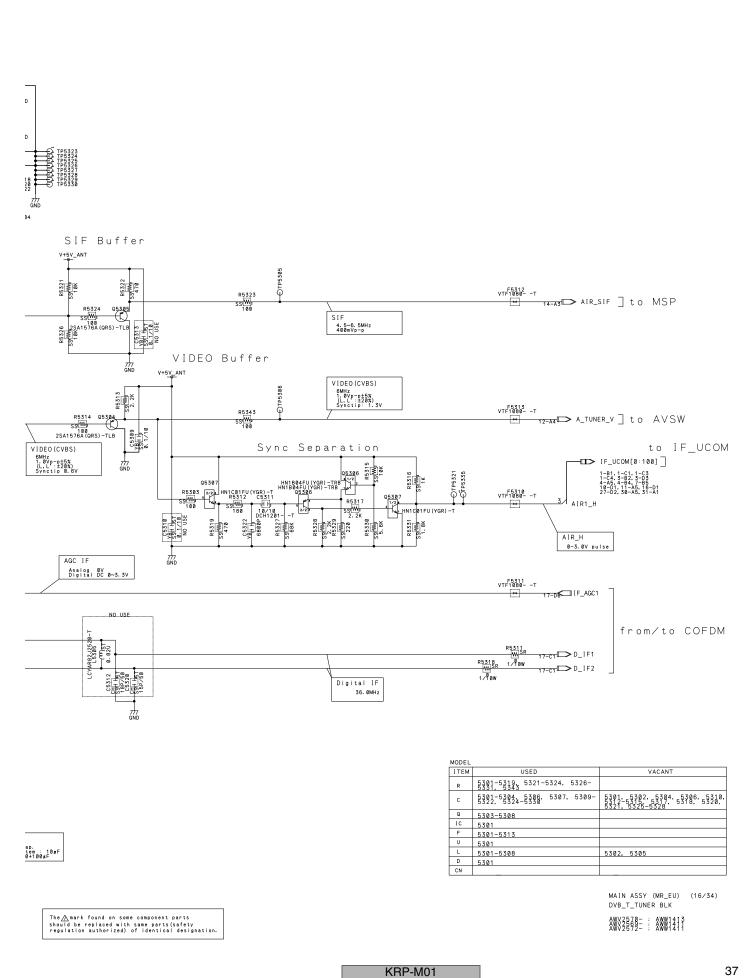


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10.17 MAIN BLOCK ASSY (16/33) [DVB_T_TUNER BLOCK]





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10.18 MAIN BLOCK ASSY (17/33) [COFDM BLOCK]

'_D V+1_8V_D_COF L5403 BTX1042- -T V+3_3V_A3_COF R5408 SSIW-PS TP5402 🔾 V+1_8V_D_COF from DVB-T_TUNER D_IF2 ________16-C6 C5421 YB SS 1 1 YB 1000P/50 C5407 YB SS 1 1 ST 0. 1/10 ₽ TSSA E HADDY BTX1042-COFDM V+1_8V_D_COF - BHASSV - BNN I L5402 BTX1042- -T IC5401 DRX3975D-QI-B1-K - TAGGY - Taggy A8U2 € 2∃9

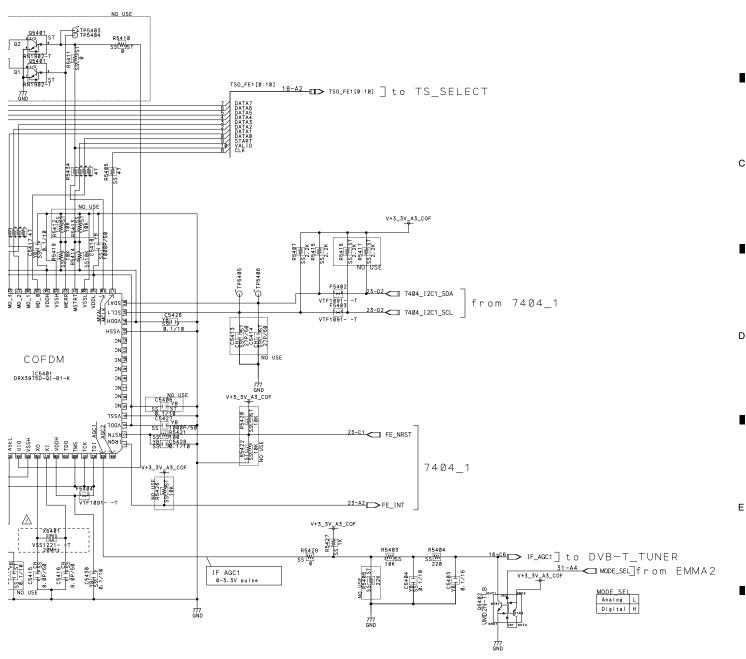
The Amark found on some component should be replaced with same parts regulation authorized) of identica

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MODEL

	ITEM	USED	VACANT
	R	5425-54283-5437-5436,9-5438	5406, 5408-5413, 5416 5417.
	С	5401-5430	5405, 5406, 5407, 5410, 5413, 5414
	Q	5401, 5402	5401
	I C	5401	
16-D1 C C TU	F	5402-5404	
to DVB-T_TUNER	Х	5401	
SDA_TU CO DVB-1_TONER	L	5401-5404	
3DA_10 _	D	5401	5401
	CN		



MAIN ASSY (MR_EU) (17/34) COFDM BLOCK

AWV2578- : AWW1413

found on some component parts
replaced with same parts(safety
n authorized) of identical designation.

KRP-M01

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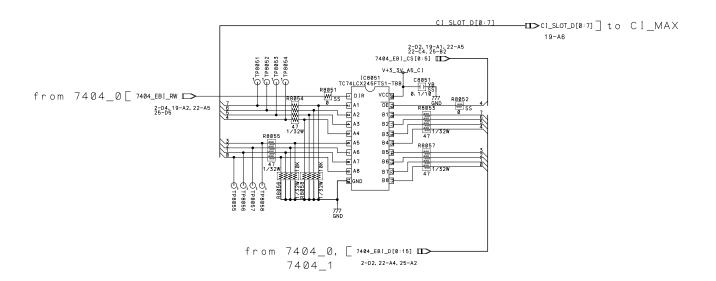
В

10.19 MAIN BLOCK ASSY (18/33) [TS_SELECT BLOCK]

 FE_TS_SEL : Select DVB-T or DVB-S tuner DVB-S/S2 DVB-T TP8001 V+3_3V_A5_CI from BOARD_IF1 [TSO_FE1[0:10] [D 17-B5 TSO_FE1[0:10]] DVB-T TC74LCX157FTS1-TBB TSO_SEL[0:10] TSO_SEL[0:10]]t o 750 RAB4CQ151J-T 1/32W TC74LCX157FTS1-TBB from 7404_ 750 RAB4CQ151J-T 1/32W V+3_3V_A5_CI MVAL MSTRT MCLK from DVB_S_TUNER [TSO_FE2[0:10] ID TSO_FE2[0:10] DVB-S/S2

40

SO_SEL[0:10]]to CIMAX



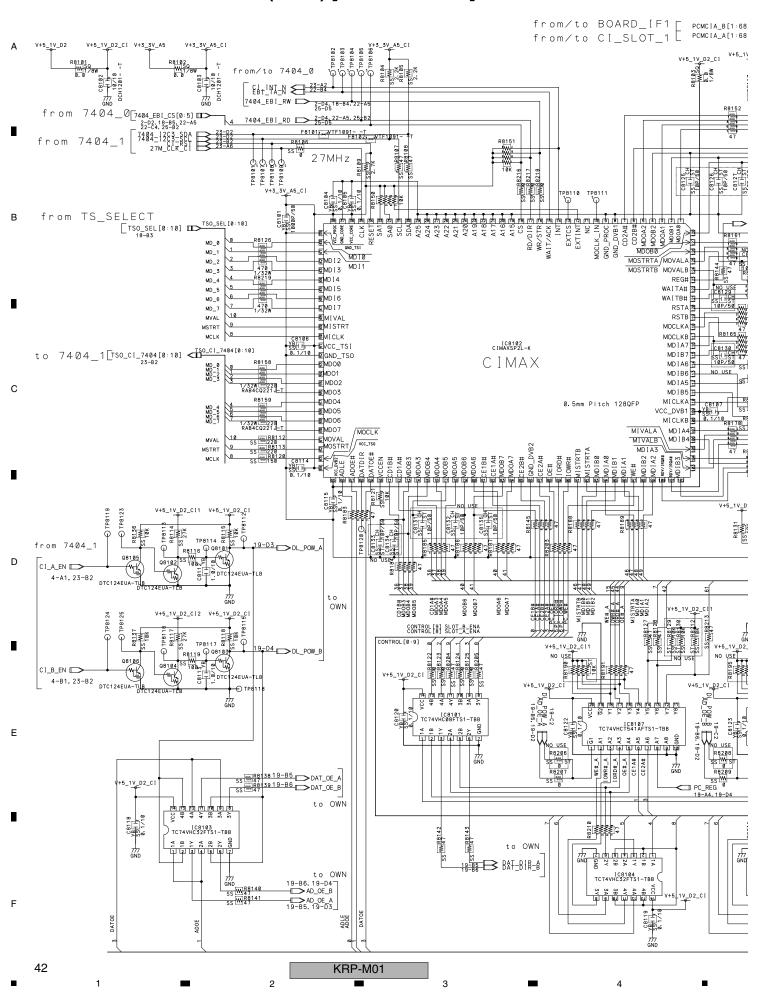
MODEL		
ITEM	USED	VACANT
R	8001-8006, 8051-8058	
c	8001-8003, 8051	
Q		
10	8001-8003, 8051	
F		
х		
L		
D		
CN		

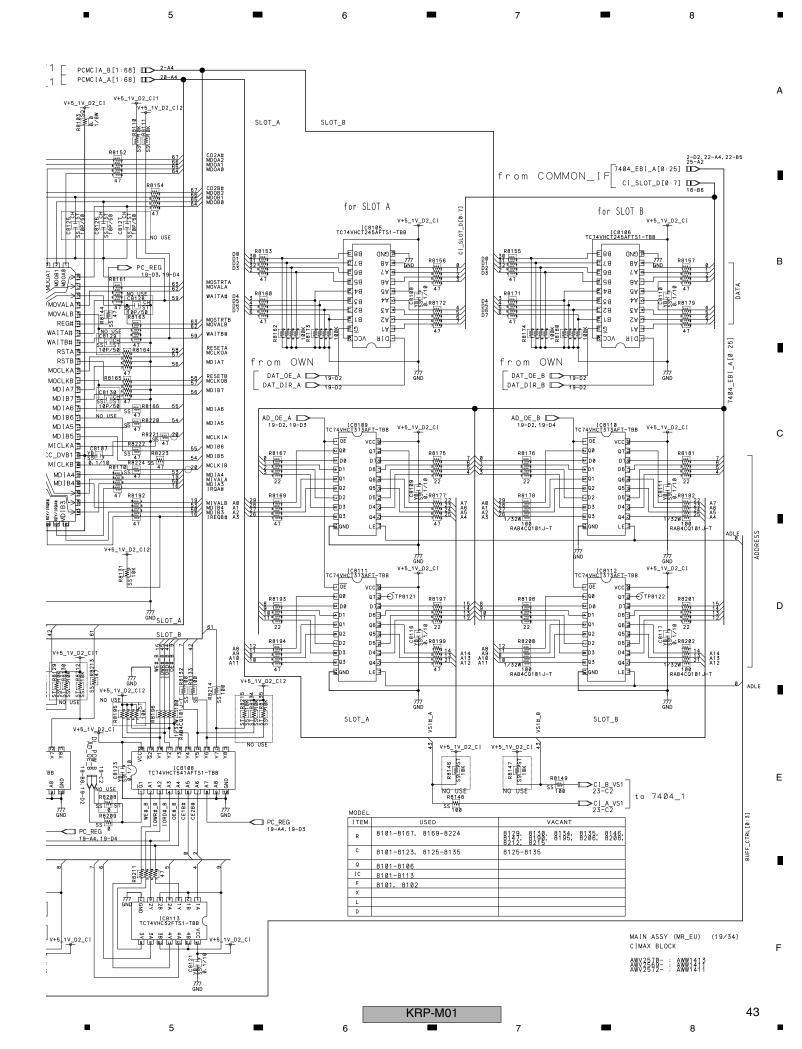
MAIN ASSY (MR_EU) (18/34) TS_SELECT BLOCK Е

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AWV2578- : AWW1413

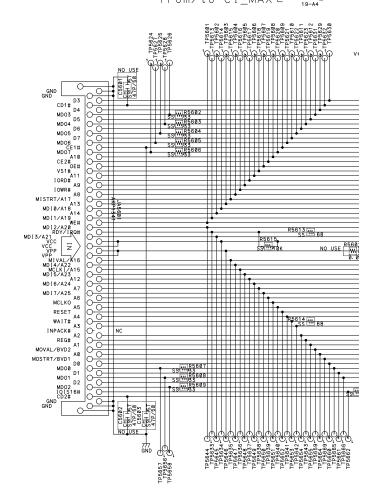
10.20 MAIN BLOCK ASSY (19/33) [CIMAX BLOCK]





from/to CI_MAX [PCMCIA_A[1:68] [

COMMON INTERFACE1 (TERRESTRIAL)



4

PCMC[A_A[1:68] PCMCIA_A[1:68] V+5_1<u>V_D</u>2_C11 R5613 W 68 NO USE | R5681 | W | ST | 0. 01 / 8W

> 5601-5615 5601, 5612 5601-5605 5601-5603 Q IC F 5601 5601 5601

> > MAIN ASSY (MR_EU) (20/34) CI_CARD_1 BLOCK

8

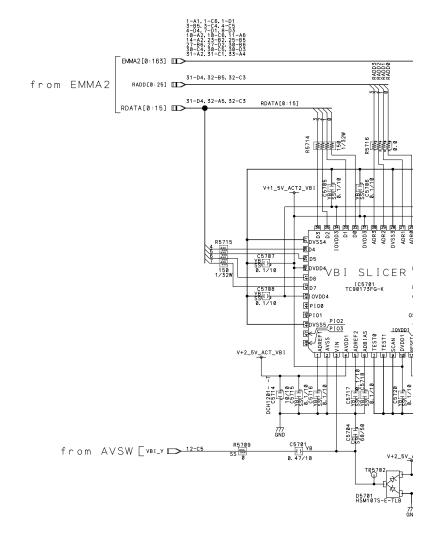
В

Е

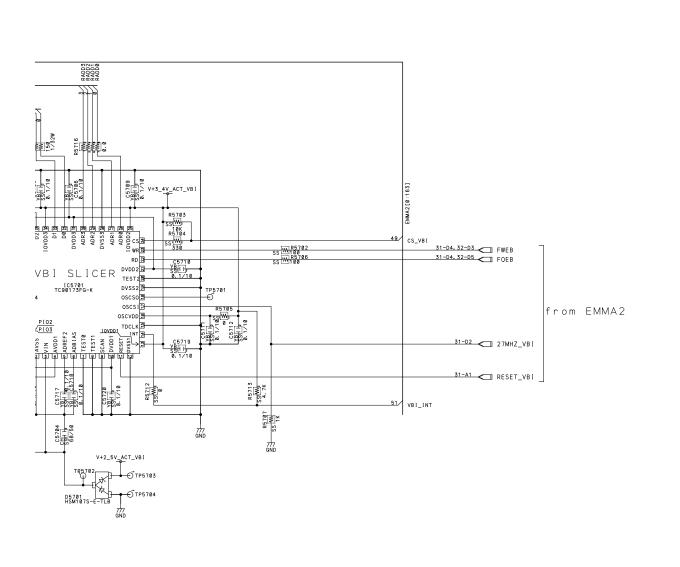
45

AWV2570- : AWW1413 AWV2572- : AWW1411

10.22 MAIN BLOCK ASSY (21/33) [VBI_SLICER BLOCK]



46



> MAIN ASSY (MR_EU) (21/34) VBI_SLICER BLOCK

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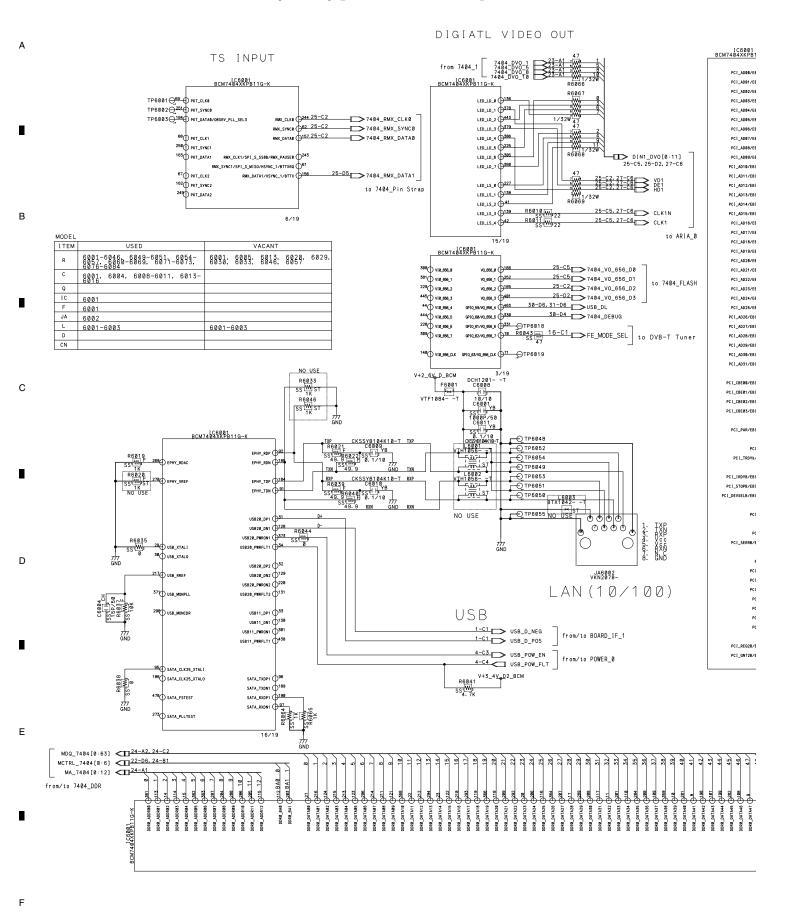
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AWY2578- AWW1413

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10.23 MAIN BLOCK ASSY (22/33) [7404_0 BLOCK]



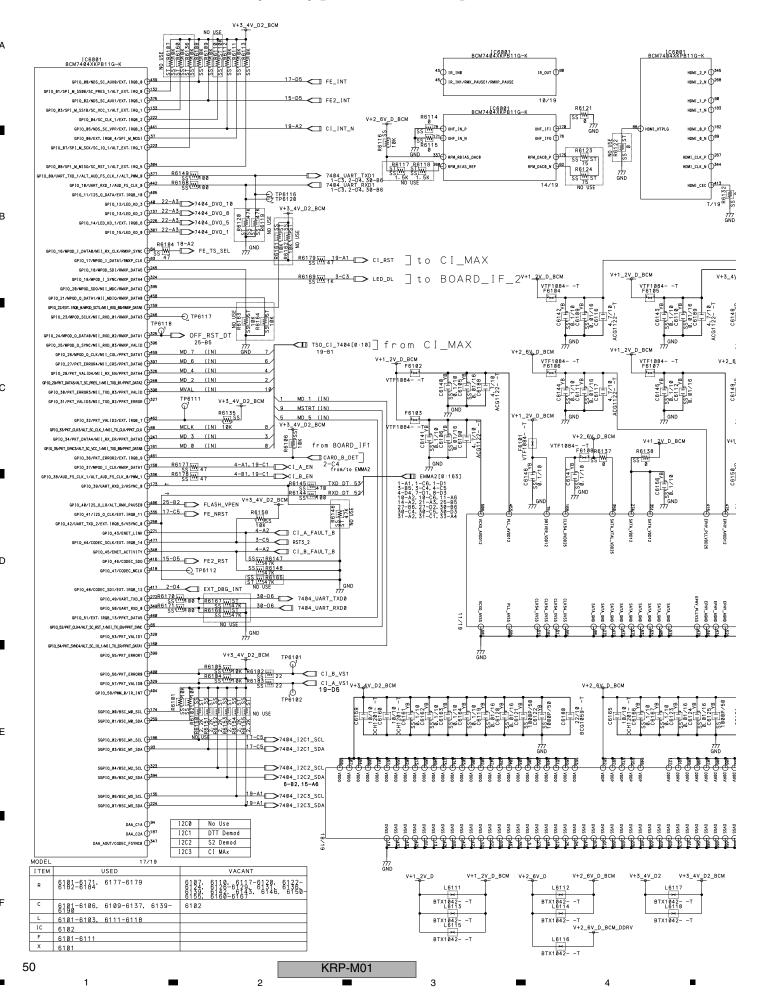
48

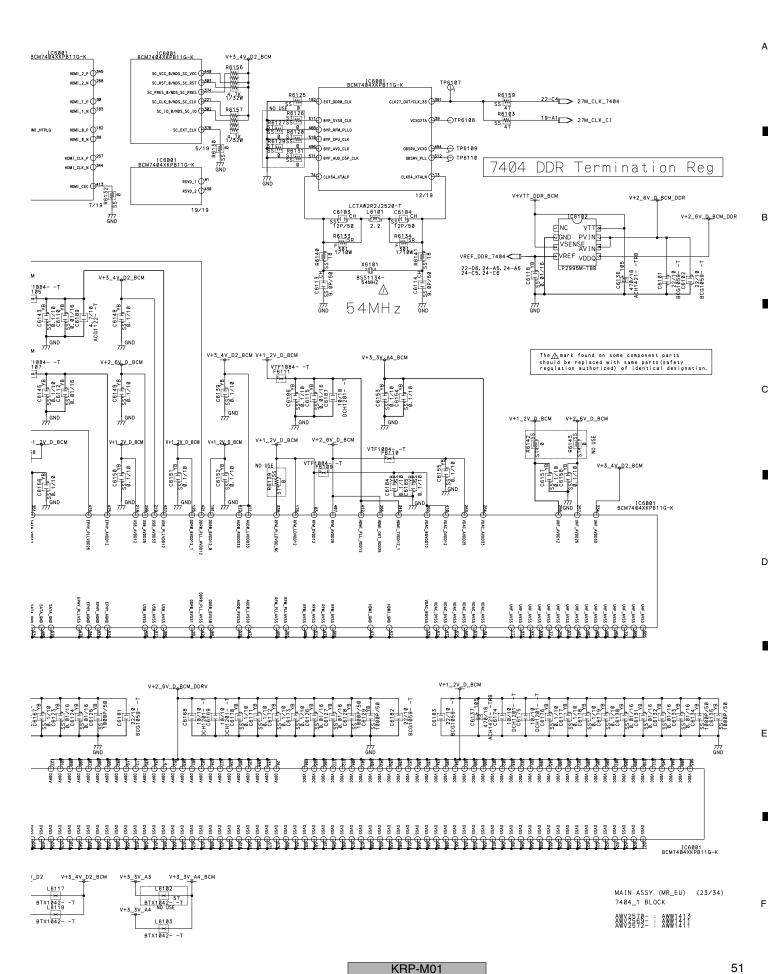
EBI BUS IC6001 BCM7404XKPB11G-K -[ID 7484_EBI_D[0:15] 2-D2, 18-C5, 25-A2 -[ID 7484_EBI_A[0:25] 2-D2, 19-A6, 22-B5 25-A2 22-C4 ANALOG VIDEO OUT 2-D2, 18-B5, 19-A1 22-C4, 25-B2 7404_EBI_CS[0:5] V+3_3<u>V_A</u>4_BCM LC6001 BCM7404XKPB11G-K 8 8 8 8 to 7404_FLASH PCI_ADBB/EBI_DATAB EBI CS86 V+3_4V_D2_8CM
R6812 NO USE
2 SS W R6813
3 4. 7k ST W R6813
2-D4_18-B4, 19-A2
25-D5 7484_EB _ RW EBI_CS16 0155 11G-K NGCNONG4E 9 9 9 9 VDAC8_8 179 2 2 2 2 2 BCM7404XKPB11G-K :11] 7-C6 -⊖_{TP6006} EBI_CS3B/PCI_GNT3B VDAC8_1 085 R6036 2-D4,18-B4,19-A2
EBI_R00 154 SS-W14 25-D54 7404_EBI_RW
EBI_RE0 353 SW1 2-D4, 25-B2 7404_EBI_RW
EBI_RE0 353 SW1 2-D4, 25-B2 7404_EBI_WE0 -O_{TP6007} R6018:WISQ 12-A4 0.01.17 TP6047 DT_MON_CVBS to BOARD_IF_0 EBI_RDB 000 R6042
EBI_RDB 0528 EW947
EBI_TSIZE8B 0593 STP6040 R6017 VDAC1_1 0 178 ⊕_{TP6042} SS 560 25-D5 7404_EBI_TSIZE0 25-D5 7404_EBI_TSIZE1 В VDAC1_2 084 ⊕_{TP6043} EBI_TSIZE1B o ARIA_0 25-D5 7404_EBI_TS PCI_AD18/EBI_ADDR2 4/19 25-D5 7404_EBI_DS /404_EBI_DS 7404_EBI_A[0:25] 2-D2,19-A6, 22-A4 25-A2 BCM7404XKPB11G-K R6077 EBI_ADDR24 0321 WM 24 SS W 100 NO USE V+3_4V_D2_BCM AUDB_LEFT_P 087 AUDIO OUT EBI_ADDR25 | 242 | W | 25 SS | W | 100 R6078 AUDR_LEFT_N 0180 04_FLASH SS1W2ST R6029 W ST SS1.7K R6030 W2ST SST 4. 7K 3 4V D2 BCM AUDØ_RIGHT_N 0^{342} SPDIF_DTV_CI R6023 HE-AAC V+3_4V_D2_BCM R6024 SS1W2 4.7K R6031 [Tuner 128_CLK/ALT_125_0_CLK \$\frac{158.0625}{58.0626} \frac{14-81}{14-81} \frac{1}{125_BCLK_DTV} \\
128_BMAWAIT_125_0_BATA \$\frac{158.0625}{58.0626} \frac{14-81}{14-81} \frac{1}{125_LRCLK_DTV} \\
128_LRWAIT_125_0_BATA \$\frac{158.0625}{58.0626} \frac{14-81}{14-81} \frac{1}{125_LRCLK_DTV} \\
128_LRCLK_DTV MSP RESET_OUTB \$\int_{167}^{167}\$ TP6020 PCI_CBE80/EBI_ADDR16 2-D4.25-B5 RESET_7404] from 7404_FLASH V+3_4V_D2_BCM for Debug PCI_PAR/EBI_ADDR28 | 313 | SS | W| | 1-C3 7404_TRST 1-C3 7404_JTDI 1-C3 7404_JTD0 BOARD_IF_0 1-C3 7404_TMS 1-C3 7404_TCK 19-A2 EBI_TA_N PCI_TRDYb/EBI_TAB V+3_4<u>V_D</u>2_BCM PCI_IRDYB/EBI_ADDR21 PCI_STOPB/EBI_ADDR22 23 1/32W BCM7404XKPB11G-K PCI_DEVSELB/EBI_ADDR23 8 ******** 586 [W]1/32W 100 RAB4CQ101J-T EJTAL_TISTS TO SEE TO THE STATE 498 TMODE_8 23-A6 27M_CLK_7404 V+3_4V_D2_BCM 499 TMODE_1 R6849
PCI_PERRB 0148 WY 2 R6050
SST.W2 R6050
SST.W2 R6050
SST.W2 R6050
SST.W2 4.7K 501 TMODE_2 500 TMODE_3 47 TP6038 EJTAG_TCK/OBSRV_PLL_SEL8 253 BSC_S_SDA/SPI_S_MOSI V+3_4<u>V_D</u>2_BCM PCI_INT_A8 48 W 1/32
PCI_INT_A1 452 4. 7K —— TP6045 EJTAG —∩ TP6039 —— TP6046 from BOARD_IF_0 BBS_SCL 1-C3 -€ TP6044 for Debug PCI_GNT88 0447 BBS_SDA □>1-C3 R6056 NO USE 1 2 BCM BBS R6006 PCI_REQ28/EBI_CS48 357 W14 PCI_GNT28/EBI_CS58 152 475 SS*W2 R6057 4. 7K R6057 4. 7K for Debug 7404_EBI_CS[0:5] to BOARD_IF_0 from/to 7404_DDR 1/19 2-D2, 18-B5, 19-A1 22-A5, 25-B2 24-D1 MDM_7404[0:7] 24-C1 MDQS_7404[0:7] 24-C1 MCLK0 7404 24-C1 MCLK0B 7404 Е 24-C1 MCLK1_7404 24-C1 MCLK1_7404 CASB CKE WEB 22-D1, 24-B1 MCTRL_7404[0:6] 23-B6, 24-A5, 24-A6 24-C5, 24-C6 VREF_DDR_7404 DDR8_DATA56 (
DDR8_DATA57 (
DDR8_DATA58 (
DDR8_DATA59 (DDRB_DNA (
DDRB_DNA (
DDRB_DNA (
DDRB_DNA (
DDRB_DNA (
DDRB_DNA (
DDRB_DNA (
DDRB_DNB (DDR8_DQS6 (
DDR8_DQS1 (
DDR8_DQS2 (
DDR8_DQS4 (
DDR8_DQS5 (
DDR8_DQS5 (
DDR8_DQS6 (C6013 YB SS! 12 0.1/10 C6014 SSI H 0. 1/10 MAIN ASSY (MR_EU) (22/34) 7404_0 BLOCK AWY2578- : AWW1411

KRP-M01

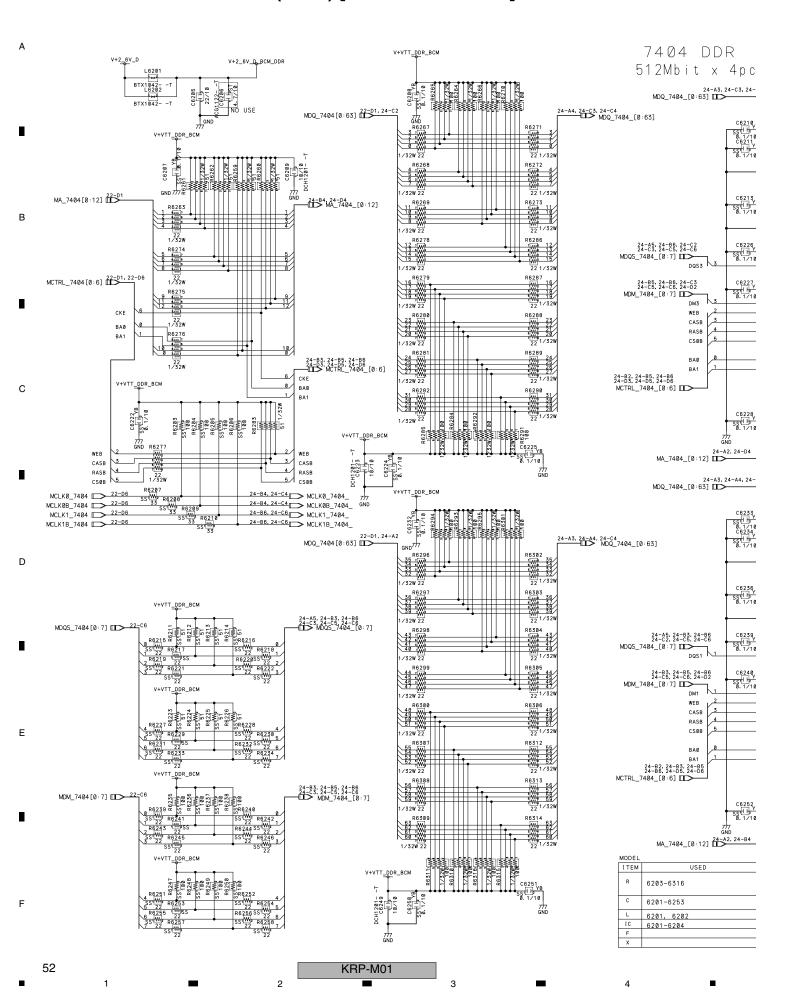
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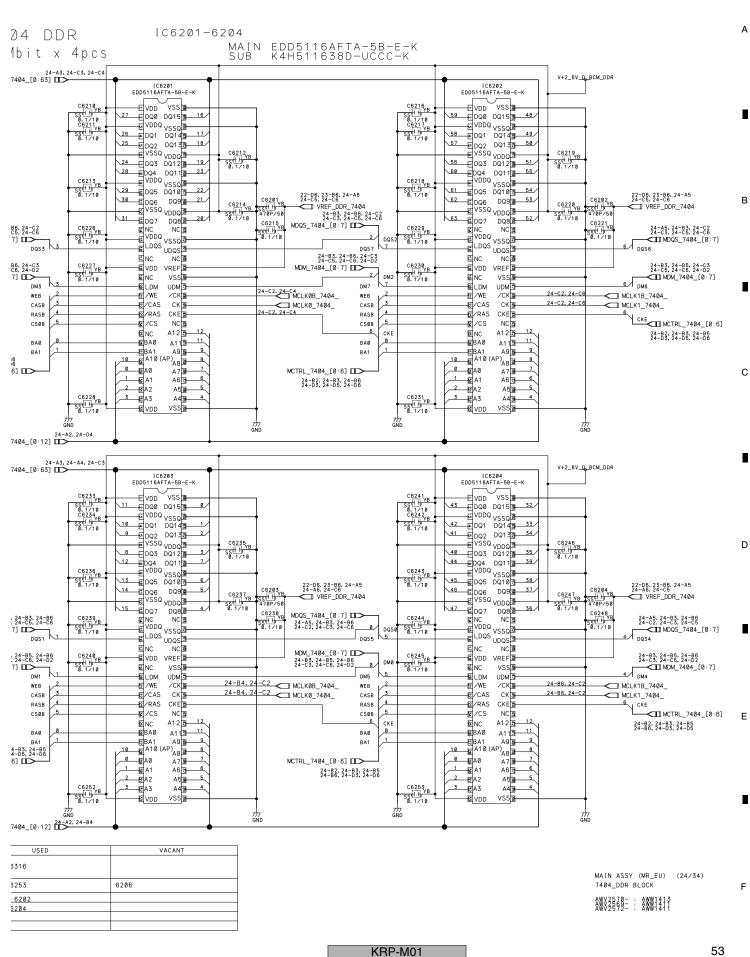
10.24 MAIN BLOCK ASSY (23/33) [7404_1 BLOCK]



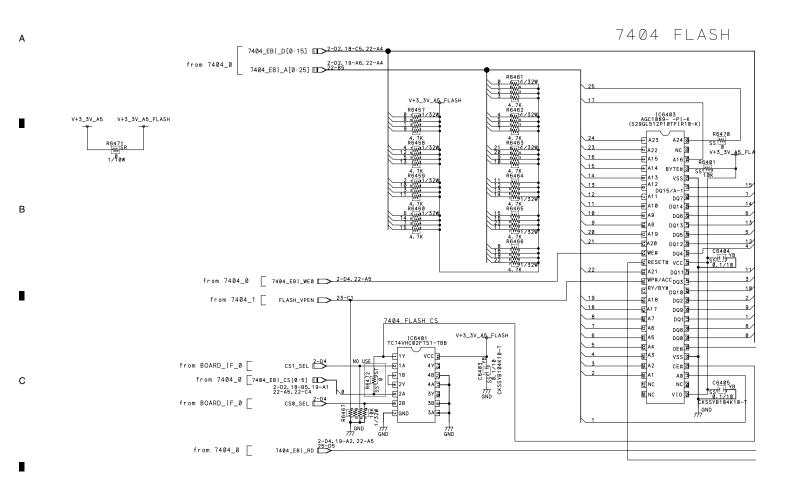


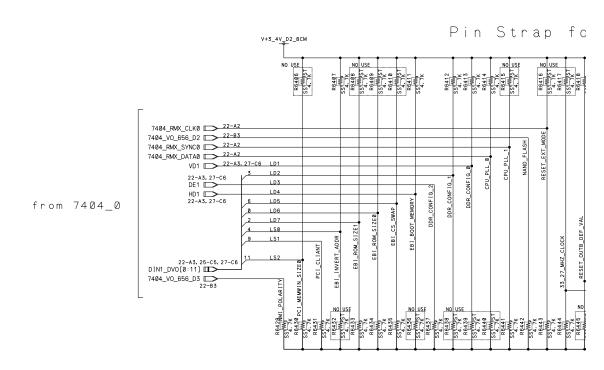
10.25 MAIN BLOCK ASSY (24/33) [7404_DDR BLOCK]





10.26 MAIN BLOCK ASSY (25/33) [7404_FLASH BLOCK]

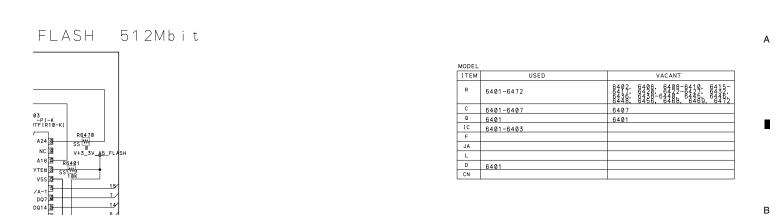


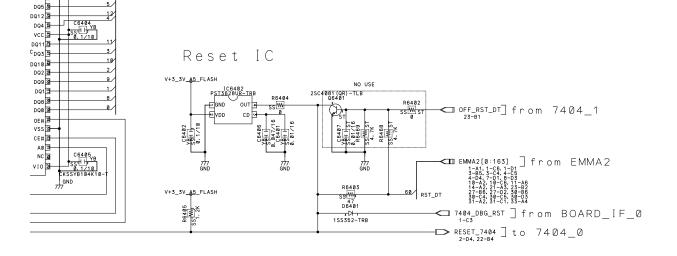


54

KRP-M01

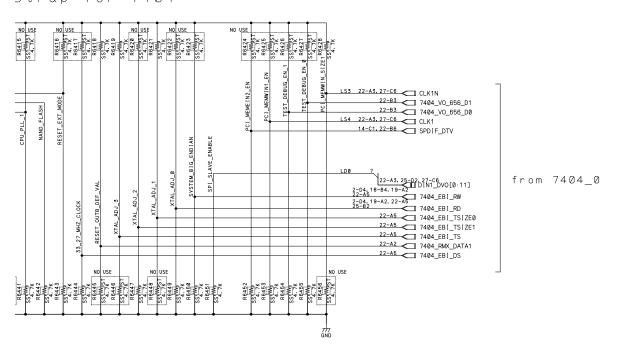
2





Strap for 7404

DQ13 5



MAIN ASSY (MR_EU) (25/34) 7404_FLASH BLOCK

AWV2578- : AWW1413

KRP-M01

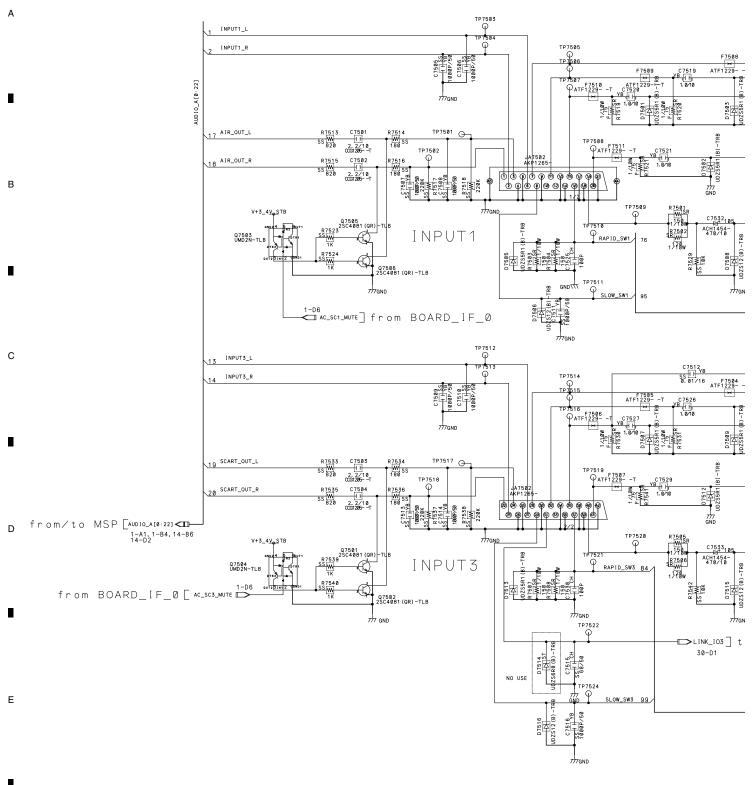
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D

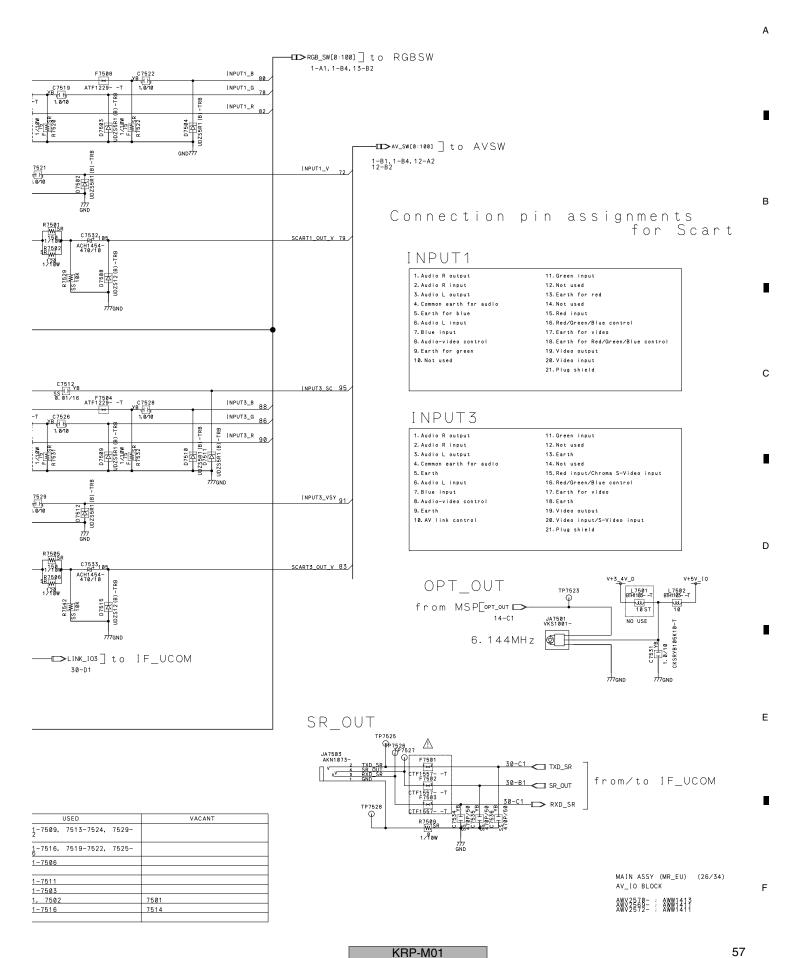
Ε

10.27 MAIN BLOCK ASSY (26/33) [AV_IO BLOCK]

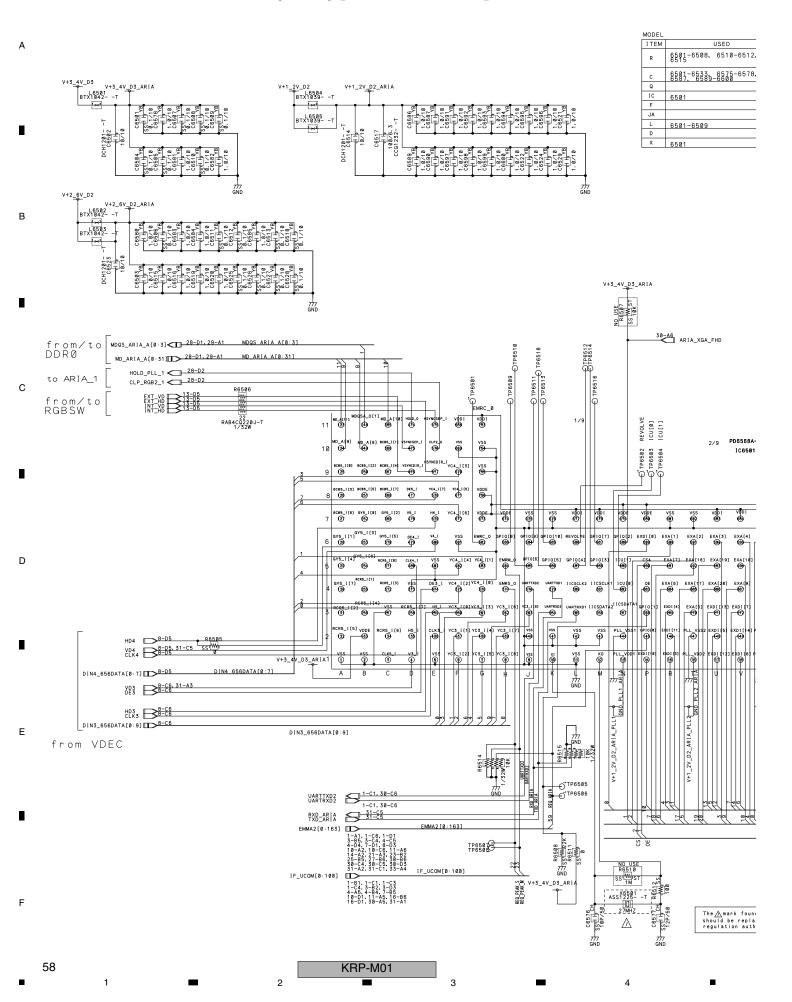


ITEM	9	JSED	
R	7501-7509. 7542	7513-7524,	75
С	7501-7516, 7536	7519-7522,	7.5
Q	7501-7506		
IC			
F	7501-7511		
JA	7501-7503		
L	7501, 7502		
D	7501-7516		
Х			

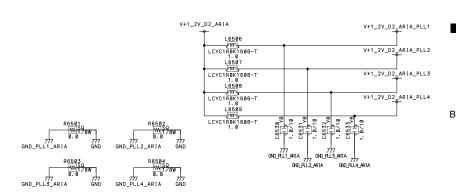
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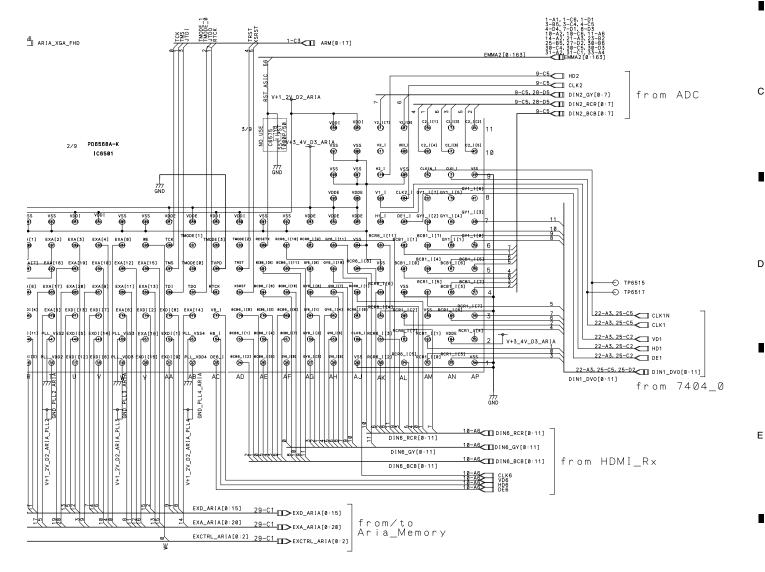


10.28 MAIN BLOCK ASSY (27/33) [ARIA_0 BLOCK]



USED	VACANT
6501-6508, 6510-6512, 6514,	6507, 6510
6501-6533, 6575-6578, 6580- 6587, 6589-6600	6575
6501	
6501-6509	
6501	





KRP-M01

MAIN ASSY (MR_EU) (27/34) ARIA_0 BLOCK

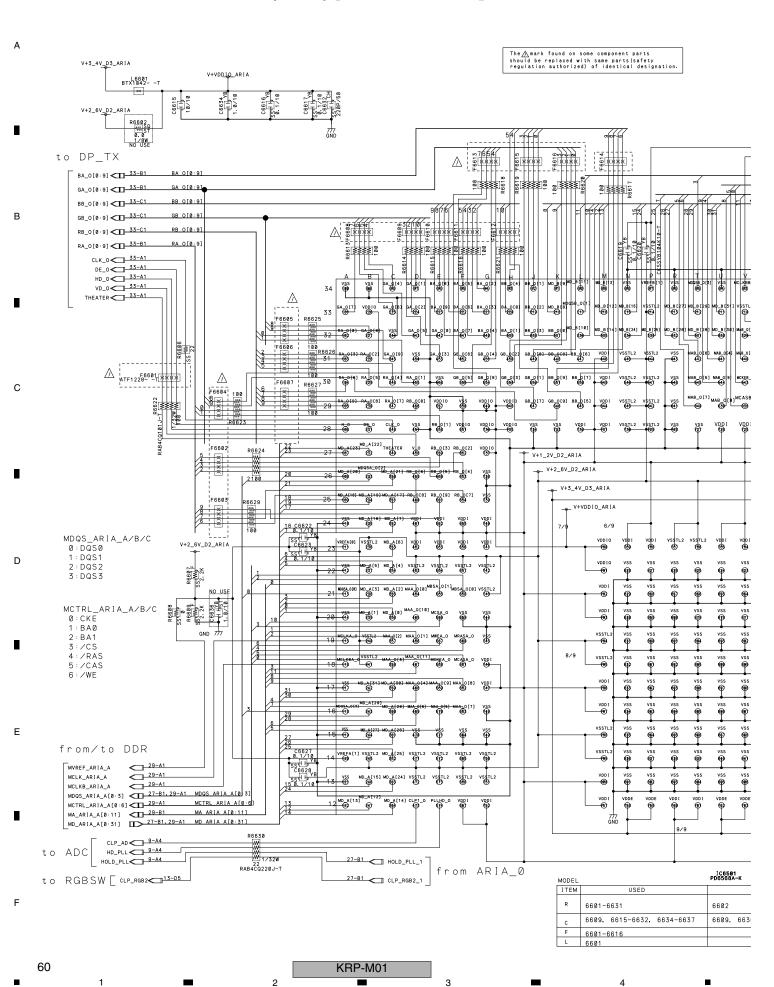
8

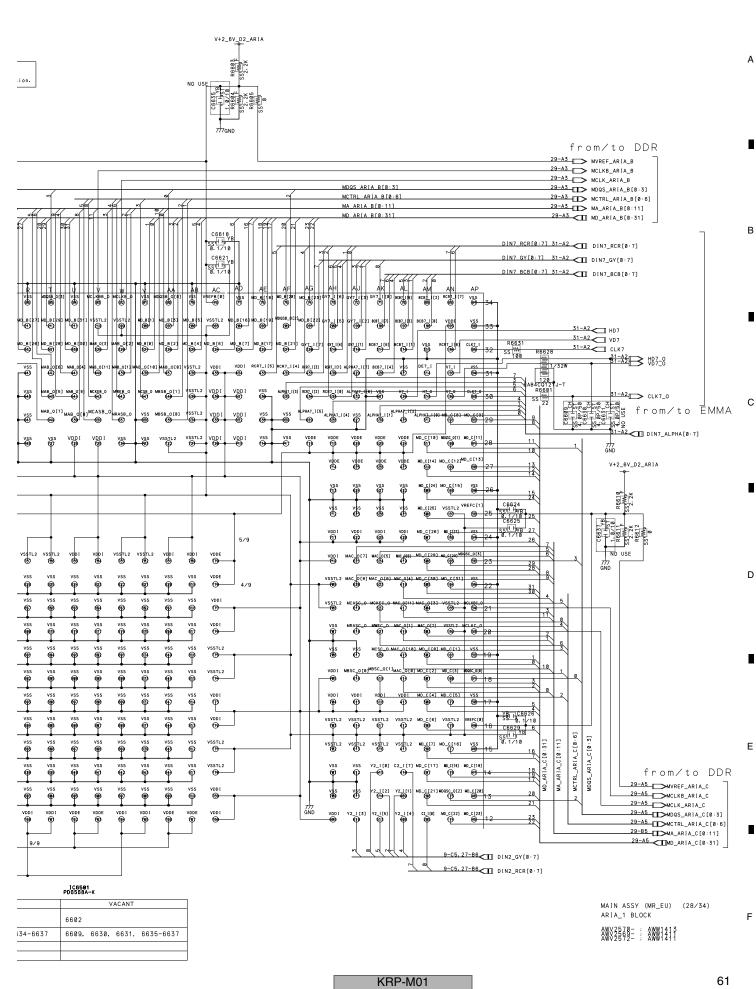
AWV2578- : AWW1413 AWV2572- : AWW1411

The ∧ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.

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10.29 MAIN BLOCK ASSY (28/33) [ARIA_1 BLOCK]

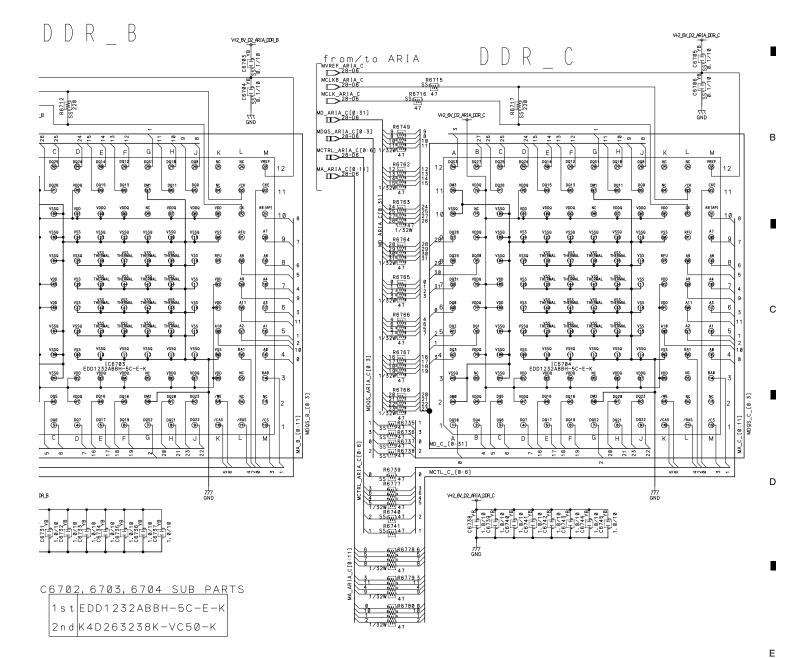




10.30 MAIN BLOCK ASSY (29/33) [ARIA_DDR BLOCK]

ARIA DDR D D R _ A D D R V+2_6V_D2_ARIA_DDR_A from/to ARIA from/to ARIA 777 GND V+2_6V_D2_ARIA_DDR_B R6747 9 W 4 8 W 1 W 1/32W MDQS_ARIA_B[0:3] MDQS_ARIA_A[0:3] V+2_6V_D2_ARIA_DOR_A MCTRL_ARIA_B[0:6] MCTRL_ARIA_A[0:6] 471/32 MA_ARIA_B [0:11 28-A6 D R6748 12 | 47 13 | W 143 | W 15 | W 1 | 1/3 2 W в E c` R6746 12 JW 13 W 143 W 156 W 471/32W A_ARIA_A[0:1] DQS3 1024 в D н **~~** 1025 D014 D01 ⊗8 c) E G VREF SS DQS3 1027 1007 1025 SS 1014 1004 **100 28** ×S ⊗ NC ⊗ 12 DM3 VDDQ DQ26 R6750 24 W 25 W 278 W 26 W 471/32 DM3 VDDQ (9) ∕¢**×** CKE 0026 DQ11 8 vssq. vsso @ NC 139 vsso. ***** AB (AP) @D_ **™** 10 VSSQ 1028 (P) V0000 D028 (P) VDDQ VSSQ vssq P RFU (97) 9 vssq (B) MA_ARIA_B[0:11]
MCTRL_ARIA_B[0:6]
MDGS_ARIA_B[0: R6752 46 19 ∖30 8 VDDQ VDD (B) 31 7 V00 vss D VDDQ (G) **14** 7 ₩ ₩ VDDQ (99) VDD GGV R6753 98 98 V00 VDDQ G vss P **43** 6 001 99 VSSQ VSSQ (B) vss (P **₩ å** VDDQ 199 VSSQ (B) EDD1232ABBH-VDDQ VDD VDDQ (S) VSSQ (B) BA1 \$6 **18** vss @ 4 NC 669 VDD 100 VSSQ vssq (1) V... R6761 20 JW 4 IC6702 EDD1232ABBH-5C VDD (VDDQ VDDQ WD (WDQ R6755 vsso NC ®® vssq VDDQ (S) NC 659 BA8 D01 DQ16 DQ5 V000 D028 DQ23 NC 699 NC 139 471/32W 001 (6 1004 (2) 006 3 0017 (B) 1 R6720 MA_A_[0:11] MDQS_A_[0:31 SS W R6729 SS W R6729 SS W R6730 D017 DQS2 DQ22 /CAS /RAS 106 (3) 007 ⊕ D021 /ርՏ ሙ 3 R6721 D04 0 R6722 SS P47 2 R6723 MCTL_B_[0:6] MCTL_A_[0:6] V+2_6V_D2_ARIA_DDR_B 47 R6769 V+2_6V_D2_ARIA_DDR_A 4 Ws 5 Ws 1/32W 47 2 SSEW R6725 1.0/ 1R6734 47 1 SS#WiR6726 R6774 47 V+3_4V_D3_ARIA 47 1/32W EXD_ARIA[0:15] 27-D4 IC<u>6702, 6703,</u> #WiR6771 3 AGC1088- -PI-K (S29GL016A90TF1R2-K) [C6701 47 1/32W EXA_ARIA[0:20] 27-D4 1 s t EDD123 FA15 A16 28 R6772 0 EXCTRL ARIA[0:2]27-D4 14 BYTE# 2nd K4D263 --⊡A14 _13_ from ARIA 12 .11 DQ7B 10 RIA V+2,6V,D2,4RIA,DDRC V+2,6V,D2,4RIA,DDRA V+2,6V,D2,4RIA,DDRB BTX1,8,42- -T BTX1,8,42- -T 14 V+2_6V_D2_AR1A 9 DOB 5 8 13, DQ133 - **Б** А8 L6702 BTX1042- -T 19 DQ5 20 R6743 ENC SSIW#0 EWE# DCH1201- -T C6714 H 10/10 C6707 H SS 1 H 0. 1/10 DQ12 L6703 BTX1042- -T DQ4 R8727
W/S R8744 ENC D0113
18K W/SS EWPH/ACC D033
18(51) ER7849
00 USE EA18 D023 10 17 DQ9 DQ1 DQ8 28 - B A5 4 VSS 13 cs CE# ARIA FLASH MA2 EXA_AR[A[0:20] EXCTRL ARIA[0:2] 62 KRP-M01

RIA DDR



2_ARIA_DOR_A	V+2_6V_D2_ARIA_DDR_B
-	-
10/10 DCH1201-	C6712 II H 10/10 DCH1281-
•	777 GND

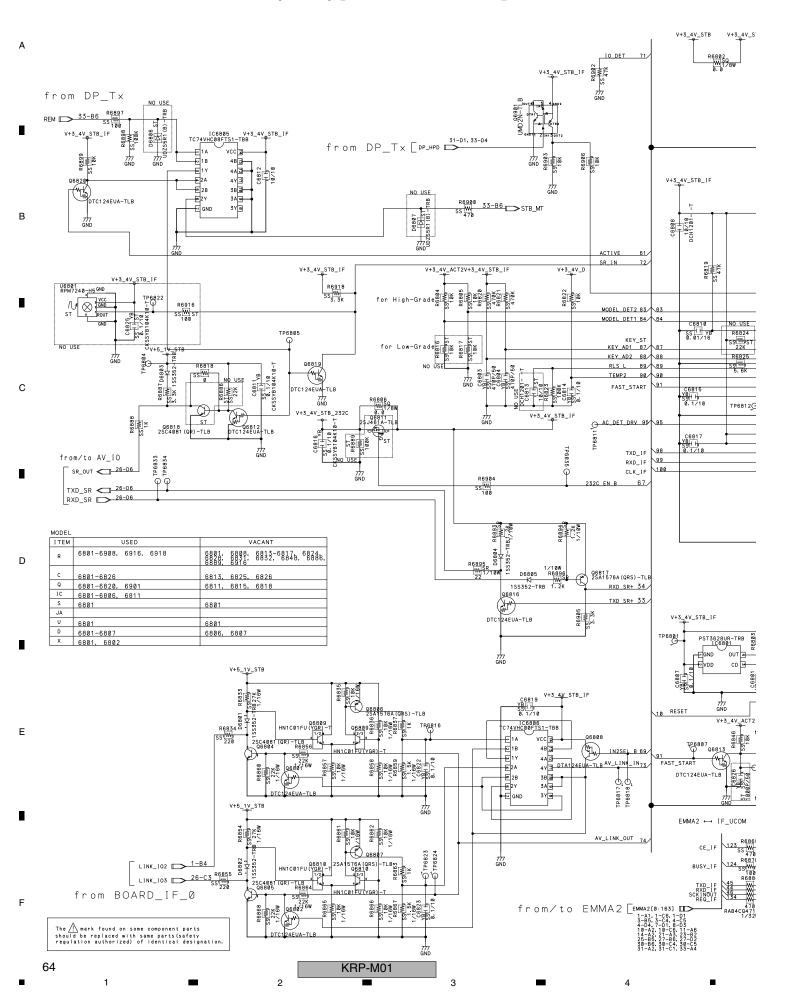
MODEL		
ITEM	USED	VACANT
R	8795,-6792b-8749,-6743-8785-	6744
С	6701-6708, 6710, 6712, 6714, 6718, 6722-6745	
10	6701-6704	
L	6701-6703	

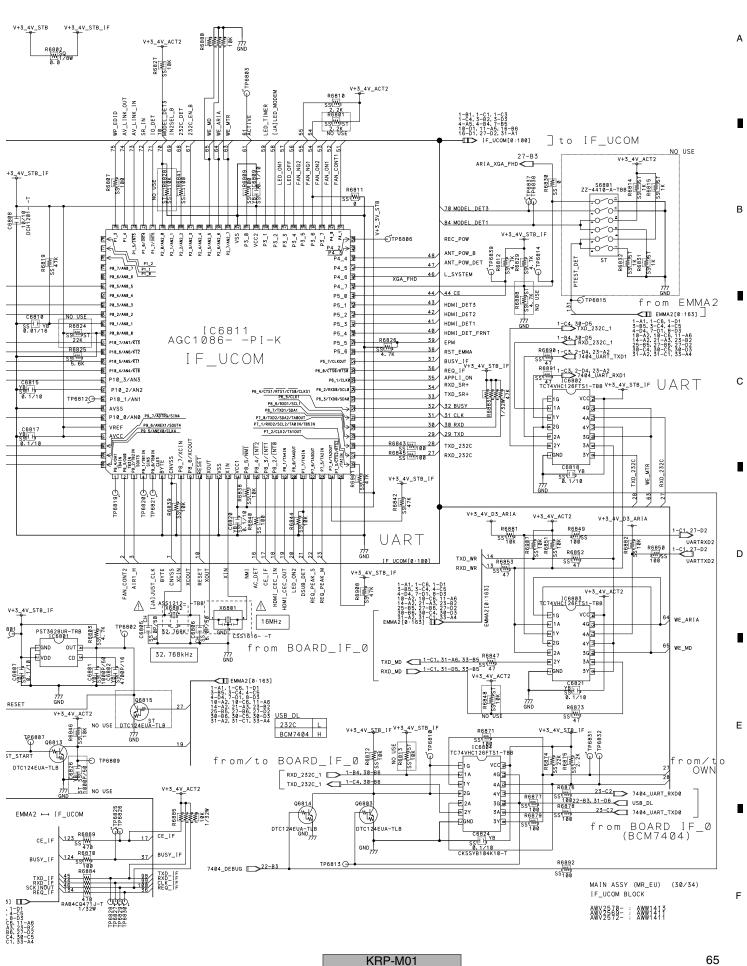
MAIN ASSY (MR_EU) (29/34) ARIA_DDR BLOCK

AWV2578- : AWW1413 AWV2572- : AWW1411

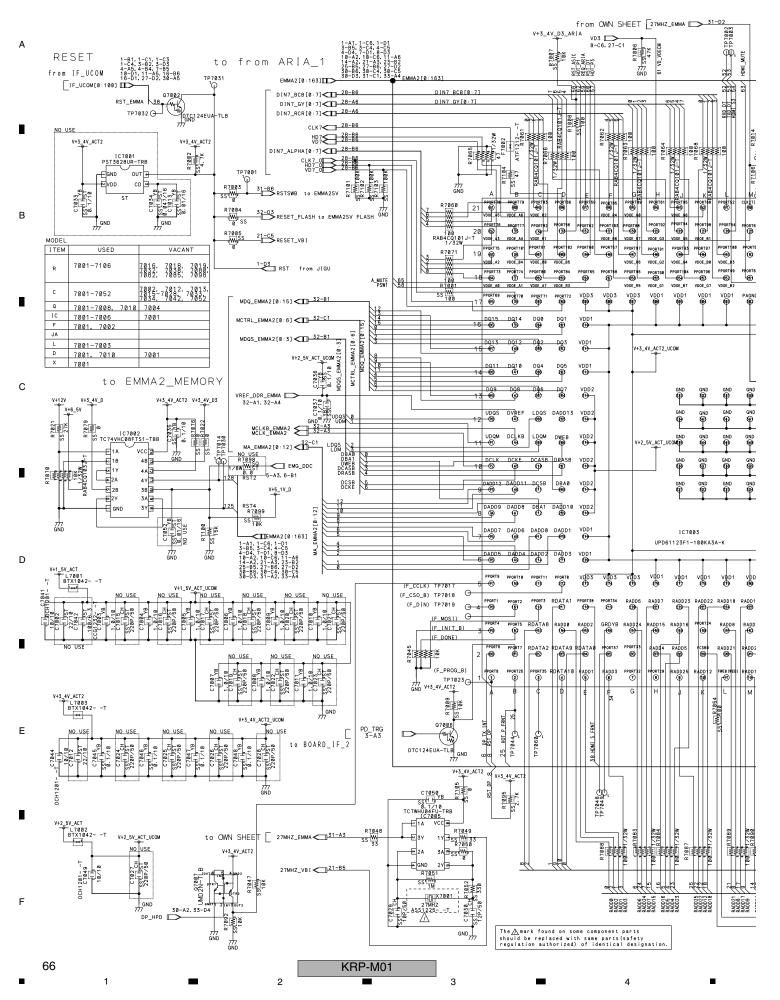
KRP-M01 63

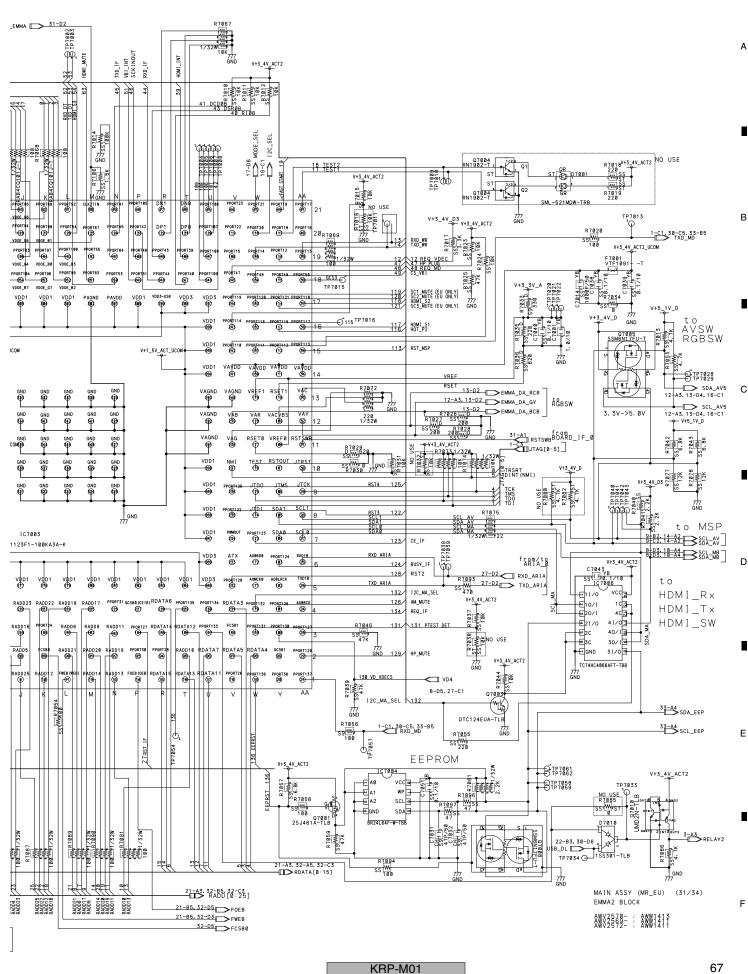
10.31 MAIN BLOCK ASSY (30/33) [IF_UCOM BLOCK]





10.32 MAIN BLOCK ASSY (31/33) [EMMA2 BLOCK]





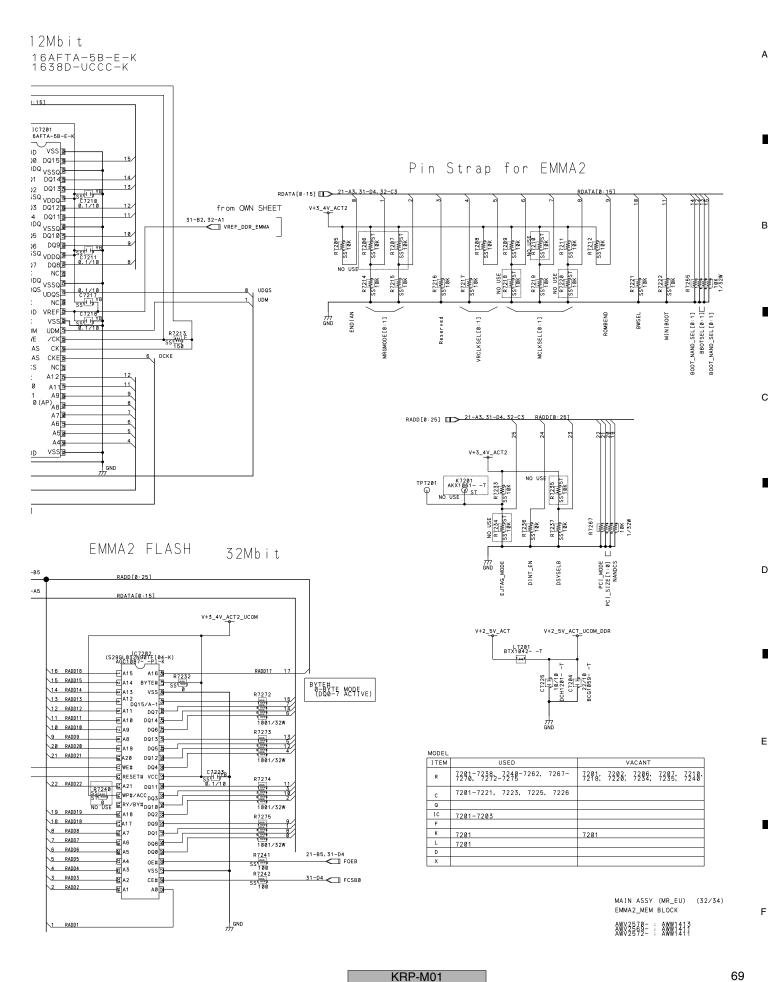
10.33 MAIN BLOCK ASSY (32/33) [EMMA2_MEM BLOCK]

EMMA2 DDR (UMI) 512Mbit MAIN EDD5116AFTA-5B-E SUB K4H511638D-UCCC-SSIW#10 SSIW#10 SSIW#10 MCLK_EMMA2 31-B2 EMMA2 DDR Termination Reg MCLKB_EMMA2 31-B2 MDQ_EMMA2_[0:15] 32-B2 V+2_5V_ACT_UCOM_DDR to OWN SHEET V+VTT_DDR_EMMA IC7201 EDD5116AFTA-5B-E-K VREF_DDR_EMMA 31-B2.32-A4 SS 1 H 0. 1/10 VSS 3 €VDD v. 1/10 DQ0 DQ15 3 C7298 DQVDQ VSSQ 3 SSH 12 DQ1 DQ14 3 ENC VTTE-PVIN3 F DQ2 DQ138 STILL EVREF VDDQ P2995M-TBB 22/10 BCG1059- -C7205 EDQ6 DQ9 —EDQ7 DQ83 C7214 VB E NC NC B
SS. 1/10 E LDQS UDQS B
LDQS UDQS B
LNC NC B 0. 1/10 C7217 SSH P — EVDD VREF28 ■ NC VSS28 V+VTT_DDR_EMMA VREF 2 C7218 VSS 2 SSH 12 -BE LDM UDM 🖫 LDM -EZ ∕WE /CK ∄ DCASE CAS СК MDQS_EMMA2_[0:3] MDQ_EMMA2[0:15] DRASB -⊠⁄RAS CKE 777GND R725 32-A3 MDQ_EMMA2_[0:15] –**₽**Z/CS NC 🖫 MDQS_EMMA2[0:3] 32-B3 MDQS_EMMA2_[0:3] A12 ₩ NC **□**31-82 DBA0 BBA0 —13BA1 A9 34 —13 A10 (AP) -BAØ **-€** A1 A6 9 -ØA2 A59 C7216 B VDD VSS 🕱 R7250 MA_EMMA2_[0:12] D 32-C2 MA_EMMA2_[0:12] MCTRL_EMMA2_[0:6] MCTRL_EMMA2_[0:6] SS 1227 0 UDQ: SS 1228 1 UDM SS 12229 2 LDQS SS 12229 3 LDQS SS 12229 3 LDM EMM. 21-A3, 31-D4, 32-B5 RADD[0:25] 21-A3, 31-D4, 32-A5
RDATA[0:15] MA_EMMA2[0:12]

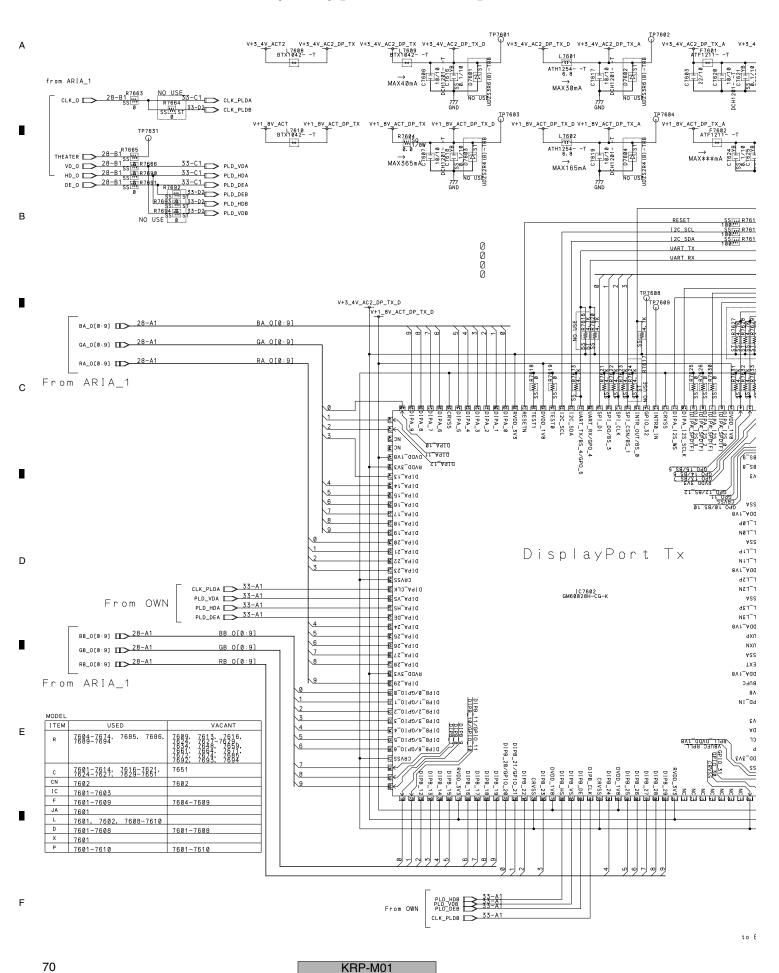
31-B2

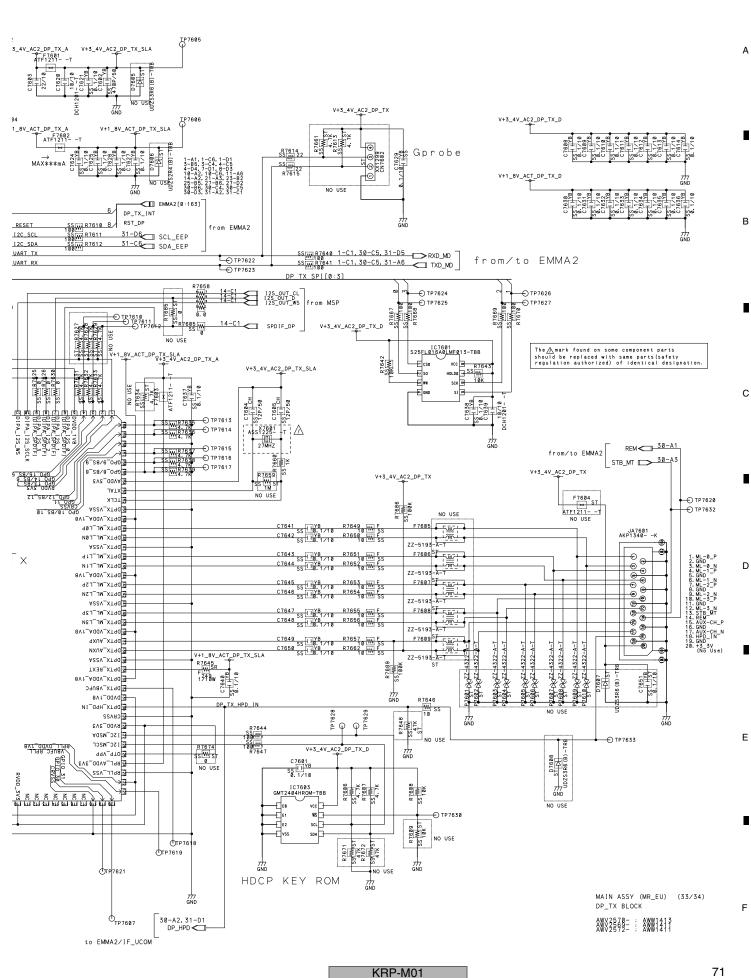
31-B2 32-B3 MA_EMMA2_[0:12] 32-C3 MCTRL_EMMA2_[0:6] MCTRL_EMMA2[0:6] RADD16 15 RADD15 14 RADD14 13 RADD13 12 RADD12 11 RADD11 10 RADD10 9 RADD9 20 RADD20 21 RADD21 FWEB ______21-B5, 31-D4 DBA0 DBA1 DCKE RESET_FLASH 31-A1 DCKE DBA1 DBA0 22 RADD22 S\$7240 NO USE W 1/32W 56 19 RADD19 \18 RADD18 8 RADD8 7 RADD7 6 RADD6 5 RADD5 RADD4 3 RADD3 2 RADD2

KRP-M01

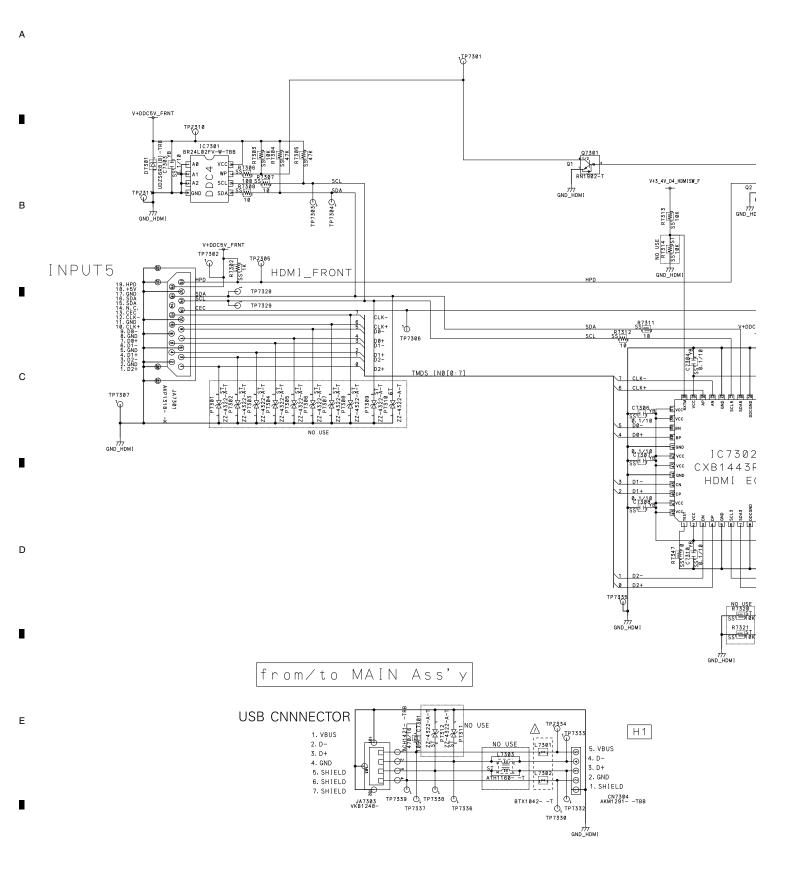


10.34 MAIN BLOCK ASSY (33/33) [DP_TX BLOCK]





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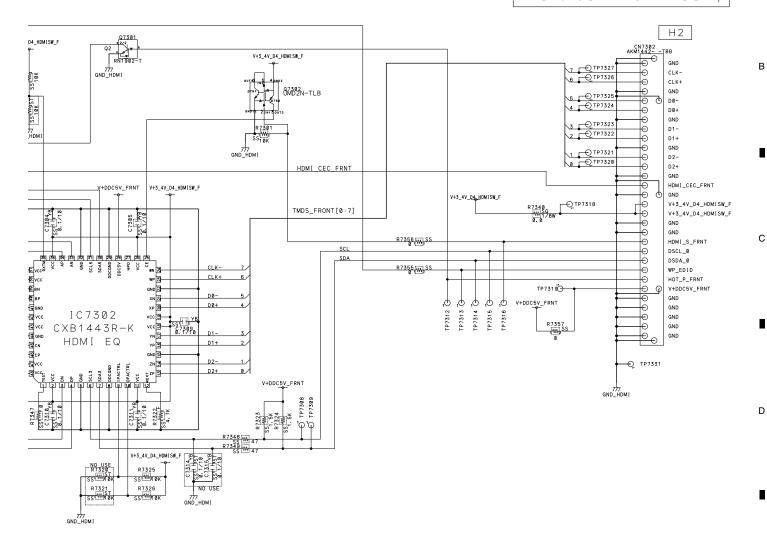


The Mark found on some component should be replaced with same parts regulation authorized) of identica

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from/to MAIN Ass'y

Α



MODEL		
ITEM	USED	VACANT
R	7321-73340, 73347-73349, 73385-	7314, 7320, 7321
С	7301, 7303-7311, 7314, 7315	7314, 7315
Q	7301, 7302	
I C	7301, 7302	
JA	7301, 7303	
CN	7302, 7304	
L	7301-7303	7303
D	7301	
P	7301-7312	7301-7312

MAIN ASSY (MR_EU) (34/34) FRONT_HDMI_USB ASS'Y

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AWY2578- : AWW1412

t found on some component parts replaced with same parts (safety n authorized) of identical designation.

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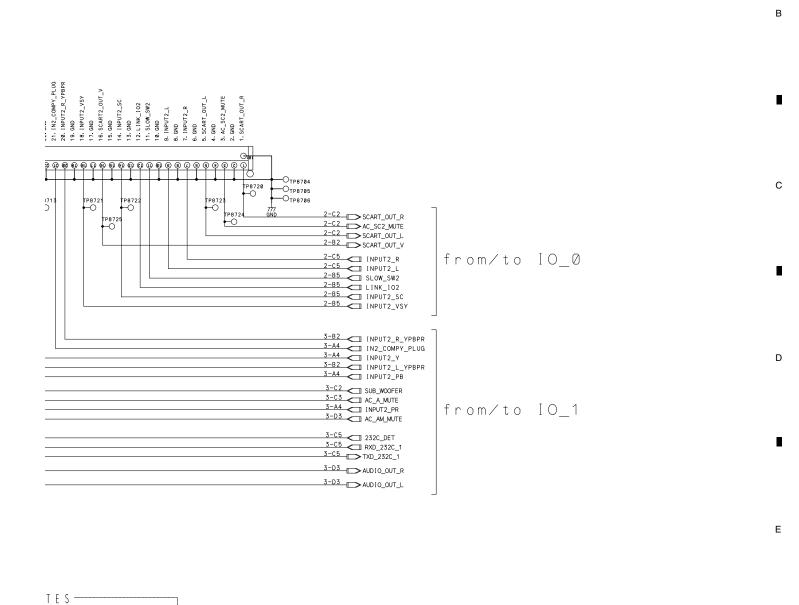
10.36 REAR IO ASSY (1/3) [BOARD_IF BLOCK]

MODEL		
ITEM	USED	VACANT
R	8701-8704	
С		
Q		
IC		
F		
х		
L		
D		
CN	8701	

RESISTORS

* RESISTORS
In dicated in Ω. ±5%, 1/16W Tolegundess otherwise noted, k:kΩ, M:MΩ

74



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KRP-M01

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8

EUKUGO ASS'Y (EU) (1/6) BOARD_IF BLOCK

AWY2571-AWY4441

5 %, 1/16 W Tolerance noted, k: k Ω , M: M Ω .

INPUT2 1.0/10 : CKSRYB105K10-T 1000P/50: CKSSYB102K50-T SCART_OUT_V TP8807 C8809 2.2/10 TP881 C031205--T 55 820 CÖ 1285 - - |

R8886 TP8811 CB810

S5 W 2 2 2 7 10

C0 20 20 - - T from BOARD_IF TP8815 TP8816

R8807 7

IN TP8817

R8808 7

R8808 7 V+3_4<u>V_S</u>TB Q8801 2SC4081 (QR) -TLB Q8802 2SC4081 (QR) -TLB

MODEL		
ITEM	USED	VACANT
R	8801-8813	
С	8801-8811	
Q	8801-8803	
10		
F	8801	
JA	8801	
L		
D	8801-8805	8802
CN		

RESISTORS RS1/8SQ***J-T RS1/10SR***J-T ₩ SS

76

1-85 SLOW_SW2

1-C5 LINK_102

| SLOW_SW2
| S

to BOARD_IF

INPUT2

1. Audio R output 11. Not used 2. Audio R input 12. Not used 3. Audio L output 13. Earth 4. Common earth for audio 14. Not used 5. Earth 15. Chroma S-Video input 6. Audio L input 16. Not used 7. Not used 17. Earth for video 8. Audio-video control 18. Earth 19. Video output 20. Video input/S-Video input 9. Earth 10. AV link control 21. Plug shield

Connection pin assignments for SCART

The Amark found on some component parts should be replaced with same parts(safety regulation authorized) of identical designation.

EUKUGO ASS'Y (EU) (2/6) 15_8-BLOCKS'Y (EU) (2/6) AWY2571-AWY4441

KRP-M01

8

- N O T E S -

RS1/8SQ***J-T

RS1/10SR***J-T

RS1/16SS***J-T

CAPACITORS

TH YB CKSRYB***K**-T

SSELF CH CCSSCH***J50-T

SS CKSSYB***K**-T

TORS

SR SW

SS

6

6

7

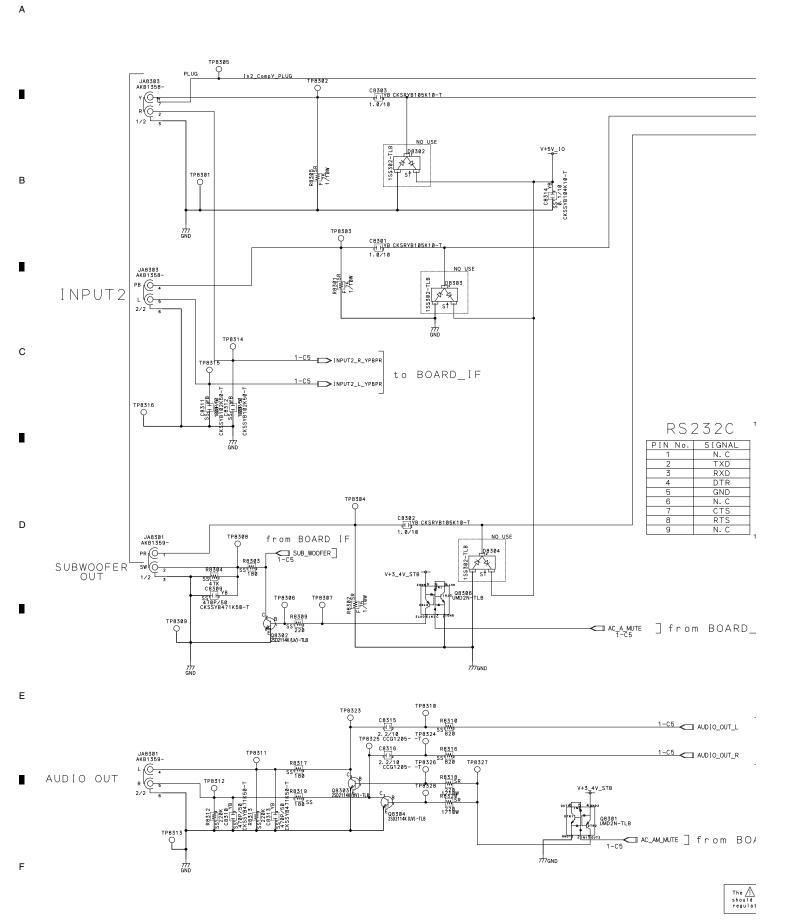
77

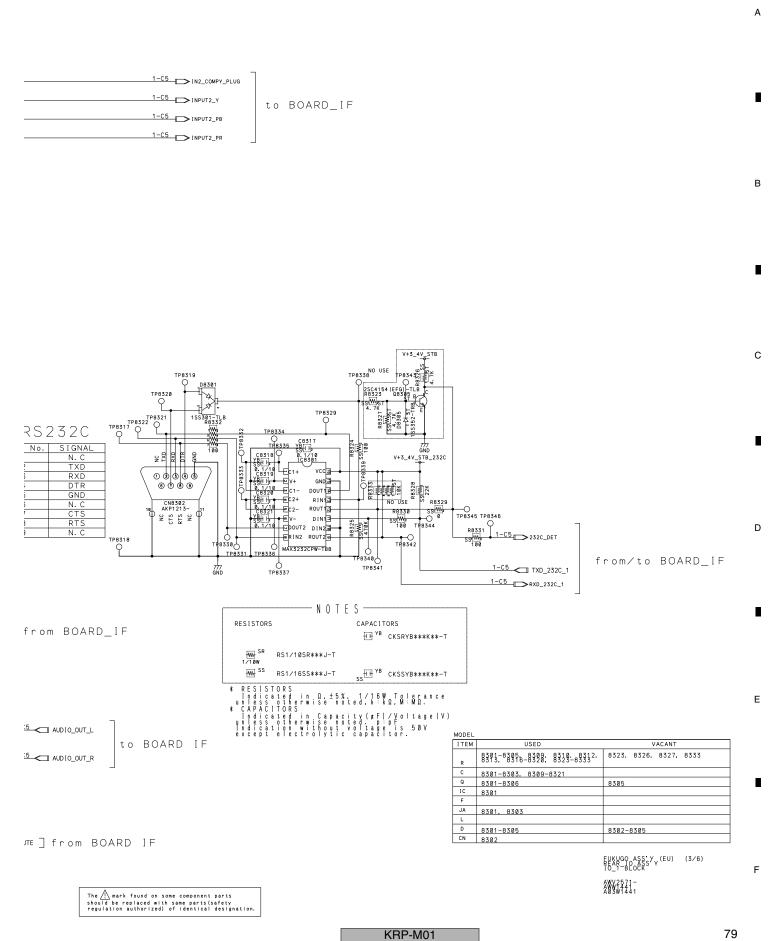
Α

В

D

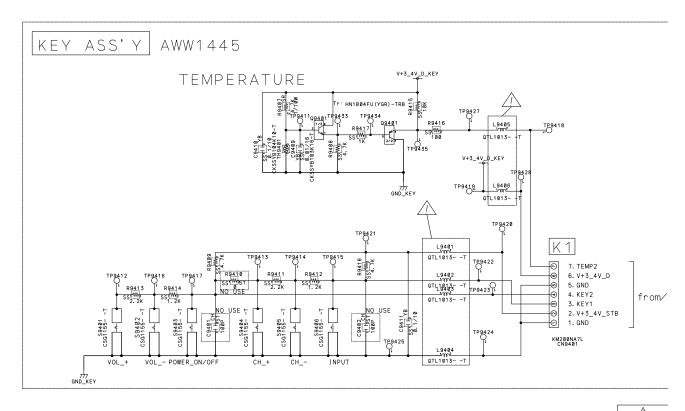
Е



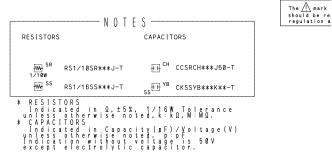


Α

ASS'Y AWW1442 LED NO USE TP9436 6. LED-5. LED_MODEM 4. LED_TIMER from MAIN ASS'Y 3. LED_OFF L9410 0 1 1 1 1 1 1 3 - 1 - 1 2. LED_ON TP94311 TP9432 D9403 TP9410 NO USE L9407 OFF ON (MODEM) 7/7 GND_LED_A 777 GND_LED_B 7/77 GND_LED_A



MODEL		
ITEM	USED	VACANT
R	9401-9405, 9407-9419	9405, 9410, 9419
С	9401-9413	9401-9403, 9405, 9408, 9412,
Q	9401	
10		
S	9401-9406	
TH	9401	
L	9401-9410	9407
D	9401-9403	
CN	9401, 9402	

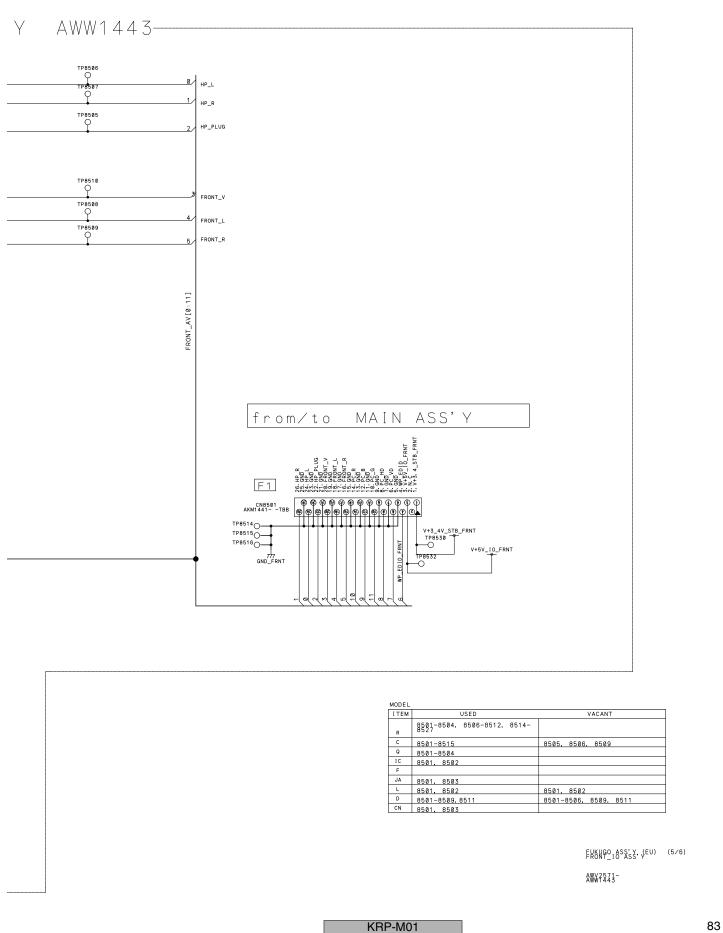


80

Α В 1AIN ASS'Y С D EMP2 +3_4V_D ND EY2 EY1 +3_4V_STB ND from/to MAIN ASS'Y Ε The M mark found on some component parts should be replaced with same parts(safety regulation authorized) of identical designation. FUKUGO ASS'Y (EU/GC) (4/6) AWY2571-AWW1446 KRP-M01 81

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В

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10.41 CI CARD ASSY

CI CARD ASS'Y AWW1444 V+5_1V_D2_C12 R8606 W. 33 R8618 W. 33 COMMON INTERFACE2 (SATELLITE) MD I 7/A25 R8620 | W 233 MCLKO WAIT# INPACK# MOVAL/BVD2 MOSTRT/BVD1 MD01 GND_CI

RESISTORS

RESISTORS

RESISTORS

RESISTORS

RESISTORS

AND THE COSSCH***J00-T

* RESISTORS

In dicated in $\Omega, \pm 5$ %, 1/16 W Tolerance
unless otherwise noted, R M: R M

KRP-M01

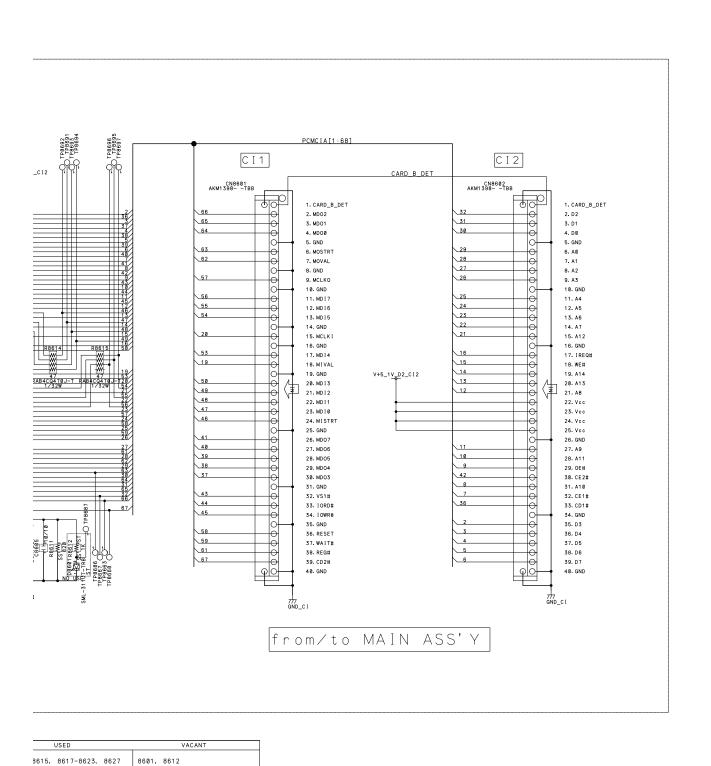
ITEM	USED
R	8601-8615, 8617-8623, 8627
С	8601-8605
Q	
I C	
F	
JA	8601
L	
D	8601
CN	8601, 8602

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8602

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10.42 VOLTAGES AND WAVEFORMS [1]VOLTAGES

MA	NN_BLOCK_Assy	REAR_IO_Assy		
M11 CN4002		Veltere	R1 CN8701	
	(AKM1399TBB)	Voltage	(AKM1399TBB)
NO.	Name	(V)	Name	NO
1	V+5V_IO	5.0	V+5V_IO	50
2	N.C.	0	N.C.	49
3	V+3_4V_STB_232C	3.4	V+3_4V_STB_232C	48
4	N.C.	0	N.C.	47
5	V+3_4V_STB	3.4	V+3_4V_STB	46
6	N.C.	0	N.C.	45
7	TXD_232C_1	3.4	TXD_232C_1	44
8	RXD_232C_1	3.4	RXD_232C_1	43
9	232C_EN_B	0	232C_EN_B	42
10	232C_DET	0/3.4	232C_DET	41
11	GND	0	GND	40
12	AUDIO_OUT_L	5.9	AUDIO OUT L	39
		0	GND	38
14	AC_AM_MUTE	0	AC_AM_MUTE	37
15	GND	0	GND	36
	AUDIO_OUT_R	5.9	AUDIO_OUT_R	35
17	GND	0	GND	34
18	INPUT2_PR	2.4	INPUT2_PR	33
19	GND	0	GND	32
20	AC_A_MUTE	0	AC A MUTE	31
21	GND	0	GND	30
22	SUB WOOFER	0	SUB_WOOFER	29
23	GND	0	GND	28
24	INPUT2_PB	2.4	INPUT2_PB	27
25	GND	0	GND	26
26	IN2_YPbPr_L	0	INPUT2_YPBPR_L	25
27	GND	0	GND	24
28	INPUT2_Y	2.4	INPUT2_Y	23
29	GND	0	GND	22
30	IN2_COMPY_PLUG	4.9	IN2_COMPY_PLUG	21
31	IN2_YPbPr_R	0	INPUT2_YPBPR_R	20
32	GND	0	GND	19
33	INPUT2_VSY	2.6	INPUT2_VSY	18
34	GND	0	GND	17
35	SCART2_OUT_V	5.1	SCART2_OUT_V	16
36	GND	0	GND	15
37	INPUT2_SC	2.2	INPUT2_SC	14
38	GND	0	GND	13
39	LINK_IO2	4.7	LINK_IO2	12
40	SLOW_SW2	0	SLOW_SW2	11
41	GND	0	GND	10
			T	+ -
42	INPUT2_L	0	INPUT2_L	9
43	GND	0	GND INPUT2_R	8
_	INPUT2_R	0		7
45	GND		GND CART OUT I	6
46	SCART_OUT_L	5.9	SCART_OUT_L	5
47	GND	0	GND	4
48	SC2_MUTE	2.9	AC_SC2_MUTE	3
49	GND	0	GND CCART OUT D	2
50	SCART_OUT_R	5.9	SCART_OUT_R	1

MAIN_BLOCK_Assy			FRONT_IO_Assy	
M12 CN4003 (AKM1441TBB) NO. Name		Voltage (V)	F1 CN8501 (AKM1441TBB) Name	NO.
1	HP R	2.1	HP_R	26
2	GND	0	GND	25
3	HP L	2.1	HP L	24
4	GND	0	GND	23
5	HP PLUG	0/3.1	HP PLUG	22
6	GND	0	GND	21
7	FRONT_V	2.5	FRONT_V	20
8	GND	0	GND	19
9	FRONT_L	-0.2 / 0.2	FRONT_L	18
10	GND	0	GND	17
11	FRONT_R	-0.2 / 0.2	FRONT_R	16
12	GND	0	GND	15
13	PC_R	1.8	PC_R	14
14	GND	0	GND	13
15	PC_B	1.8	PC_B	12
16	GND	0	GND	11
17	PC_G	1.8	PC_G	10
18	GND	0	GND	9
19	PC_HD	0/3.4	PC_HD	8
20	GND	0	GND	7
21	PC_VD	0/3.4	PC_VD	6
22	GND	0	GND	5
23	WP_EDID	0	WP_EDID	4
24	V+5V_IO_FRONT	5.0	V+5V_IO_FRONT	3
25	N.C	0	N.C	2
26	V+3_4V_STB_FRONT	3.4	V+3_4V_STB_FRONT	1

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MAIN_BLOCK_Assy CARD_Assy					
	M21 CN4101	Voltage	CI1 CN8601		
(AKM1398TBB)		(V)	(AKM1398TBB)		
NO.	Name] (*)	Name	NO.	
1	CARD_B_DET	0	CARD_B_DET	1	
2	MDOB2	0.1	MDO2	2	
3	MDOB1	0.1	MDO1	3	
4	MDOB0	0.1	MDO0	4	
5	GND	0	GND	5	
6	MOSTRTB	0.1	MOSTRT	6	
7	MOVALB	0.1	MOVAL	7	
8	GND	0	GND	8	
9	MCLK0B	0.1	MCLK0	9	
10	GND	0	GND	10	
11	MDIB7	1.6	MDI7	11	
12	MDIB6	1.6	MDI6	12	
13	MDIB5	1.6	MDI5	13	
14	GND	0	GND	14	
15	MCLKIB	1.6	MCLKI	15	
16	GND	0	GND	16	
17	MDIBI4	1.6	MDI4	17	
18	MIVALB	1.6	MIVAL	18	
19	GND	0	GND	19	
20	MDIB3	1.6	MDI3	20	
21	MDIB2	1.6	MDI2	21	
22	MDIBI1	1.6	MDI1	22	
23	MDIBI0	1.6	MDI0	23	
24	MISTRTB	1.6	MISTRT	24	
25	GND	0	GND	25	
26	MDOB7	0	MDO7	26	
27	MDOB6	0	MDO6	27	
28	MDOB5	0	MDO5	28	
29	MDOB4	0	MDO4	29	
30	MDOB3	0	MDO3	30	
31	GND	0	GND	31	
32	VS1#_B	2.8	VS1#	32	
33	IORD#_B	1.6	IORD#	33	
34	IOWR#_B	1.6	IOWR#	34	
35	GND	0	GND	35	
36	RESETB	1.6	RESET	36	
37	WAITB#	0	WAIT#	37	
38	REG#_B	1.6	REG#	38	
39	CD2B#	5.0	CD2#	39	
40	GND	0	GND	40	

MAIN	_BLOCK	_Assy	CARD	_Assy

		 	- The state of the		
M22 CN4102		Voltage	CI2 CN8602		
(AKM1398TBB)		(V)	(AKM1398TB		
NO.			Name	NO.	
1	CARD_B_DET	0	CARD_B_DET	1	
2	D2	0	D2	2	
3	D1	0	D1	3	
4	D0	0	D0	4	
5	GND	0	GND	5	
6	A0	1.6	A0	6	
7	A1	1.6	A1	7	
8	A2	1.6	A2	8	
9	A3	1.6	A3	9	
10	GND	0	GND	10	
11	A4	1.6	A4	11	
12	A5	1.6	A5	12	
13	A6	1.6	A6	13	
14	A7	1.6	A7	14	
15	A12	1.6	A12	15	
16	GND	0	GND	16	
17	IREQB#	0	IREQ#	17	
18	WE#_B	1.6	WE#	18	
19	A14	1.6	A14	19	
20	A13	1.6	A13	20	
21	A8	1.6	A8	21	
22	Vcc	0	Vcc	22	
23	Vcc	0	Vcc	23	
24	Vcc	0	Vcc	24	
25	Vcc	0	Vcc	25	
26	GND	0	GND	26	
27	A9	1.6	A9	27	
28	A11	1.6	A11	28	
29	OE#_B	1.6	OE#	29	
30	CE2B#	1.6	CE2#	30	
31	A10	1.6	A10	31	
32	CE1B#	1.6	CE1#	32	
33	CD1B#	5.0	CD1#	33	
34	GND	0	GND	34	
35	D3	0	D3	35	
36	D4	0	D4	36	
37	D5	0	D5	37	
38	D6	0	D6	38	
39	D7	0	D7	39	
40	GND	0	GND	40	

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MAIN_BLOCK_Assy FRONT_HDMI_Assy

	M13 CN4901	Voltogo	H1 CN7302		
(AKM1442TBB) NO. Name		Voltage (V)	(AKM1442TBB)		
		(*)	Name	NO.	
1	GND	0	GND	30	
2	CLK-	1.6/3.0	CLK-	29	
3	CLK+	1.6/3.0	CLK+	28	
4	GND	0	GND	27	
5	D0-	3.3/3.0	D0-	26	
6	D0+	3.3/3.0	D0+	25	
7	GND	0	GND	24	
8	D1-	1.6/3.0	D1-	23	
9	D1+	1.6/3.0	D1+	22	
10	GND	0	GND	21	
11	D2-	1.6/3.0	D2-	20	
12	D2+	1.6/3.0	D2+	19	
13	GND	0	GND	18	
14	HDMI_CEC_FRNT	3.3	HDMI_CEC_FRNT	17	
15	GND	0	GND	16	
16	V+3_4V_D4_HDMISW_F	3.3	V+3_4V_D4_HDMISW_F	15	
17	V+3_4V_D4_HDMISW_F	3.3	V+3_4V_D4_HDMISW_F	14	
18	GND	0	GND	13	
19	GND	0	GND	12	
20	HDMI_S_FRNT	0/3.3	HDMI_S_FRNT	11	
21	DSCL_0	0/5.0	DSCL_0	10	
22	DSDA_0	0/5.0	DSDA_0	9	
23	WP_EDID	0	WP_EDID	8	
24	HOT_P_FRNT	0/3.3	HOT_P_FRNT	7	
25	V+DDC5V_FRNT	0/5.0	V+DDC5V_FRNT	6	
26	GND	0	GND	5	
27	GND	0	GND	4	
28	GND	0	GND	3	
29	GND	0	GND	2	
30	GND	0	GND	1	

MAIN_BLOCK_Assy F-HDMI_Assy(for USB)

M13 CN4004 (AKM1276TBB)		Voltage (V)	CN7304 (AKM1291TBB)		
NO.	Name	(*)	Name	NO.	
1	SHIELD	0	SHIELD	1	
2	GND	0	GND	2	
3	D+	0	D+	3	
4	D-	0	D-	4	
5	VBUS	5.1	VBUS	5	

MAIN_BLOCK_Assy

KEY_Assy

M2 CN4204 (AKW1343TBB)		Voltage	K1 CN9401 (KM200NA7L)		
NO.	Name	(V)	Name	NO.	
1	OPEN	0			
2	OPEN	0			
3	TEMP2	2.1	TEMP2	7	
4	GND	0	GND	5	
5	KEY1	3.4	KEY1	3	
6	GND	0	GND	1	
7	LED-	0			
8	LED_TIMER	3.3/0			
9	LED_ON	2.8/0			
10	OPEN	0			
11	OPEN	0			
12	LED-	0			
13	LED_OFF	3.3/0			
14	LED_MODEM(LED-)	0			
15	OPEN	0			
16	V+3_4V_STB	3.4	V+3_4V_STB	2	
17	KEY2	3.4	KEY2	4	
18	V+3_4V_D	3.3	V+3_4V_D_KEY	6	
19	OPEN	0			
20	OPEN	0			

MAIN_BLOCK_Assy LED_Assy

M2 CN4204 (AKW1343TBB)		Voltage	L1 CN9402 (KM200NA6L)		
NO.		(V)	Name N		
1	OPEN	0			
2	OPEN	0			
3	TEMP2	2.1			
4	GND	0			
5	KEY1	3.4			
6	GND	0			
7	LED-	0	LED-	6	
8	LED_TIMER	3.3/0	LED_TIMER	4	
9	LED_ON	2.8/0	LED_ON	2	
10	OPEN	0			
11	OPEN	0			
12	LED-	0	LED-	1	
13	LED_OFF	3.3/0	LED_OFF	3	
14	LED_MODEM(LED-)	0	LED_MODEM	5	
15	OPEN	0			
16	V+3_4V_STB	3.4			
17	KEY2	3.4			
18	V+3_4V_D	3.3			
19	OPEN	0			
20	OPEN	0			

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MAIN_BLOCK_Assy **FAN** M31 CN4201 Voltage (AKM1276- -TBB) (V) NO. Name Name NO. 1 N.C. 0 2 FAN_VCC2 7.0/8.9 VCC 3 FAN_NEG2 0.1 NG 4 GND 0 GND 5 N.C. 0

MAIN BLOCK Assy POWER SUP

M1 CN4203 Voltage			P2	Voltage	
(AKM1440-) NO. Name		(V)	(B26B-PNDZ-1) Name	NO.	(V)
_		40.4		_	4.0
1	V+17V	19.1	V+5_1V_STB	26	4.9
2	V+5_1V_STB	4.9	V+17V	25	19.1
3	GND	0	GND	24	0
4	GND	0	GND	23	0
5	V+12V	13.0	V+12V	22	13.0
6	V+12V	13.0	V+12V	21	13.0
7	GND	0	GND	20	0
8	GND	0	GND	19	0
9	V+6_5V	6.6	V+6_5V	18	6.6
10	V+6_5V	6.6	V+6_5V	17	6.6
11	V+6_5V	6.6	V+6_5V	16	6.6
12	V+6_5V	6.6	V+6_5V	15	6.6
13	GND	0	GND	14	0
14	GND	0	GND	13	0
15	GND	0	GND	12	0
16	GND	0	GND	11	0
17	V+3_4V_STB	3.4	V+3_4V_STB	10	3.4
18	V+3_4V_STB	3.4	V+3_4V_STB	9	3.4
19	V+3_4V_STB	3.4	V+3_4V_STB	8	3.4
20	V+3_4V_STB	3.4	V+3_4V_STB	7	3.4
21	GND	0	V+3_4V_STB	6	3.4
22	V+3_4V_STB	3.4	GND	5	0
23	PD_TRG	0	GND	4	0
24	GND	0	PD_TRG	3	0
25	AC_DET	3.1	RELAY	2	3.1
26	RELAY	3.2	AC_DET	1	3.2

[2]WAVEFORMS

Refer to the section "5.2 DIAGNOSIS FLOWCHART OF FAILURE ANALYSIS" of the Service Manual for KRP-M01 (ARP3508) .

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11. PCB CONNECTION DIAGRAM 11.1 MAIN BLOCK AND FRONT_HDM_USB ASSYS

SIDE A

MAIN BLOCK ASSY

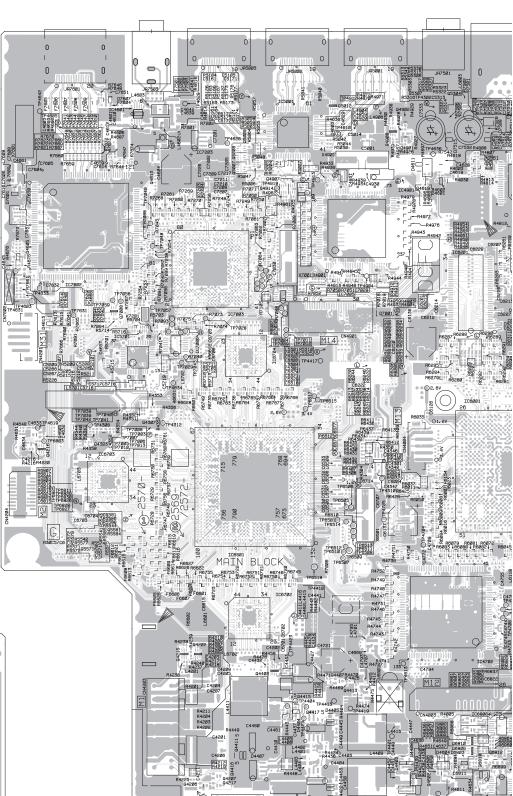
NOTE FOR PCB DIAGRAMS:

- The parts mounted on this PCB include all necessary parts for several destinations.
 For further information for respective destinations, be sure to check with the schematic diagram.
- 2. View point of PCB diagrams.

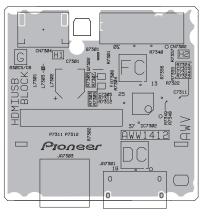
 Connector Capacitor

 SIDE A

 P.C.Board Chip Part SIDE B



FRONT_HDM_USB ASSY



(ANP2225-B)

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N2 AWW1411 2569/2572

SIDE A (ANP2225-B) 91 KRP-M01

SIDE B

TP52Ø5 R52Ø4 C52Ø34 OTP53Ø8

(ANP2225-B)

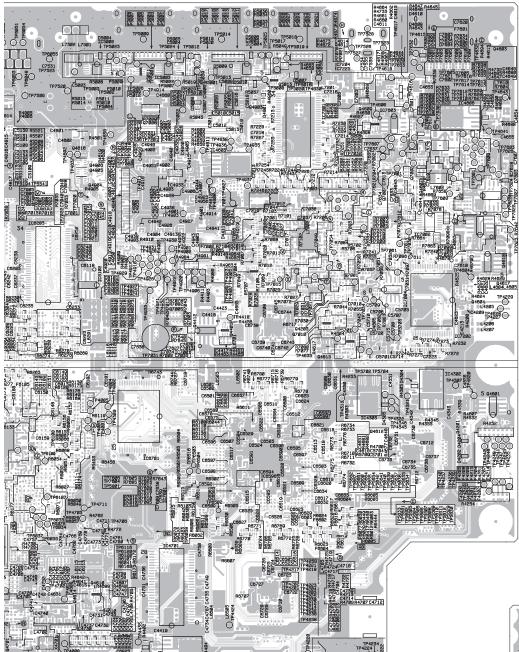
92

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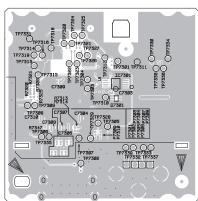
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SIDE B

MAIN BLOCK ASSY



FRONT_HDM_USB ASSY



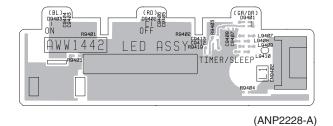
(ANP2225-B)

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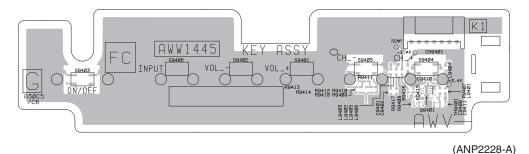
11.2 REAR IO, LED, FRONT IO, CI CARD AND KEY ASSYS

SIDE A

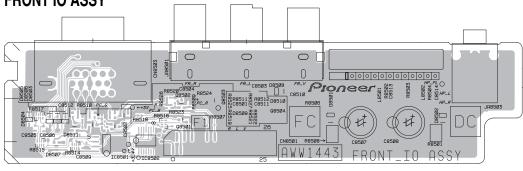
LED ASSY



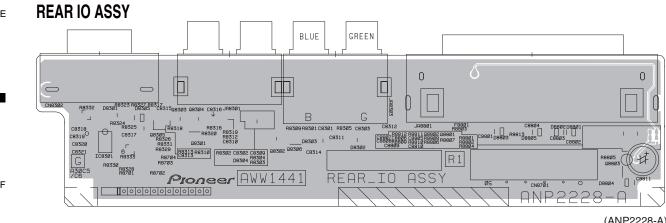
KEY ASSY



FRONT IO ASSY



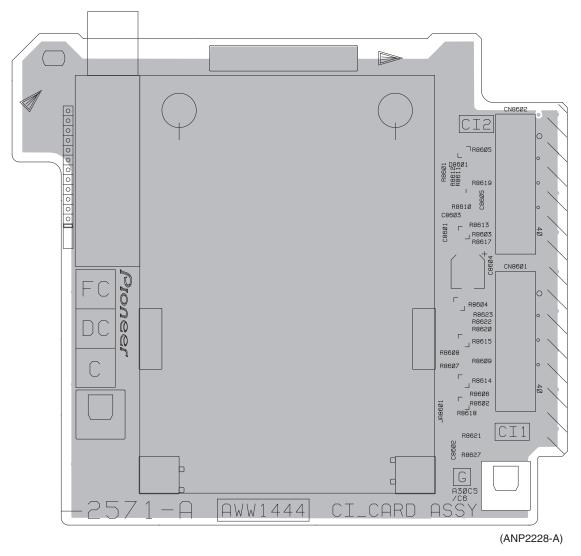
(ANP2228-A)



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(ANP2228-A)

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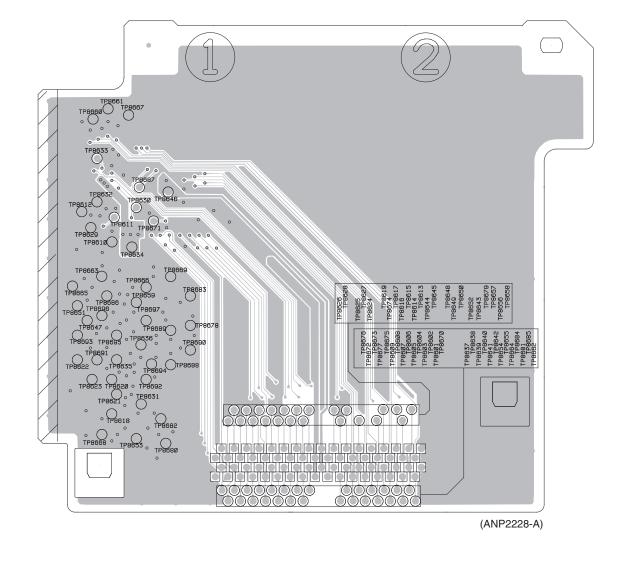
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SIDE B

CI CARD ASSY



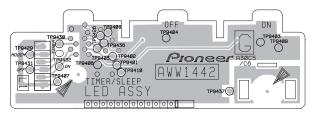
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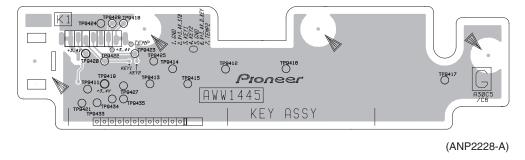
SIDE B

LED ASSY

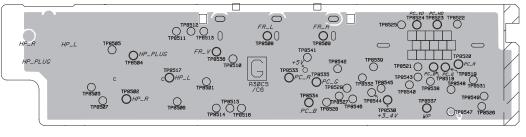


(ANP2228-A)

KEY ASSY

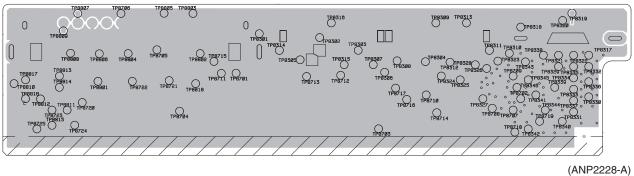


FRONT IO ASSY



(ANP2228-A)

REAR IO ASSY



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12. PCB PARTS LIST

NOTES: • Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.

- ullet The igtriangle mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- When ordering resistors, first convert resistance values into code form as shown in the following examples. Ex.1 When there are 2 effective digits (any digit apart from 0), such as 560 ohm and 47 k ohm (tolerance is shown by J = 5%, and K = 10%).

 \rightarrow 561 ······RD1/4PU $\boxed{5}$ $\boxed{6}$ $\boxed{1}$ J 56×10^{1} 560Ω $47 k\Omega$ 0.5Ω 1Ω

Ex.2 When there are 3 effective digits (such as in high precision metal film resistors). $5.62 \text{ k}\Omega \rightarrow 562 \times 10^{\overline{1}} \rightarrow 5621 \cdots RN1/4PC$ [3] [6] [2] [7] F

• Meaning of the figures and others in the parentheses in the parts list. Example) IC 301 is on the point (face A, 91 of x-axis, and 111 of y-axis) of the corresponding PC board.

		IC 301 (A, 91, 11.	1) IC NJM2068V					
Mark	No.	Description	Part No.	Mark N	o. Des	scription		Part No.
LIS	T OF	ASSEMBLIES .						
		ASSY(EU MR)	AWV2570	Unit N	lame: FRO	NT_HDM	_USB	ASSY(EU MR)
		T_HDM_USB_ASSY(EU MR)	AWW1412					· · ·
	2MAIN	BLOCK ASSY(EU MR)	AWW1413	SEMIC	ONDUCTOR	S		
		. ,		IC 73				BR24L02FV-W
NSP	1FUKUG	O ASSY(EU MR)	AWV2571	IC 73	302			CXB1443R
	2REAR	IO ASSY(EU MR)	AWW1441	Q 73	301			RN1902
		ASSY(EU MR)	AWW1442	Q 73	302			UMD2N
		T IO ASSY(EU MR)	AWW1443	D 73	301			UDZS6R8(B)
		RD ASSY(EU MR)	AWW1444					
	2KEY A	ASSY(EU MR)	AWW1445	MISCE	LLANEOUS			
۵				∆L 73	301,7302 CHIP BE	EDS FILTER		BTX1042
<u> </u>	1POWEF	R SUPPLY UNIT	AXY1223		301 HDMI CONNE			AKP1318
					303 USB CONNEC			VKB1248
					302 30P CONNEC			AKM1442
				CN 73	304 CONNECTOR			AKM1291
				RESIS [*]	TORS			
				R 73	322			RS1/16SS4701F
				R 73	340			RS1/8SQ0R0J
				Other	Resistors			RS1/16SS###J
				CAPAC	CITORS			
				C 73	301			ACH1421
				C 73	303-7311			CKSSYB104K10
				Unit l	Name: MA	AIN BLO	CK A	SSY(EU MR)
				SEMIC	ONDUCTOR	S		
				NSP IC 64		_		AGC1089
				NSP IC 67				AGC1088
				NSP IC 68				AGC1086

NSP IC 7202 AGC1087

MISCELLANEOUS

BPZ26P050FTC 3001 SCREW 3001 HEAT SINK B ANH1645 3002 THERMAL SHEET B AEB1417

RESISTORS

RS1/8SQ###J All Resistors

MISCELLANEOUS

⚠ U 5201 FE AXF1195 **⚠** U 5301 FE AXF1191

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Mark No. Description	Part No.	Mark No. Description P	art No.
Block Name: BOARD_IF_0 BLOCK	K(EH)	Block Name: POWER_0 BLOCK(EH)	
SEMICONDUCTORS		SEMICONDUCTORS	
Q 4001	2SA1576A		5523N001B A
D 4001,4002	1SS352		Q200WNA1ZPH
D 4003-4008	1SS301	,	IB3842PFV-G-E1
2 1000 1000			JM2846DL3-05
MISCELLANEOUS		•	D8903FV
CN 4001.4002 50P CONNECTER	AKM1399	10 1000	300001 7
CN 4001,4002 30F CONNECTEN CN 4003 FFC CONNECTOR 26P	AKW1399 AKM1441	IC 4308 N	JM78M12DL1A ■
CN 4004 CONNECTOR	AKM1276		AT4610AIGV-1
GN 4004 GOINNEGTOR	ARIVI1270	•	TC124EUA
DECICTORS			SA1576A
RESISTORS	DAD 400000 I		TA143EUA
R 4009-4011,4014,4015	RAB4CQ220J	Q +3007,7303	INITOLON
R 4013,4016	RAB4CQ101J	Q 4308-4310 D	TC124EUA
R 4020,4022,4023	RAB4CQ220J		D
R 4021	RAB4CQ0R0J		MD2N
R 4029	RS1/10SR471J		SS352
			B521S-40
R 4030	RS1/10SR470J	D 4316	SS352
Other Resistors	RS1/16SS###J		
		MISCELLANEOUS	_
CAPACITORS			TX1039
C 4003	ACH1421		TH1111
		L 4306 CHIP COIL B'	TH1126
Block Name: BOARD_IF_1 BLOCK	((EH)		
	-()	<u>RESISTORS</u>	
MISCELLANEOUS			S1/8SQ0R0J
CN 4101,4102 40P CONNECTER	AKM1398		S1/10SR0R0J C
*		•	S1/8SQ0R0J
CN 4104 80P CONNECTOR RCPT	BKP1159		S1/16SS3901F
DEGICTORS			S1/16SS1003D
RESISTORS		11 1020	31/10001000D
R 4114	RS1/16SS103J	R 4327 R	S1/16SS2202F
Other Resistors	RS1/8SQ###J		\$1/16\$\$2201E
			S1/16SS4701F
Block Name: BOARD_IF_2 BLOCK	K(EH)		S1/16SS5101F
			S1/16SS2701F
<u>SEMICONDUCTORS</u>		11 4040	31/103327011
Q 4201,4206,4207,4217	DTC124EUA	R 4346 R	S1/16SS1501F
Q 4202	RN1902		S1/10331301F S1/8SQ0R0J
Q 4203	UMD2N	,	
Q 4215	HN1A01FU	Other Resistors R	S1/16SS###J D
Q 4216	RN2902	0.4.04.04.000	
		CAPACITORS	
D 4202,4204	1SS352	· · · · ·	KSRYB105K10
D 4202,4204	100002		KSRYB104K25
MISCELLANEOUS			KSSYB682K25
	DTV1020	C 4306,4331 B	CG1064
L 4201 CHIP BEEDS FILTER	BTX1039	C 4309,4310 C	EHVAW330M25
L 4205-4207 CHIP BEEDS FILTER	BTX1042		
F 4201-4205,4207-4209 INDUCTOR	CTF1557	C 4311 A	CG1147
F 4213-4215 INDUCTOR	CTF1557	C 4313,4327,4345 D	CH1201
CN 4201 CONNECTOR	AKM1276		CH1165
			KSRYB105K10
CN 4204 20P CONNECTOR	AKM1343		EHVAW101M6R3 E
CN 4203 26P CONNECTOR	AKM1440	0 1020	2110/10/10/10/10
		C 4332,4335,4347,4348 C	KSSYB104K10
<u>RESISTORS</u>			KSSYB473K16
R 4201-4207,4209-4211	RS1/8SQ0R0J		KSSYB104K10
R 4217,4218	RS1/8SQ0R0J	0 7002	עואדטועוטטא
R 4251,4253	RS1/10SR0R0J	Block Name: POWER_1 BLOCK(EH)	I
R 4252,4254	RS1/10SR102J	DIOCK NAME: FOWER_I BLOCK(ER)	
Other Resistors	RS1/16SS###J	CEMICONDUCTORS	
-	- , " •	SEMICONDUCTORS	WA4500D5
<u>CAPACITORS</u>			IM1593DF
C 4203	CKSSYB102K50		D8606FV
			JM2846DL3-33
C 4208	DCH1201	- , -	N1902 F
C 4211	CKSSYB103K16	Q 4403,4413 U	PA1917TE
C 4216	CKSSYB104K10		
		Q 4404-4406,4414 D	TC124EUA
	K	RP-M01	99
			- •

KRP-M01 7

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	Mark	No.	Description	Part No.	Mark	No.	Description	Part No.
		4407,4408	<u>-</u>	2SC4081		4502 CHIP B	•	BTX1039
		4409,4410		DTA124EUA			NDUCTOR(2.2 UH)	ATH1244
		4411,4412,44	115	RSS100N03		4505 INDUC		CTH1254
Α	Q	4416		RSS090P03	L	4506 INDUC	TOR(270 UH)	ATH1242
		4417,4418	110	SP8M4	L	4507 INDUC	TOR	ATH1235
		4401,4402,44	110	1SS352 RB060M-30	DEC	ICTORC		
	U	4405-4407		NDUOUIVI-3U		ISTORS 4503,4504,45	506	RS1/8SQ0R0J
	MISC	ELLANEC	ous			4510,4512	000	RS1/4SA101J
_			HIP BEEDS FILTER	BTX1039		4511		RS1/4SA150J
			IDUCTOR(2.8 UH)	ATH1243		4524,4525		RS1/10SR0R0J
	L	4413-4416 CI	HIP BEEDS FILTER	BTX1039	R	4536		RS1/16SS1003D
	RES	ISTORS			R	4537,4539		RS1/16SS3302D
В	R	4401-4403,44	411-4413	RS1/8SQ0R0J	Ot	her Resistors		RS1/16SS###J
Ь	R	4406,4438		RS1/16SS1203D				
		4407,4425-44	·	RS1/16SS3302D		ACITORS		
		4415,4416,44	188	RS1/8SQ0R0J		4502,4506,45	515,4530	CCSSCH101J50
	К	4421		RS1/16SS5602D		4504		CKSSYB104K10
	D	4429		RS1/16SS2702D	C	4507,4517 4527,4528		DCH1165 CKSSYB103K25
		4429 4440		RS1/16SS1002D	C	4527,4528 4529		CKSSYB223K16
_		4442		RS1/16SS3902D	U	1020		ONGO I DZZON IU
		4444,4445		RS1/16SS3302D	С	4535-4537		ACH1495
		ner Resistors		RS1/16SS###J		4538		CEHVAW100M35
						4539-4541,4	546	CKSRYB104K50
		<u>ACITORS</u>			C	4542,4543		CKSRYB104K25
С		4401,4415,44		CKSRYB104K16	С	4544,4545		CKSRYB224K16
		4402,4413,44	·	DCH1201	0	1517		CKCD//Deoo//Eo
		4403-4406,44 4407,4455,44		DCH1165 CKSRYB682K50		4547 4548,4549		CKSRYB682K50 CKSRYB104K16
		4407,4455,44 4411	101	CCG1232		4552,4553		BCG1059
	С	4417,4423,44	126 4434	CKSSYB104K10	Bloc	k Name· P	OWER_3 BLOCK(EH)	
		4417,4423,44	·	CKSSYB471K50	Dioc	taine. F		
		4422,4429,44		DCH1201	SEM	IICONDUC	TORS	
		4427,4465	100	CKSRYB105K10		4601	10110	LTC3407EMSE-2
	С	4432,4437		CCSSCH101J50		4602-4604,4	606	NJM2846DL3-18
						4601,4603,46		RSS090P03
		4435		CCSSCH470J50	Q			UPA1917TE
D		4436,4439		CKSSYB152K50	Q	4604,4606,46	617	2SC4081
	C C	4438 4440		CCSSCH330J50 CKSSYB682K25	0	4COE 4COO		DTC104FHA
	C	4441		CKSSYB221K50		4605,4608 4609,4610,46	310	DTC124EUA RN1902
	O			ONOOT BEETHOU		4613	J12	RSS100N03
	С	4447,4448,44	151,4452	BCG1059		4614.4615		RTQ045N03
		4454		CKSRYB334K10		4616		RTQ040P02
		4462-4464,44	466-4468	BCG1059	-			-
	С	4470		CKSSYB104K10		4603,4607,46	608,4612	1SS352
	Bloc	k Name: P	OWER_2 BLOCK(EH	1)	D	4609,4610		RB551V-30
	~ =	10011				CELLANEC		
Е		ICONDUC	<u>IUKS</u>	DD0004551		4601 CHIP B		BTX1039
_		4501		BD8624EFV			HIP INDUCTOR(2.2 UH)	ATH1244
		4503 4502		LNBH23PP/1B DTC124EUA	L	4604,4605 CI	HIP BEEDS FILTER	BTX1042
		4502 4504		D10124EUA RN1902	DEC	ISTORS		
		4504 4507		RTQ045N03		4601,4606-4	617 4632	RS1/8SQ0R0J
	u	1001		III WUTUNUU		4659	U11, 1 002	RS1/16SS1503D
	Q	4509		DTA124EUA		4663		RS1/16SS1003D
_		4510		2SC4081	R			RS1/16SS2003D
		4501		1SS352		4667		RS1/16SS6202D
		4508		D1FM3				
	D	4509		TDZ5R1		4687,4688,46	694	RS1/8SQ0R0J
	n	4510,4511		RB520S-30	Ut	her Resistors		RS1/16SS###J
F		4513		RB060M-30	CVD	ACITORS		
	,					4602,4621,46	623.4634	CKSSYB104K10
	MISC	ELLANEC	<u>ous</u>			4604,4608	J_0, 100 i	CKSRYB104K16
	L	4501 CHIP B	EEDS FILTER	BTX1042		,		
	100			KRI	P-M01			
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Mark	No. Description	Part No.	Mark	No. Description	Part No.	
	4605,4606,4609,4654	CKSRYB105K10		4806,4808,4810	CKSSYB473K16	
С	4610,4612	BCG1059		4811-4817	CKSSYB104K10	
С	4615	CKSSYB102K50				
0	4040 4047	0000011470150	С	4818-4820	DCH1201	Α
C C	4616,4617 4618	CCSSCH470J50 CKSSYB103K16	Dies	ok Nama, UDMI, DV BI OCK/EU		
C	4626,4628,4638,4640	DCH1201	Вюс	ck Name: HDMI_RX BLOCK(EH))	
Č	4636,4649,4651	CKSSYB104K10	SEM	IICONDUCTORS		
С	4641,4656	DCH1201		4901	SII9135ACTU	
			Q	4901,4902	DTC124EUA	
С	4645,4648	CCSSCH101J50		4903	2SC4081	
Bloc	k Name: VDEC BLOCK(EH)		Q Q		UMD2N RN1902	
D .00	it name. VBLO BLOOK(En)		u	4906	NN 1902	
<u>SEM</u>	ICONDUCTORS		Q	4910	2SA1576A	
	4701	HY57V641620FTP-6	Q		HN1C01FU	В
	4702	CM0048BF	D	4901	RB520S-30	
Q	4701,4702	2SA1576A	MICA	OFLI ANEOUS		
MISC	CELLANEOUS			CELLANEOUS 4901-4905 CHIP SOLID INDUCTOR	QTL1013	
	4701,4707 CHIP BEEDS FILTER	BTX1042		4906,4907 CHIP BEEDS FILTER	BTX1042	
	4702,4703 COIL	LCYC6R8K2125		4901,4902 CHIP FERRITE BEADS	ATF1211	
	4708 CHIP BEEDS FILTER	BTX1042		4901 CRYSTAL(28.322 MHz)	ASS1226	
⚠ F	4701 INDUCTOR	CTF1557	CN	N 4901 30P CONNECTOR	AKM1442	
⚠ X	4701 CRYSTAL(28.63636 MHz)	ASS1214				
DE01	ICTO DO		· · · · · · · · · · · · · · · · · · ·	SISTORS		
	ISTORS	DC1/0C00D0 I		4940-4943,4976-4979	ACN1275	
	4703,4727 4710,4720	RS1/8SQ0R0J RS1/16SS1500F		4944 4945-4954	RAB4CQ100J RAB4CQ680J	С
	4710,4720 4711,4721	RS1/16SS2201F		4986	RS1/8SQ0R0J	C
	4712,4722	RS1/16SS1101F		ther Resistors	RS1/16SS###J	
R	4713,4715,4723	RS1/16SS2701F				
			CAP	PACITORS		
	4714	RS1/16SS1001F		4901-4928,4932,4933	CKSSYB102K50	
	4726,4737-4745	RAB4CQ470J	C	4929	CKSSYB103K16	
	4746-4752 ner Resistors	RAB4CQ101J RS1/16SS###J		4930,4931	CCSSCH9R0D50	
Oti	IEI NESISIUIS	no i/ 1000###J	C	4934,4937-4940 4936,4941,4946,4951	CKSSYB104K10 DCH1201	
CAP	ACITORS		O	1300,1311,1310,1331	DOTTIZOT	
	4701,4704-4711	CKSRYB105K10	С	4942-4945,4947-4950	CKSSYB104K10	
	4702,4703	CCSRCH300J50	С	4952-4960	CKSSYB104K10	
	4712,4718,4720	CKSSYB103K16				D
	4713,4717 4714,4710	CCSSCH330J50	Bloc	ck Name: HDMI_SW BLOCK(EH	ı)	
U	4714,4719	CCSSCH680J50	SEM	IICONDUCTORS		
С	4715,4716	CKSSYB102K50		5001	CXB1444R	
	4721	CEHVAW101M6R3		5002-5004	BR24L02FV-W	
	4722-4736,4738-4774	CKSSYB104K10	Q	5007-5009	UMD2N	
	4737,4793-4797	DCH1201		5011-5013	RN1902	
С	4787	CKSSYB104K10	D	5004-5006	UDZS6R8(B)	
Bloc	k Name: ADCC BLOCK(EH)		MISO	<u>CELLANEOUS</u>		
	,		· · · · · · · · · · · · · · · · · · ·	5001,5002 CHIP SOLID INDUCTOR	DTL1041	
	ICONDUCTORS		JA	5001-5003 HDMI CONNECTOR	AKP1318	Е
IC	4801	AD9985KSTZ-110	DE0	NOTODO		_
MISC	CELLANEOUS			SISTORS	DAD4000D0 I	
	4801,4802 CHIP BEEDS FILTER	BTX1042		5006 5058	RAB4CQ0R0J RS1/16SS4701F	
_		2.7		ther Resistors	RS1/16SS###J	
RES	ISTORS					
	4804	RS1/16SS2701F	CAP	PACITORS		
	4805-4808	RS1/16SS470J		5001	BCG1059	
	4809-4814	RAB4CQ680J	C	5003-5007,5009-5013	CKSSYB104K10	
	4815 ner Resistors	RAB4CQ103J RS1/8SQ###J	C	5014 5015,5016,5018,5019	DCH1201 CKSSYB104K10	
Oti		,	U	0010,0010,0010,0010	ULNEULGLOOM	
	<u>ACITORS</u>		Bloc	ck Name: AV_SW BLOCK(EH)		F
	4801	CKSSYB823K10				
C	4802	CKSSYB822K16		IICONDUCTORS		
U	4803-4805,4807,4809	CKSSYB104K10	IC	5101	R2S11006FT	
			KRP-M01		1	01
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Mark No. Description	Part No.	Mark No. Description	Part No.
Q 5151,5152,5161,5162 Q 5171,5172	2SA1576A 2SA1576A	CAPACITORS C 5801-5804,5807-5814	CKSRYB105K10
·	ZONIOTON	C 5815,5817,5841	CKSSYB473K16
MISCELLANEOUS L 5191-5193 CHIP BEEDS FILTER	BTX1042	C 5818,5842,5844,5846 C 5824,5861,5864,5865	CKSSYB103K16 CCSSCH560J50
	DIXIO 1 2	C 5835-5838	CCG1205
RESISTORS R 5151,5154,5161,5164	RS1/10SR510J	C 5843,5845,5847,5855	DCH1201
R 5171,5174	RS1/10SR510J	C 5851,5853,5911,5912	DCH1165
R 5191 Other Resistors	RS1/8SQ0R0J RS1/16SS###J	C 5852,5854,5856,5859 C 5857,5858	CKSSYB103K16 CCSSCH9R0D50
	1101/1000//////	C 5860,5862,5871,5873	DCH1201
CAPACITORS C 5105,5131,5152,5153	DCH1201	C 5863,5870,5872,5874	CKSSYB103K16
C 5109-5112,5138-5143	CKSRYB105K10	C 5875,5877,5886,5888	DCH1201
C 5132-5136,5151,5161	CKSSYB104K10	C 5876,5878,5885,5887 C 5901,5902,5913	CKSSYB103K16 CKSSYB102K50
C 5137 C 5162,5163,5172,5173	DCH1165 DCH1201	C 5921,5922,5941,5942	DCH1201
	01/00/1040/4/40	C 5923,5924,5943,5944	CCSSCH150J50
C 5171	CKSSYB104K10	C 5931,5932	DCH1165
Block Name: RGB_SW BLOCK(EH)		C 5933	CKSSYB682K25
SEMICONDUCTORS		C 5951,5952,5955,5956 C 5960,5962,5978	CKSSYB472K16 DCH1201
IC 5501	R2S11001FT	0 5004 5000 5077	01/00/104041/40
Q 5537	2SA1576A	C 5961,5963,5977 C 5971,5972,5980	CKSSYB104K10 CKSRYB105K10
Q 5571,5581 Q 5572,5582	HN1B04FU HN1C01FU	C 5979	CEHVAW470M6R3
		Block Name: DVB_S_TUNER BLK(EH))
MISCELLANEOUS L 5501 CHIP BEEDS FILTER	BTX1042	-	,
		SEMICONDUCTORS IC 5201	STV-0903
RESISTORS R 5513	RAB4CQ101J	⚠ D 5202	1.5SMC24A
R 5547-5549	RS1/16SS6801F	D 5203	RB060L-40
R 5554 Other Resistors	RAB4CQ0R0J RS1/16SS###J	MISCELLANEOUS	
Other nesistors	1101/1000###0	L 5202 CHIP BEEDS FILTER	BTX1042
<u>CAPACITORS</u>	01/07)/71/05//10	F 5201 FERRITE CORE F 5206.5207 FERRITE CORE	VTF1080 VTF1091
C 5510-5515 C 5516-5518	CKSRYB105K10 CCSSCH221J50	⚠ X 5201 CRYSTAL(27 MHz)	ASS1225
C 5541-5546,5549-5556	CKSSYB103K16	<u>RESISTORS</u>	
C 5547,5548 C 5557-5559,5561-5565	CCSSCH680J50 CKSSYB104K10	R 5201,5203,5205	RS1/10SR0R0J
		R 5204 R 5242	RS1/10SR103J RAB4CQ103J
C 5560,5591 C 5571,5582	DCH1201 CKSRYB474K10	R 5243,5244	RAB4CQ470J
C 5572,5581	CCG1205	Other Resistors	RS1/16SS###J
Block Name: MSP BLOCK(EH)		<u>CAPACITORS</u>	
		C 5201,5202	BCG1059
SEMICONDUCTORS	MCDECE1M OV CO	C 5203 C 5204,5212-5220	CKSSYB102K50 CKSSYB103K16
IC 5801 IC 5911,5931,5951	MSP5651M-QK-C3 NJM4565V	C 5221,5222	CCSSCH120J50
IC 5971	BH3544F	C 5225-5261	CKSSYB103K16
Q 5901 Q 5971	HN1A01FU 2SC4081	C 5262,5263	CEHVAW101M6R3
		C 5264,5265,5267-5269	CKSSYB104K10
D 5807,5808	UDZS8R2(B)	Block Name: DVB_T_TUNER BLK(EH)
MISCELLANEOUS		SEMICONDUCTORS	
⚠ X 5801 CRYSTAL(20.25 MHz)	ASS1217	IC 5301	TC7W66FU
<u>RESISTORS</u>		Q 5303	DTC124EUA
R 5806	RS1/10SR0R0J	Q 5304,5305 Q 5306	2SA1576A HN1B04FU
R 5821-5823 R 5993-5995	RAB4CQ471J RS1/8SQ0R0J	Q 5307	HN1C01FU
Other Resistors	RS1/16SS###J	Q 5308	RN1902
		⚠ D 5301	1.5SMC6.8A
102	KRP-M0	01	
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В

С

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ark No. Description	Part No.	Mark No. Description	Part No.
<u> //ISCELLANEOUS</u>		Block Name: CIMAX BLOCK(EH)	
L 5301,5303,5304 CHIP COIL	BTH1121		
L 5306-5308 CHIP BEEDS FILTER	BTX1042	<u>SEMICONDUCTORS</u>	
F 5301-5306 FERRITE CORE	VTF1080	IC 8101	TC74VHC08FTS1
F 5307,5308 INDUCTOR	CTF1557	IC 8102	CIMAXSP2L
F 5309-5313 FERRITE CORE	VTF1080	IC 8103,8104,8113	TC74VHC32FTS1
		IC 8105,8106	TC74VHCT245AFTS1
<u>ESISTORS</u>		IC 8107,8108	TC74VHCT541AFTS1
R 5302,5311,5318	RS1/10SR0R0J	•	
R 5304	RS1/8SQ0R0J	IC 8109-8112	TC74VHCT373AFT
Other Resistors	RS1/16SS###J	Q 8101-8106	DTC124EUA
APACITORS		MISCELLANEOUS	
C 5303,5316	CKSSYB103K16	F 8101,8102 FERRITE CORE	VTF1091
C 5307,5311,5324,5330	DCH1201	1 0101,01021EHHHIL OOKE	VII 1051
C 5309	CKSSYB104K10	DECICTORS	
C 5319	CEHVAW101M6R3	RESISTORS	DC1/0000D01
		R 8101-8103	RS1/8SQ0R0J
C 5322	CKSRYB682K50	R 8126,8219	RAB4CQ471J
C F200	D004004	R 8145,8152-8157,8160	RAB4CQ470J
C 5329	BCG1064	R 8150,8151	RAB4CQ103J
ock Name: COFDEM BLOCK(EF	D	R 8158,8159	RAB4CQ221J
·	-,	R 8161,8163-8165	RAB4CQ470J
EMICONDUCTORS		R 8162,8173,8174,8180	RAB4CQ104J
IC 5401	DRX3975D-QI-B1	R 8167,8175,8176,8181	RAB4CQ220J
Q 5402	UMD2N	R 8169-8172,8177,8179	RAB4CQ470J
		R 8178,8182,8196,8200	RAB4CQ101J
ISCELLANEOUS L 5401-5403 CHIP BEEDS FILTER	BTX1042	R 8183-8189,8191,8192	RAB4CQ470J
		R 8193,8197,8198,8201	RAB4CQ220J
L 5404 CHIP COIL	LCYAR82J2520	R 8194,8199,8203,8210	RAB4CQ470J
F 5402-5404 FERRITE CORE	VTF1091	R 8202	RAB4CQ101J
X 5401 CRYSTAL RESONATOR	VSS1221	R 8202 R 8211	RAB4CQ470J
<u>ESISTORS</u>			
R 5401	RS1/8SQ0R0J	Other Resistors	RS1/16SS###J
R 5434-5436	RAB4CQ470J		
R 5438	RAB4CQ471J	<u>CAPACITORS</u>	
Other Resistors	RS1/16SS###J	C 8101	CKSSYB102K50
		C 8102,8103	DCH1201
APACITORS		C 8104-8111,8114-8123	CKSSYB104K10
C 5401,5402	CCSSCH101J50	C 8112,8113	CKSRYB105K10
•		·,-··•	222.001110
C 5403	CKSRYB104K16	Block Name: CI_CARD_1 BLOCK(E	Ή)
C 5404,5417,5419,5420	CKSSYB104K10	DIOCK Haille. OI_CAND_I BLOCK(E	,
C 5408	CCSSCJ3R0C50	MICOELLANEOUS	
C 5409	CCSSCH180J50	MISCELLANEOUS JA 5601 PC CARD CONNECTOR	AKP1341
C 5411,5412	CKSSYB103K16	JA JUUT FU DAND CONNECTOR	ANT 1941
C 5415,5416	CCSSCH8R0D50	<u>RESISTORS</u>	
,			RS1/16SS###J
C 5418,5421,5427,5429	CKSSYB102K50	All Resistors	no i/1000###J
C 5422,5424	CCG1205	CADACITODO	
C 5423,5425,5426,5428	CKSSYB104K10	<u>CAPACITORS</u>	
0 5400	01/06/17 12 11:12	C 5604	CEHVAW470M16
C 5430	CKSSYB104K10	C 5605	DCH1201
ock Name: TS_SELECT BLOCK	(EH)	Block Name: VBI_SLICER BLOCK(I	Ξ Η)
EMICONDUCTORS		<u>SEMICONDUCTORS</u>	
IC 8001-8003	TC74LCX157FTS1	IC 5701	TC90173FG
IC 8051	TC74LCX245FTS1	D 5701	HSM107S-E
ESISTORS		MISCELLANEOUS	
R 8005,8006	RAB4CQ151J	L 5701,5702 CHIP BEEDS FILTER	BTX1042
R 8053-8055,8057	RAB4CQ470J	L 3701,3702 UNIT DEEDS FILIEN	DIA1042
R 8056,8058		DECICTORS	
	RAB4CQ103J	RESISTORS	
•	RS1/16SS###J	R 5701	RS1/8SQ0R0J
Other Resistors			
Other Resistors		R 5714,5715	RAB4CQ151J
	CKSSYB104K10		

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Mark No. Description	Part No.	Mark No. Description	Part No.
CAPACITORS C 5701	CKSRYB474K10	C 6166,6167,6170-6173 C 6169,6174,6175,6187	CKSSYB104K10 DCH1201
C 5704	CCSSCH680J50	0 0100,0174,0170,0107	DOMEZON
C 5705-5712,5715-5720	CKSSYB104K10	C 6176-6179,6184-6186	CKSSYB104K10
C 5714	DCH1201	Block Name: 7404_DDR BLOCK(EH)	
Block Name: 7404_0 BLOCK(EH)		<u>SEMICONDUCTORS</u>	
<u>SEMICONDUCTORS</u>		IC 6201-6204	EDD5116AFTA-5B-E
IC 6001	BCM7404XKPB11G		
MICOELLANGOLIO		MISCELLANEOUS	DTV4040
MISCELLANEOUS F 6001 FERRITE CORE	VTF1084	L 6201,6202 CHIP BEEDS FILTER	BTX1042
JA 6002 RJ45 CONNECTOR TRNS	VKN2078	<u>RESISTORS</u>	
ON GOOD THE TO GOINNED TON THING	VI(1/2070	R 6259-6262,6283	RAB4CQ510J
<u>RESISTORS</u>		R 6263,6267-6269	RAB4CQ220J
R 6002-6004	RS1/10SR750J	R 6264-6266,6270,6284	RAB4CQ101J
R 6007-6009,6016	RAB4CQ101J	R 6271-6282,6286-6290	RAB4CQ220J
R 6014	RS1/16SS1101F	R 6285,6291-6295,6301	RAB4CQ101J
R 6018	RS1/8SQ0R0J	R 6296-6300,6302-6309	RAB4CQ220J
R 6019	RS1/16SS1001F	R 6310,6311,6315,6316	RAB4CQ101J
R 6021,6022,6039,6040	RS1/16SS49R9F	R 6312-6314	RAB4CQ220J
R 6037	RS1/16SS1002F	Other Resistors	RS1/16SS###J
R 6045,6066-6069,6072	RAB4CQ470J		
R 6051,6071	RAB4CQ472J	<u>CAPACITORS</u>	
R 6073	RS1/10SR75R0F	C 6201-6204	CKSSYB471K50
		C 6205	BCG1059
R 6079-6084	RAB4CQ101J	C 6207,6208,6210-6222	CKSSYB104K10
Other Resistors	RS1/16SS###J	C 6209,6223,6249 C 6224-6248,6250-6253	DCH1201 CKSSYB104K10
<u>CAPACITORS</u>		0 0224-0240,0230-0233	UN3310104N10
C 6001	CKSSYB102K50	Block Name: 7404_FLASH BLOCK(E	H)
C 6004	CCSSCH150J50	_ ,	,
C 6008,6015,6016	DCH1201	<u>SEMICONDUCTORS</u>	
C 6009-6011,6013,6014	CKSSYB104K10	IC 6401	TC74VHC02FTS1
Block Nomes 7404 4 BLOCK/FU		IC 6402	PST3628UR
Block Name: 7404_1 BLOCK(EH)		D 6401	1SS352
<u>SEMICONDUCTORS</u>		<u>RESISTORS</u>	
IC 6102	LP2995M	R 6457-6466	RAB4CQ472J
		R 6467	RAB4CQ103J
MISCELLANEOUS		R 6471	RS1/10SR0R0J
L 6101 INDUCTOR	LCTAW2R2J2520	Other Resistors	RS1/16SS###J
L 6103 CHIP BEEDS FILTER	BTX1042	CARACITORS	
L 6111-6118 CHIP BEEDS FILTER F 6101-6111 FERRITE CORE	BTX1042 VTF1084	CAPACITORS C 6401	CKSSYB103K16
∴ X 6101 CRYSTAL RESONATOR	BSS1134	C 6402-6405	CKSSYB104K10
Z. X. OTOT OTTOTAL RESOLUTION	D001101	C 6406	CKSSYB473K16
<u>RESISTORS</u>			
R 6133,6134	RS1/10SR3010F	Block Name: AV_IO BLOCK(EH)	
R 6156,6157	RAB4CQ472J	OF MICONDUCTORS	
Other Resistors	RS1/16SS###J	SEMICONDUCTORS	0004004
<u>CAPACITORS</u>		Q 7501,7502,7505,7506 Q 7503,7504	2SC4081 UMD2N
C 6101,6102,6180-6183	BCG1059	D 7501-7505,7507	UDZS5R1(B)
C 6103,6104	CCSSCH120J50	D 7506,7508,7515,7516	UDZS12(B)
C 6105,6106,6109-6112	CKSSYB103K16	D 7509-7513	UDZS5R1(B)
C 6113,6114	CCSSCH9R0D50		
C 6115,6118-6120,6123	CKSSYB103K16	MISCELLANEOUS	
0 0440 0447 0400 0400	1001100	L 7502 CHIP COIL	BTH1103
C 6116,6117,6188-6190	ACG1122 CKSSYB102K50	F 7501-7503 INDUCTOR	CTF1557
C 6121,6122,6125,6128 C 6124,6126,6127	CKSSYB103K16	F 7504-7511 CHIP FERRITE BEADS JA 7501 OPT. LINK OUT 12MB/S	ATF1229 VKS1001
C 6129.6134.6135	CKSSYB102K50	JA 7501 OFT. LINK OUT 12MB/5 JA 7502 RGB CONNECTOR	AKP1265
C 6130-6133	CKSSYB103K16	ON 1992 HAD CONNECTOR	7111 1200
		JA 7503 MINI JACK(4P)	AKN1073
C 6136,6137	ACH1421		
C 6139-6158,6161-6164	CKSSYB104K10	RESISTORS	
C 6159,6160,6165,6168	DCH1201	R 7501,7503-7505,7507	RS1/10SR151J
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Mark No. Description	Part No. RS1/10SR151J	Mark No. Description Block Name: ARIA_DDR BLOCK(EH)	Part No.	
R 7509 R 7519-7522,7530-7532 R 7541	RS1/10SR0R0J RS1/10SR75R0F RS1/10SR75R0F	SEMICONDUCTORS IC 6702-6704	EDD1232ABBH-5C-E	Α
Other Resistors	RS1/16SS###J	MISCELLANEOUS L 6701-6703 CHIP BEEDS FILTER	BTX1042	
<u>CAPACITORS</u>		E 0701 0700 01111 BEEDO FIETER	DIXIOTE	
C 7501-7504 C 7505-7511,7513,7514 C 7512 C 7515 C 7516	CCG1205 CKSSYB102K50 CKSSYB103K16 CCSSCH680J50 CKSSYB102K50	RESISTORS R 6745-6780 Other Resistors	RAB4CQ470J RS1/16SS###J	•
C 7519-7522,7526-7529 C 7525,7530 C 7531 C 7532,7533	CKSRYB105K10 CCSRCH101J50 CKSRYB105K10 ACH1454	CAPACITORS C 6701-6707 C 6708,6710,6712,6714 C 6718 C 6722-6745	CKSSYB104K10 DCH1201 CKSSYB103K16 CKSRYB105K10	В
C 7534-7536	CKSSYB471K50	Block Name: IF_UCOM BLOCK(EH)		
Block Name: ARIA_0 BLOCK(EH)		SEMICONDUCTORS		
SEMICONDUCTORS IC 6501 MISCELLANEOUS	PD6568A	IC 6801 IC 6802-6804 IC 6805 IC 6806	PST3628UR TC74VHC126FTS1 TC74VHC08FTS1 TC74VHC00FTS1	•
L 6501-6503 CHIP BEEDS FILTER L 6504,6505 CHIP BEEDS FILTER L 6506-6509 INDUCTOR	BTX1042 BTX1039 LCYC1R0K1608	Q 6801-6803,6812-6814 Q 6804,6805 Q 6806,6807,6817	DTC124EUA 2SC4081 2SA1576A	С
⚠ X 6501 CRYSTAL(27 MHz) RESISTORS	ASS1225	Q 6808 Q 6809,6810 Q 6816,6819,6820	DTA124EUA HN1C01FU DTC124EUA	
R 6501-6504 R 6506 R 6514,6515 Other Resistors	RS1/8SQ0R0J RAB4CQ220J RAB4CQ103J RS1/16SS###J	Q 6901 D 6801-6805	UMD2N 1SS352	•
CAPACITORS C 6501,6504-6513,6518 C 6502,6514,6523	CKSSYB104K10 DCH1201	MISCELLANEOUS ⚠ X 6801 CERAMIC OSCILLATOR ⚠ X 6802 CRYSTAL OSCILLATOR	CSS1616 ASS1212	
C 6503,6515,6516 C 6517 C 6519-6522,6524	CKSRYB105K10 CCG1232 CKSRYB105K10	RESISTORS R 6802,6806 R 6880,6885 R 6883	RS1/8SQ0R0J RAB4CQ103J RAB4CQ473J	D
C 6525-6528 C 6529-6533,6578 C 6576 C 6577	CKSSYB104K10 CKSRYB105K10 CCSSCH100D50 CCSSCH120J50	R 6884 R 6893,6894,6896	RAB4CQ471J RS1/10SR122J	_
C 6580-6587,6589-6600	CKSRYB105K10	R 6895 Other Resistors	RS1/10SR220J RS1/16SS###J	
Block Name: ARIA_1 BLOCK(EH)		CAPACITORS C 6801	CKSSYB102K50	
MISCELLANEOUS L 6601 CHIP BEEDS FILTER ⚠ F 6601-6616 FERRITE BEADS ARRAY	BTX1042 ATF1228	C 6802 C 6803,6804 C 6805,6806 C 6807,6809,6811	CKSSYB472K16 CKSSYB471K50 CCSSCH8R0D50 CKSSYB104K10	E
RESISTORS R 6603,6604,6607 R 6609-6611 R 6613-6627,6629 R 6628 R 6630	RS1/16SS2201F RS1/16SS2201F RAB4CQ101J RAB4CQ121J RAB4CQ220J	C 6808,6812 C 6810 C 6814-6824 Block Name: EMMA2 BLOCK(EH)	DCH1201 CKSSYB103K16 CKSSYB104K10	•
Other Resistors	RS1/16SS###J	SEMICONDUCTORS		
CAPACITORS C 6615 C 6616-6629 C 6632 C 6634	DCH1201 CKSSYB104K10 CCSSCH221J50 CKSRYB105K10	IC 7002 IC 7003 IC 7004 IC 7005 IC 7006	TC74VHC08FTS1 UPD61123F1-100KA3A BR24L64F-W TC7WHU04FU TC74HC4066AFT	F
U 6634 ■ 5 ■	6	KRP-M01	105 8	•

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Mark No. Description	Part No. 2SJ461A	Mark No. Description	Part No.
Q 7001,7003,7006	DTC124EUA	Block Name: DP_TX BLOCK	
Q 7005,7008	SSM6N17FU	SEMICONDUCTORS	
Q 7007,7010	UMD2N	IC 7601	S25FL016A0LMF013
D 7010	1SS301	IC 7602	GM60028H-CG
		IC 7603	GMT2404HROM
MISCELLANEOUS	DT://0.40		
L 7001-7003 CHIP BEEDS FILTER F 7001 FERRITE CORE	BTX1042	MISCELLANEOUS	
F 7001 FERRITE CORE F 7002 CHIP FERRITE BEADS	VTF1091 ATF1212	L 7601,7602 CHIP INDUCTOR	ATH1254
⚠ X 7001 CRYSTAL(27 MHz)	ASS1225	L 7608-7610 CHIP BEEDS FILTER F 7601-7603 CHIP FERRITE BEADS	BTX1042 ATF1211
EX TOOL ON ONE (ET MILE)	71007220	JA 7601 DP CONNECTOR	AKP1340
<u>RESISTORS</u>		⚠ X 7601 CRYSTAL(27 MHz)	ASS1225
R 7026-7028	RS1/16SS2000D		
R 7029,7036	RS1/16SS6200D	<u>RESISTORS</u>	
R 7033	RS1/16SS3300D	R 7604	RS1/8SQ0R0J
R 7035 R 7045,7067,7070,7073	RS1/16SS2200D RAB4CQ103J	R 7645	RS1/10SR2490F
11 7043,7007,7070,7073	11AD40Q1000	R 7649-7657,7662 R 7658	RS1/16SS10R0F RAB4CQ0R0J
R 7060-7064,7066,7068	RAB4CQ101J	Other Resistors	RS1/16SS###J
R 7065	RAB4CQ470J	other resistors	1101/1000###0
R 7069,7071,7083,7084	RAB4CQ101J	<u>CAPACITORS</u>	
R 7072	RAB4CQ221J	C 7601,7608-7614,7616	CKSSYB104K10
R 7074	RAB4CQ103J	C 7602	CKSSYB471K50
R 7075	RAB4CQ220J	C 7603	BCG1059
R 7073	RAB4CQ222J	C 7604,7605	CCSSCH120J50
R 7087-7091	RAB4CQ101J	C 7606,7607,7617,7619	DCH1201
Other Resistors	RS1/16SS###J	C 7618,7621,7624-7627	CKSSYB104K10
		C 7620,7639	DCH1201
<u>CAPACITORS</u>		C 7629-7638,7640-7650	CKSSYB104K10
C 7001,7003-7011	CKSRYB105K10		
C 7014 C 7029	CKSSYB102K50 CCSSCH100D50	Unit Name:FUKUGO ASSY((EU MR)
C 7029 C 7030	CCSSCH100D50 CCSSCH120J50		
C 7031,7032	CCSSCH470J50	MISCELLANEOUS	
		3001 SCREW	BPZ26P050FTC
C 7035-7040,7043	CKSSYB104K10	Unit Name DEAD IO ACCV	/ELLMD\
C 7041,7044,7049	DCH1201	Unit Name: REAR IO ASSY	
C 7045-7048,7050,7051	CKSSYB104K10	Block Name: BOARD_IF BLOCK(EU	MK)
Block Name: EMMA2_MEM BLOCK(EH)	MISCELLANEOUS	
•	•	CN 8701 50P CONNECTER	AKM1399
<u>SEMICONDUCTORS</u>			
IC 7201	EDD5116AFTA-5B-E	RESISTORS	
IC 7203	LP2995M	All Resistors	RS1/16SS###J
MISCELLANEOUS		Block Name: REAR_IO_0 BLOCK(EL	I)
L 7201 CHIP BEEDS FILTER	BTX1042	Block Name: NEAR_10_0 BEOOK(EC	')
		<u>SEMICONDUCTORS</u>	
RESISTORS	DO1/100015005	Q 8801,8802	2SC4081
R 7213 R 7243-7246,7257-7259	RS1/16SS1500F	Q 8803	UMD2N
R 7243-7246,7257-7259 R 7247-7254,7256	RAB4CQ101J RAB4CQ220J	D 8801,8804	UDZS12(B)
R 7255,7267	RAB4CQ103J	D 8803,8805	UDZS5R1(B)
R 7260,7261,7268-7270	RAB4CQ560J	MISCELLANEOUS	
		⚠ F 8801 CHIP FERRITE BEADS	ATF1229
R 7262,7272-7275	RAB4CQ101J	JA 8801 RGB CONNECTOR	AKP1266
Other Resistors	RS1/16SS###J		
<u>CAPACITORS</u>		<u>RESISTORS</u>	
C 7201	CKSRYB105K10	R 8801	RS1/8SQ151J
C 7202-7204	BCG1059	R 8802 R 8803,8813	RS1/8SQ121J RS1/10SR75R0F
C 7205,7206,7225	DCH1201	Other Resistors	RS1/16SS###J
C 7207-7221,7223	CKSSYB104K10	Other resistors	1101/1000###0
C 7226	ACH1421	<u>CAPACITORS</u>	
		C 8801	CKSRYB105K10
		C 8802,8805-8808	CKSSYB102K50
		C 8803	CCSSCH680J50
		C 8804	CKSSYB103K16
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Mark No. Description C 8809,8810	Part No. CCG1205	Mark No. Description R 8517	Part No. RAB4CQ222J
C 8811	ACH1454	Other Resistors	RS1/16SS###J
Block Name: REAR_IO_1 BLOCK(EU) SEMICONDUCTORS IC 8301 Q 8301,8306 Q 8302-8304 D 8301	MAX3232CPW UMD2N 2SD2114K 1SS301	CAPACITORS C 8501,8510-8512 C 8502,8509 C 8503,8504,8513,8514 C 8507,8508 C 8515	CKSRYB105K10 CKSSYB104K10 CKSSYB102K50 ACH1454 CKSRYB104K16
MISCELLANEOUS		Unit Name: CI CARD ASSY	(EU WK)
JA 8301 4P PIN JACK RA JA 8303 4P PIN JACK RA CN 8302 9P D-SUB SOCKET	AKB1359 AKB1358 AKP1213	MISCELLANEOUS JA 8601 PC CARD CONNECTOR CN 8601,8602 40P CONNECTER	AKP1341 AKM1398
RESISTORS R 8301,8302,8305 R 8318,8320 R 8332 Other Resistors	RS1/10SR75R0F RS1/10SR221J RAB4CQ101J RS1/16SS###J	RESISTORS R 8602-8605 R 8614,8615 Other Resistors	RAB4CQ330J RAB4CQ470J RS1/16SS###J
<u>CAPACITORS</u>		CAPACITORS C 8604	CEHVAW470M16
C 8301-8303 C 8309,8310,8313 C 8311,8312	CKSRYB105K10 CKSSYB471K50 CKSSYB102K50	C 8605 Unit Name: KEY ASSY(EU I	DCH1201
C 8314,8317-8321 C 8315,8316	CKSSYB104K10 CCG1205	•	vii i <i>j</i>
		SEMICONDUCTORS Q 9401	HN1B04FU
Unit Name: LED ASSY(EU M	R)	TH 9401	TH05-3H103F
SEMICONDUCTORS D 9401	SML-521MDW	MISCELLANEOUS	
D 9402 D 9403	TLRV1022 SMLE12BC7T(NP)	⚠ L 9401-9406 CHIP SOLID INDUCTOR S 9401-9406 PUSH SWITCH CN 9401 L-PLUG(7P)	QTL1013 CSG1155 KM200NA7L
MISCELLANEOUS ⚠ L 9408-9410 CHIP SOLID INDUCTOR CN 9402 L-PLUG(6P)	QTL1013 KM200NA6L	RESISTORS R 9407 Other Resistors	RS1/10SR4701F RS1/16SS###J
RESISTORS All Resistors	RS1/10SR###J	CAPACITORS C 9409	CKSSYB103K16
CAPACITORS C 9404,9406,9407	CKSSYB103K16	C 9410,9411	CKSSYB104K10
Unit Name: FRONT IO ASSY	(EU MR)		
<u>SEMICONDUCTORS</u>	-		
IC 8501 IC 8502 Q 8501 Q 8502-8504 D 8507	BR24L01AFJ-W TC74VHC08FTS1 DTC124EUA 2SC4081 1SS301		
D 8508	UDZS5R1(B)		
MISCELLANEOUS JA 8501 PIN JACK(3P) JA 8503 MINI JACK CN 8501 FFC CONNECTOR 26P CN 8503 15P D-SUB SOCKET	AKB1303 AKN1085 AKM1441 AKP1214		
RESISTORS R 8501,8508 R 8506,8510-8512 R 8514 R 8515,8516	RST1/2SP120J RS1/10SR75R0F RAB4CQ473J RAB4CQ101J		
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ORDER NO. ARP3508

MEDIA RECEIVER

KRP-M01

THIS MANUAL IS APPLICABLE TO THE FOLLOWING MODEL(S) AND TYPE(S).

Model	Туре	Power Requirement	Remarks
KRP-M01	WYSIXK5	AC 220 V to 240 V	
KRP-M01	WYSXJ5	AC 220 V to 240 V	

This service manual should be used together with the following manual(s).

Model No.	Order No.	Remarks
KRP-M01	ARP3509	SCHEMATIC DIAGRAM, PCB CONNECTION DIAGRAM, PCB PARTS LIST, etc.



PIONEER CORPORATION 4-1, Meguro 1-chome, Meguro-ku, Tokyo 153-8654, Japan PIONEER ELECTRONICS (USA) INC. P.O. Box 1760, Long Beach, CA 90801-1760, U.S.A. PIONEER EUROPE NV Haven 1087, Keetberglaan 1, 9120 Melsele, Belgium PIONEER ELECTRONICS ASIACENTRE PTE. LTD. 253 Alexandra Road, #04-01, Singapore 159936 © PIONEER CORPORATION 2008

This service manual is intended for qualified service technicians; it is not meant for the casual do-it-yourselfer. Qualified technicians have the necessary test equipment and tools, and have been trained to properly and safely repair complex products such as those covered by this manual.

Improperly performed repairs can adversely affect the safety and reliability of the product and may void the warranty. If you are not qualified to perform the repair of this product properly and safely, you should not risk trying to do so and refer the repair to a qualified service technician.

WARNING

This product contains certain electrical parts contain chemicals which are known to the State of California to cause cancer, birth defects or other reproductive harm.

Health & Safety Code Section 25249.6 - Proposition 65

NOTICE

(FOR CANADIAN MODEL ONLY)

Fuse symbols (fast operating fuse) and/or (slow operating fuse) on PCB indicate that replacement parts must be of identical designation.

REMARQUE

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(POUR MODÈLE CANADIEN SEULEMENT)

C Les symboles de fusible (fusible de type rapide) et/ou (fusible de type lent) sur CCI indiquent que les pièces de remplacement doivent avoir la même désignation.

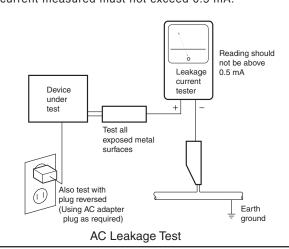
(FOR USA MODEL ONLY)

1. SAFETY PRECAUTIONS

The following check should be performed for the continued protection of the customer and service technician.

LEAKAGE CURRENT CHECK

Measure leakage current to a known earth ground (water pipe, conduit, etc.) by connecting a leakage current tester such as Simpson Model 229-2 or equivalent between the earth ground and all exposed metal parts of the appliance (input/output terminals, screwheads, metal overlays, control shaft, etc.). Plug the AC line cord of the appliance directly into a 120 V AC 60 Hz outlet and turn the AC power switch on. Anycurrent measured must not exceed 0.5 mA.



ANY MEASUREMENTS NOT WITHIN THE LIMITS OUTLINED ABOVE ARE INDICATIVE OF A POTENTIAL SHOCK HAZARD AND MUST BE CORRECTED BEFORE RETURNING THE APPLIANCE TO THE CUSTOMER.

2. PRODUCT SAFETY NOTICE

Many electrical and mechanical parts in the appliance have special safety related characteristics. These are often not evident from visual inspection nor the protection afforded by them necessarily can be obtained by using replacement components rated for voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in this Service Manual.

Electrical components having such features are identified by marking with a \triangle on the schematics and on the parts list in this Service Manual.

The use of a substitute replacement component which does not have the same safety characteristics as the PIONEER recommended replacement one, shown in the parts list in this Service Manual, may create shock, fire, or other hazards.

Product Safety is continuously under review and new instructions are issued from time to time. For the latest information, always consult the current PIONEER Service Manual. A subscription to, or additional copies of, PIONEER Service Manual may be obtained at a nominal charge from PIONEER.

2

[Important Check Points for Good Servicing]

In this manual, procedures that must be performed during repairs are marked with the below symbol. Please be sure to confirm and follow these procedures.

Product safety



Please conform to product regulations (such as safety and radiation regulations), and maintain a safe servicing environment by following the safety instructions described in this manual.

① Use specified parts for repair.

Use genuine parts. Be sure to use important parts for safety.

2 Do not perform modifications without proper instructions.

Please follow the specified safety methods when modification(addition/change of parts) is required due to interferences such as radio/TV interference and foreign noise.

3 Make sure the soldering of repaired locations is properly performed.

When you solder while repairing, please be sure that there are no cold solder and other debris. Soldering should be finished with the proper quantity. (Refer to the example)

Make sure the screws are tightly fastened.

Please be sure that all screws are fastened, and that there are no loose screws.

5 Make sure each connectors are correctly inserted.

Please be sure that all connectors are inserted, and that there are no imperfect insertion.

6 Make sure the wiring cables are set to their original state.

Please replace the wiring and cables to the original state after repairs. In addition, be sure that there are no pinched wires, etc.

Make sure screws and soldering scraps do not remain inside the product.

Please check that neither solder debris nor screws remain inside the product.

There should be no semi-broken wires, scratches, melting, etc. on the coating of the power cord.

Damaged power cords may lead to fire accidents, so please be sure that there are no damages. If you find a damaged power cord, please exchange it with a suitable one.

There should be no spark traces or similar marks on the power plug.

When spark traces or similar marks are found on the power supply plug, please check the connection and advise on secure connections and suitable usage. Please exchange the power cord if necessary.

10 Safe environment should be secured during servicing.

When you perform repairs, please pay attention to static electricity, furniture, household articles, etc. in order to prevent injuries. Please pay attention to your surroundings and repair safely.

2. Adjustments



To keep the original performance of the products, optimum adjustments and confirmation of characteristics within specification. Adjustments should be performed in accordance with the procedures/instructions described in this manual.

3. Lubricants, Glues, and Replacement parts



Use grease and adhesives that are equal to the specified substance. Make sure the proper amount is applied.

4. Cleaning



For parts that require cleaning, such as optical pickups, tape deck heads, lenses and mirrors used in projection monitors, proper cleaning should be performed to restore their performances.

5. Shipping mode and Shipping screws



To protect products from damages or failures during transit, the shipping mode should be set or the shipping screws should be installed before shipment. Please be sure to follow this method especially if it is specified in this manual.

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1. SERVICE PRECAUTIONS

1.1 NOTES ON SOLDERING

- For environmental protection, lead-free solder is used on the printed circuit boards mounted in this unit.
 Be sure to use lead-free solder and a soldering iron that can meet specifications for use with lead-free solders for repairs accompanied by reworking of soldering.
- Compared with conventional eutectic solders, lead-free solders have higher melting points, by approximately 40 °C.
 Therefore, for lead-free soldering, the tip temperature of a soldering iron must be set to around 373 °C in general, although the temperature depends on the heat capacity of the PC board on which reworking is required and the weight of the tip of the soldering iron.

Do NOT use a soldering iron whose tip temperature cannot be controlled.

Compared with eutectic solders, lead-free solders have higher bond strengths but slower wetting times and higher melting temperatures (hard to melt/easy to harden).

The following lead-free solders are available as service parts:

Parts numbers of lead-free solder:

GYP1006 1.0 in dia.

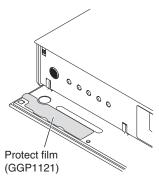
GYP1007 0.6 in dia.

GYP1008 0.3 in dia.

1.2 NOTES SPECIFIC TO THIS PRODUCT

1. Notes before starting repair

- The high-gloss resin parts of the exterior of this product are easily scratched. During disassembly and reassembly of this
 product, be careful not to scratch the exterior.
- If the door of this product is pressed firmly from the front or when the KEY Assy and LED Assy are reassembled, print of the
 front-panel operating section may be transferred to the inside surface of the door. To avoid this, be sure to attach the protect
 film to the inside surface of the door before repairing. If protect film is not available, slip a cleaning cloth or the like inside the
 door for protection.
- Remove the attached protect film after product installation is completed. If the repaired product is to be delivered to the customer's home or a dealer, leave the protect film attached.



2. Note on Disassembly/Reassembly

1) Fixing screws for the HDMI connector and system cable connector

For tightening the screws for the HDMI connector and system cable connector, do not use an electric screwdriver. Tighten them manually. If they are tightened too forcefully with an electric screwdriver, the screw heads may be damaged, in which case the screws cannot be loosened/tightened any more.

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2. SPECIFICATIONS 2.1 ACCESSORIES

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Power cable
 Only the power cable appropriate for your country or region is supplied:

 (ADG1214)

 (ADG1223)

 For Europe, except UK and Republic of Ireland

 WYSIXK5 only

 *Ferrite core (ATX1039)

 Cable tie (for ferrite core)

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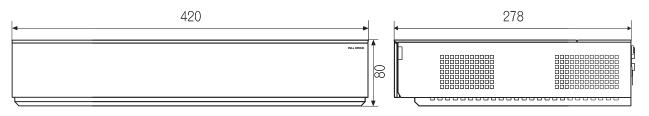
Item			Media Receiver, model: KRP-M01		
Colour Sys	stem		PAL/SECAM/NTSC 3.58/NTSC 4.43/PAL 60		
TV	Receiving S	iystem	B/G, D/K, I, L, L'		
Function	Tuner	VHF/UHF	E2-E69 ch, F1-F6 ch, I21-I69 ch, IR A-IR J ch		
(Analogue)		CATV	Hyper-band, S1-S41 ch		
	Auto Channel Preset		99 ch, Auto Preset, Auto Label, Auto Sort		
	STEREO		NICAM/A2		
TV	Receiving S	ystem	DVB-T(2K/8K COFDM)		
Function	Tuner	VHF/UHF	VHF Band III (170 MHz to 230 MHz) and UHF Band IV, V (470 MHz to 862 MHz)		
(Digital)	Auto Chann	nel Preset	999 ch, Auto Preset, Auto Label, Auto Sort		
	STEREO		MPEG layer I/II, Dolby Digital, Dolby Digital Plus, HE-AAC v1		
TV	Receiving S	ystem	DVB-S, DVB-S2		
Function	IF Tuner		950 MHz to 2150 MHz		
(Satellite)	Auto Channel Preset		5000 ch, Auto Preset, Auto Label, Auto Sort		
	STEREO		MPEG layer I/II, Dolby Digital, Dolby Digital Plus, HE-AAC v1		
Terminals	Rear	INPUT 1	SCART (AV in, RGB in, TV out), HDMI in *1		
		INPUT 2	SCART (AV in/out, S-Video in, AV link *2), Component Video in, AUDIO in		
		INPUT 3	SCART (AV in/out, S-Video in, RGB in, AV link ^{*2}), HDMI in ^{*1}		
		INPUT 4	HDMI in ^{*1}		
		CONTROL OUT	1		
		SYSTEM CABLE	1		
		Antenna	75 Ω Din Type for VHF/UHF in/SAT (Satellite) in		
		AUDIO OUT	AUDIO out (Fixed)		
		SUB WOOFER OUT	Variable		
		DIGITAL OUT	Digital audio output (Optical)		
		LAN (10/100)	1		
	Front	INPUT 5	Video in, HDMI in ^{*1}		
		PC INPUT	Analogue RGB		
		INPUT 5/PC INPUT	Audio in		
		USB	USB in ^{*3}		
		PHONES	16 Ω to 32 Ω recommended		
		COMMON INTERFACE	2, CA Module		
		Power Requirements	220 V to 240 V AC, 50 Hz/60 Hz, 52 W (0.4 W Standby)		
		Weight	4.5 kg (9.9 lbs)		

^{*1} This conforms to HDMI 1.3 (Deep Colour) and HDCP1.1. HDMI (High-Definition Multimedia Interface) is a digital interface that handles both video and audio using a single cable. HDCP (High-bandwidth Digital Content Protection) is a technology used to protect copyrighted digital contents that use the Digital Visual Interface (DVI).

Design and specifications are subject to change without notice.

Dimensions (Media Receiver)

KRP-M01 Unit: mm



KRP-M01

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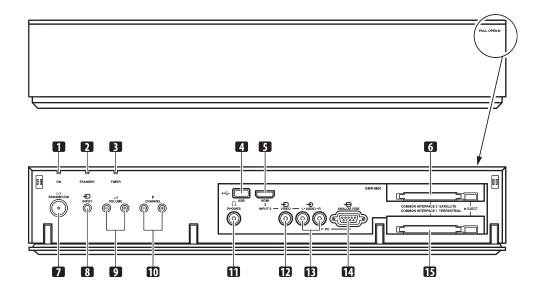
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^{*2} Switchable from menu.

^{*3} This conforms to USB 1.1 and 2.0 specifications.

(Front)

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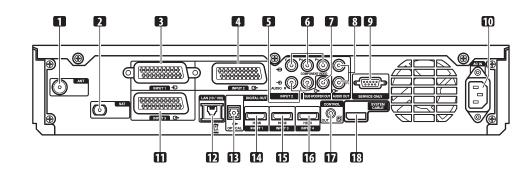


- Power ON indicator
- STANDBY indicator
- 3 TIMER indicator
- USB port
- INPUT 5 terminal (HDMI)
- COMMON INTERFACE 2 SATELLITE slot
- STANDBY/ON button
- INPUT button

- VOLUME Up/Down buttons
- 10 CHANNEL Up/Down buttons 11 PHONES output terminal

- 12 INPUT 5 terminal (Video)13 INPUT 5/PC INPUT terminals (Audio)
- 14 PC INPUT terminal (Analogue RGB)
 15 COMMON INTERFACE 1 TERRESTRIAL slot

(Rear)



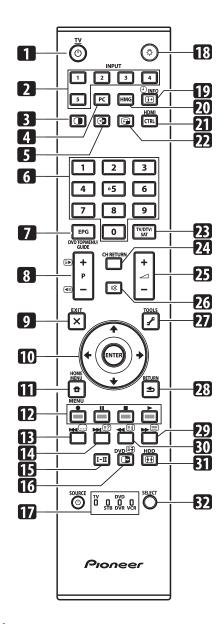
- ANT (Antenna) input terminal
- SAT (Satellite) input terminal INPUT 1 terminal (SCART)
- INPUT 2 terminal (SCART)
- INPUT 2 terminals (Audio)
 INPUT 2 terminals (COMPONENT VIDEO: Y, P_B, P_R)
- SUB WOOFER OUT terminal
- AUDIO OUT terminals
- RS-232C terminal (SERVICE ONLY) (used for factory setup)

- 10 AC IN terminal
- INPUT 3 terminal (SCART) LAN (10/100) port 11
- DIGITAL OUT terminal (OPTICAL)
- INPUT 1 terminal (HDMI) INPUT 3 terminal (HDMI) 14
- 15
- INPUT 4 terminal (HDMI) 16
- CONTROL OUT terminal 17
- 18 SYSTEM CABLE terminal

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■ Remote Control Unit

This section describes the functions of the buttons available when the TV mode has been selected by using the **SELECT** button.



- 1 **OTV**: Turns on the power to the flat screen TV or places it into the standby mode.
- 2 INPUT: Selects an input source of the flat screen TV. ("INPUT 1", "INPUT 2", "INPUT 3", "INPUT 4", "INPUT 5")
- 3 Switches the screen mode among 2-screen, picture-in-picture, and single-screen.
- **4 PC:** Selects the PC terminal as an input source.
- 5 Switches between the two screens when in the 2-screen or picture-in-picture mode.

- **6 0 to 9:** TV/External input mode: Selects a channel. Teletext mode: Selects a page.
- Turns the power on when the STANDBY indicator lights red.
- **7 EPG:** Displays the Electronic Programme Guide in DTV/SAT (Satellite) input mode.
- 8 P+/P-: TV/External input mode: Selects a channel.
- **9 X EXIT:** Returns to the normal screen in one step.
- 10 ↑/↓/←/→: Selects a desired item on the setting screen. ENTER: Executes a command.

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- 11 HOME MENU: Displays the HOME MENU screen.
- **12 Colour (RED/GREEN/YELLOW/BLUE):**Controls a BD player for HDMI Control functions only.
- 13 : Jumps to Teletext subtitle page.
 Turns subtitle on and off in DTV input mode depending on the broadcast.
- 14 (E?): Displays hidden characters.
- **15** $\mathbf{I} \mathbf{II}$: Sets the sound multiplex mode.
- 16 P:: TV/External input mode: Freezes a frame from a moving image. Press again to cancel the function.
 - E : Teletext mode: Stops updating Teletext pages. Press again to release the hold mode.
- 17 TV, STB, DVD/DVR, VCR: These indicators show the current selection and status when you control other connected equipment, using the supplied remote control unit.
- 18 : Lights up buttons.

 Lights turn off if no operations are performed within five seconds. This is used for remote control use in dark locations.
- 19 (+) (-) INFO: Displays the channel information. Displays the banner information.
- **20 HMG (Home Media Gallery):** Displays the Home Media Gallery screen.
- 21 HDMI CTRL: Displays the HDMI Control menu.
- **22** Moves the location of the small screen when in the picture-in-picture mode.
- **23 TV/DTV/SAT:** Switches the mode among TV, DTV and SAT.
- 24 CH RETURN: Returns to the previous channel.
- 25 ____ +/___ -: Sets the volume.
- **26** W: Mutes the sound.
- 27 F TOOLS: Displays the TOOLS Menu.
- 28 TETURN: Restores the previous menu screen.
- 29 : Selects the Teletext mode (all TV image, all TEXT image, TV/TEXT image).
- **30** Displays an Index page for the CEEFAX/FLOF format. Displays a TOP Over View page for the TOP format.
- 31 (F): Selects the screen size.
- **32 SELECT:** Switches the selection among TV, STB, DVD/DVR, and VCR, so that you can control other connected equipment, using the supplied remote control unit.



 When using the remote control unit, point it at the display panel.

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3. BASIC ITEMS FOR SERVICE 3.1 CHECK POINTS AFTER SERVICING

Items to be checked after repair (PDP)

To ensure the quality of the product after repair, check the recommended items shown below:

No.	Procedures	Item to be checked
1	Check if all the symptoms pointed out by the customer have been addressed.	The symptoms in question must not be reproduced.
2	Connect the peripheral equipment.	Connect all external peripheral equipment as originally connected and check if the connections are correct.
3	Check the video and audio.	Tune in to the stations that the customer would normally receive and check if video and audio are normal.
4	Check the huttons and controls	Use the buttons and controls on the remote control unit and main unit and check if they operate properly.
5	Check the cabinet.	Check for any scratches or dirt that have been made or attached on the cabinet after receiving the product for repair.

See the table below for the items to be checked regarding video and audio:

Item to be checked regarding video	Item to be checked regarding audio
Block noise	Distortion
Horizontal noise	Noise
Dot noise	Volume too low
Disturbed image (video jumpiness)	Volume too high
Too dark	Volume fluctuating
Too bright	Sound interrupted
Mottled color	

Cleaning



Name	Part No.	Remarks		
Cleaning paper	GED-008	Used to fan cleaning. Refer to "9.3 BOTTOM SECTION."		

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Quick Reference upon Service Visit ① Notes, PD/SD diagnosis, and methods for various settings

Notes when visiting for service

1. Notes when disassembling/reassembling

1) Rear case

When reassembling the rear case, the screws must be tightened in a specific order. Be careful not to tighten them in the wrong order forcibly. For details, see "Rear Case" in "7. DISASSEMBLY".

② Attaching screws for the HDMI and system cable terminals When attaching the HDMI and system cable terminals after replacing the Assembly, secure the terminals manually with a screwdriver, but not with an electric screwdriver.

If you tighten the screws too tightly with an electric screwdriver, the screw heads may be damaged, in which case the screws cannot be untightened/tightened any more.

2. On parts replacement

1) How to discharge before replacing the Assys

A charge of significant voltage remains in the Plasma Panel even after the power is turned off. Safely discharge the panel before replacement of parts, in either manner indicated below:

A: Let the panel sit at least for 3 minutes after the power is turned off. B: Turn the Large Signal System off before the power is turned off then, after 1 minute, turn the power off.

For details, see "5.6 [1] PANEL DRIVE-POWER ON/OFF FUNCTION".

② On the settings after replacement of the Assys Some boards need settings made after replacement of the Assys. For details, see "8. EACH SETTING AND ADJUSTMENT".

3. On various settings

1) Setting in Factory mode

After a Mask indication into the panel is performed, be sure to set the Mask setting to "OFF" then exit Factory mode.

PD		SD				
No. of LEDs flashing	MR	Panel No. of LEDs		MR		
Red 1	MR_POWER	SQ_LSI Blue				
Panel		Module Device Blue 2				
Red 2 POWER		communication				
Red 3	SCAN	DIGITAL-RST2	Blue 3			
Red 4	SCN-5V	Panel temperature	Blue 4			
Red 6	Y-DCDC		Audio Blue 5 Audio (MSP)		SP)	
Red 7	Y-SUS	Module microcomputer communication				
Red 8	ADRS		Blue 7	Main 3-wire serial communication		
Red 10 Red 11	X-DCDC X-SUS	Panel main IIC Blue 8 communication		Main IIC communication		
Red 12	DIG-DCDC		Blue 9	Main microcomputer communication		
Red 15	UNKNOWN	FAN	Blue 10	FAN		
1100 10	0	Unit high temperature	Blue 11	Unit high temperature		
		0 1		D-T	D-TUNER communication	
		DC-IN	Blue 13	RST2/RST4		
		Panel main EEPROM	Blue 15	Main EEPROM		
Special LED F		atterns		Subcategory confirmation		
	Panel	MR		procedure		
PD (2-15)		PD (1) B R • • • 0		If the DISPLAY key is pressed during shutdown, the orange LED flashes. (MR only)		
SD (1-15) R		SD (7-15) R		SD	SI	O Subcategory
System fa	ilure B	Standalone operation B R (MRMS01)			1	Tuner 1
MD	andby (Dad I ED III)	, ,			2	MSP/MAP AV Switch
	of B	Rewriting of R R software (PC)	•••••		4	RGB Switch
Rewriting of B software (PC)		Rewriting of B		8	5	Main VDEC
NO	B	software (USB)			6	VDEC-SDRAM
BACKUP		After rewriting is completed success-			7	AD/PLL
		fully, the orange LED goes dark.			8	HDMI
For special p	atterns other than	Rewriting of software B failed (USB)			9	Display Port Tx
	ere, see 5.1[1].			13	1	RST2
					2	RST4

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How to locate several items on the Factory menu

{ } : Item on the Factory menu [] : Key on the remote control unit " " : Screen indication

1. Confirmation of accumulated power-on time and power-on count Select {INFORMATION} then {HOUR METER}.

(After entering Factory mode, press [♣] four times.)

2. Confirmation of the Power-down and Shutdown histories

① Panel system

PD: Select {PANEL FACTORY} then {POWER DOWN}.

(After entering Factory mode, press [MUTING] once, press [ENTER/SET],
then press [] two times.)

SD: Select {PANEL FACTORY} then {SHUT DOWN}.

SD: Select {PANEL FACTORÝ} then {SHUT DOWN}. (After entering Factory mode, press [MUTING] once, press [ENTER/SET], then press [♣] three times.)

② MR section

Select {INFORMATION} then {MAIN NG}. (After entering Factory mode, press [♣] two times.)

3 Panel main section

Select (PANEL MAIN FACTORY) then (PM NG INFO).
After entering Factory mode, press [MUTING] twice, then press [ENTER/SET].

3. How to display the Mask indication

Mask indication in the panel side

Select (PANEL FACTORY) then {RASTER MASK SETUP}.
 (After entering Factory mode, press [MUTING] once, press [ENTER/SET], then press [1] 8 times.)

2. Press [ENTER/SET], then select a Mask indication, using [↑] or [↓].

Adjustments and Settings after replacement of the Assys (Procedures in Factory mode)

MR

To Standalone operation: MRMS01

1. DIGITAL Assy (Panel): Transfer of backup data

To System operation: SYSS01 To System operation: MRMS00

Note: After issuing a command, unplug then again plug

To Standalone operation: SYSS00

in the AC power cord.

 Select (PANEL FACTORY), {ETC}, then {BACKUP DATA}. (After entering Factory mode, press [MUTING] once, press [ENTER/SET], press [♣] seven times, then press [ENTER/SET].)

Other SD main categories have subcategories. For details, see 5.4[2].

- ② Select {TRANSFER}, using [→], then hold [ENTER/SET] pressed for at least 5 seconds.
- ③ After transfer of backup data is completed, (ETC) is automatically selected, and the LED on the front panel returns to normal lighting.

2. MAIN BLOCK Assy (MR), MAIN Assy (Panel): Execution of FINAL SETUP.

- ① Select (INITIALIZE) then (FINAL SETUP), then press [ENTER/SET]. (After entering Factory mode, press [MUTING] three times, then press [♣] four times.)
- Select "YES", using [→]. Then hold [ENTER/SET] pressed for at least 5 seconds.
 After "FINAL SETUP IS COMPLETE" is displayed on the screen, turn the POWER switch of the main unit off.

3. POWER SUPPLY Unit (Panel): Clearance of the accumulated power-on count and maximum temperature value

- count and maximum temperature value
 ① Select {PANEL FACTORY}, {ETC}, then {P COUNT INFO}. (After entering Factory mode, press [MUTING] once, press [ENTER/SET], press [♣] seven times, press [ENTER/SET], then press [♣] six times.)
- [ENTER/SET], then press [♣] six times.)
 ② Press [♠] to select "CLEAR". Hold [ENTER/SET] pressed for at least 5 seconds. After clearance is completed, "ETC" is automatically selected. Clear the maximum temperature value (MAX TEMP) in the same manner.

4. Other Assys (Panel): Clearance of the maximum temperature value

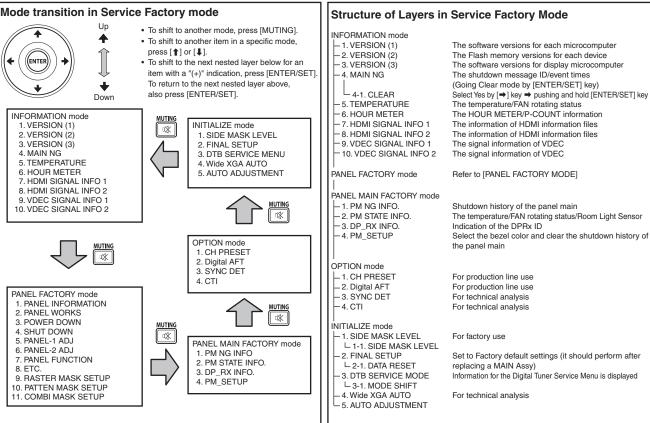
- Select {PANEL FACTORY}, {ETC}, then {MAX TEMP}. (After entering Factory mode, press [MUTING] once, press [ENTER], press [♣] seven times, press [ENTER/SET], then press [♣] seven times.)
- ② Press → to select "CLEAR". Hold [ENTER/SET] pressed for at least 5 seconds After clearance is completed, "ETC" is automatically selected.

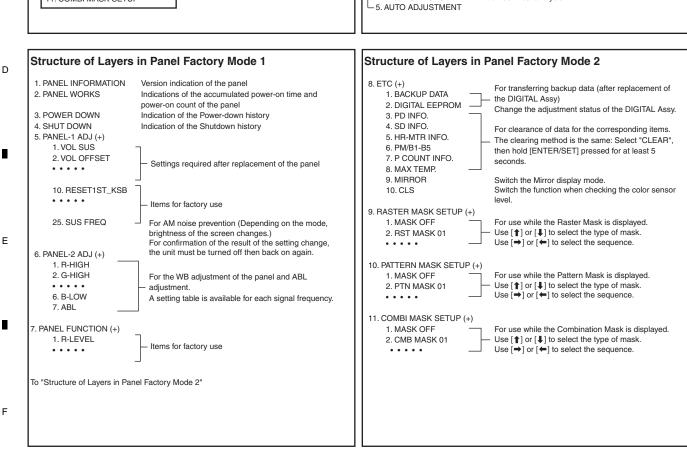
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Quick Reference upon Service Visit ② Mode transition and structure of layers in Service Factory mode



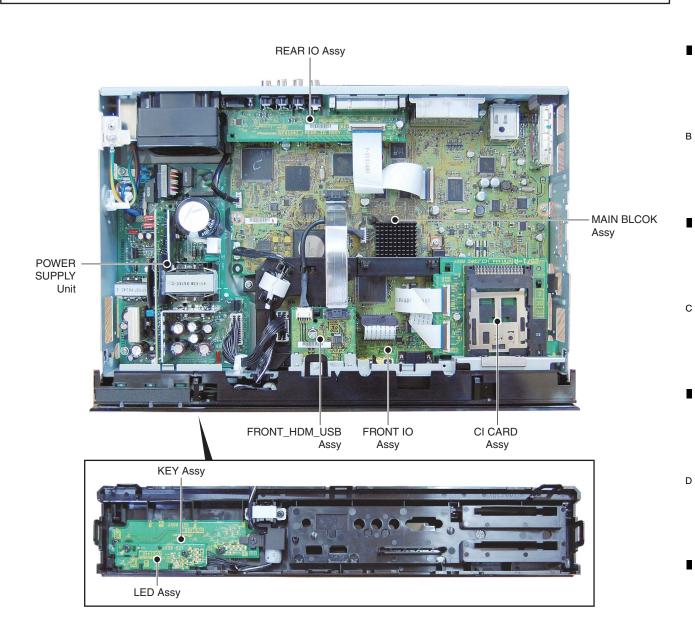


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Note: The wiring shown in the photo is different from the actual wiring, because the product in the photo is a prototype. Upon servicing, be sure to restore the original wiring of the unit after repair work.



 $NOTES: \bullet \textit{Parts marked by "NSP" are generally unavailable because they are not in our \textit{Master Spare Parts List.}$

• The \triangle mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.

Mark No. Description LIST OF ASSEMBLIES	Part No.	Mark No. Description	Part No.
NSP 1MAIN ASSY 2FRONT_HDM_USB ASSY 2MAIN BLOCK ASSY	AWV2570 Y AWW1412 AWW1413	NSP 1FUKUGO ASSY 2REAR IO ASSY 2LED ASSY 2FRONT IO ASSY 2CI CARD ASSY 2KEY ASSY	AWV2571 AWW1441 AWW1442 AWW1443 AWW1444 F
		1POWER SUPPLY UNIT	AXY1204
		KRP-M01	13

6 **-** 7 **-** 8

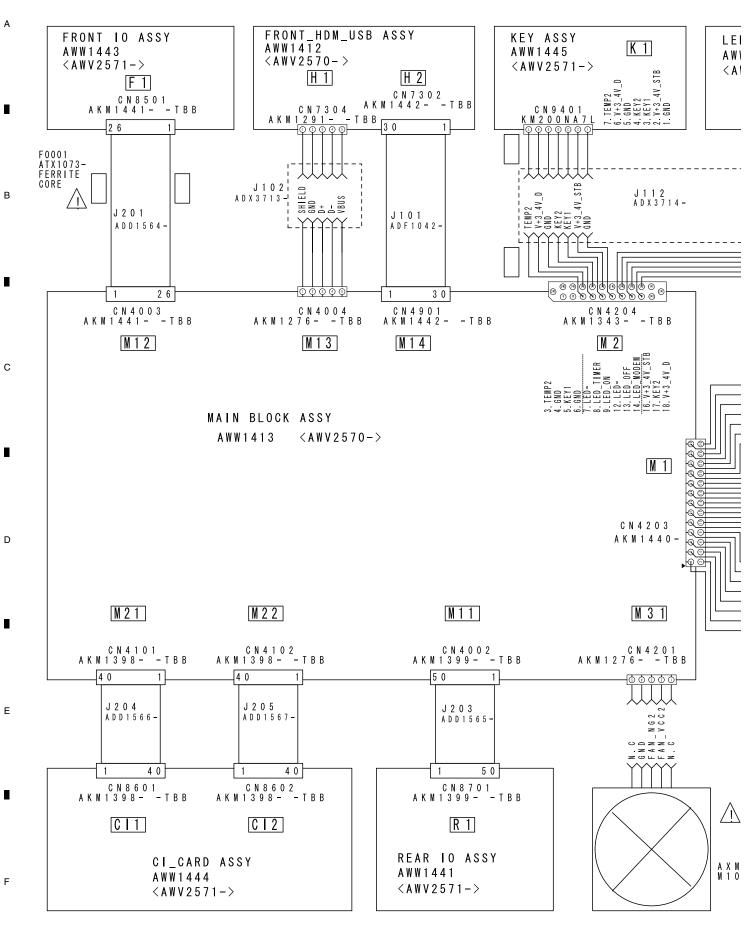
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4. BLOCK DIAGRAM

4.1 OVERALL WIRING DIAGRAM

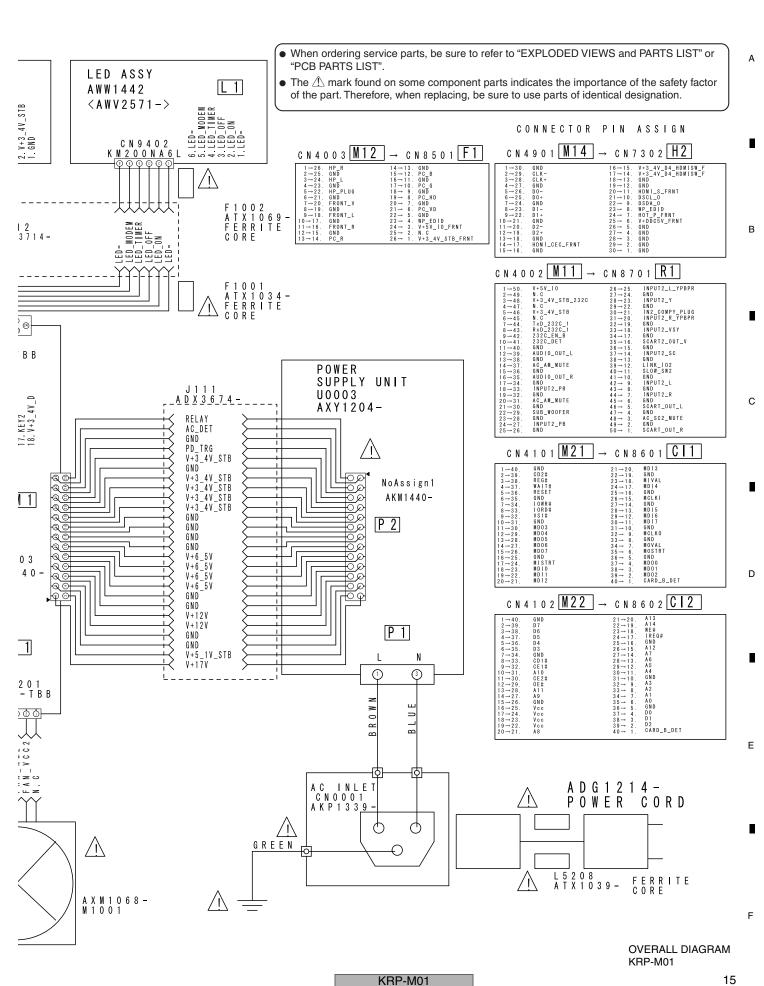


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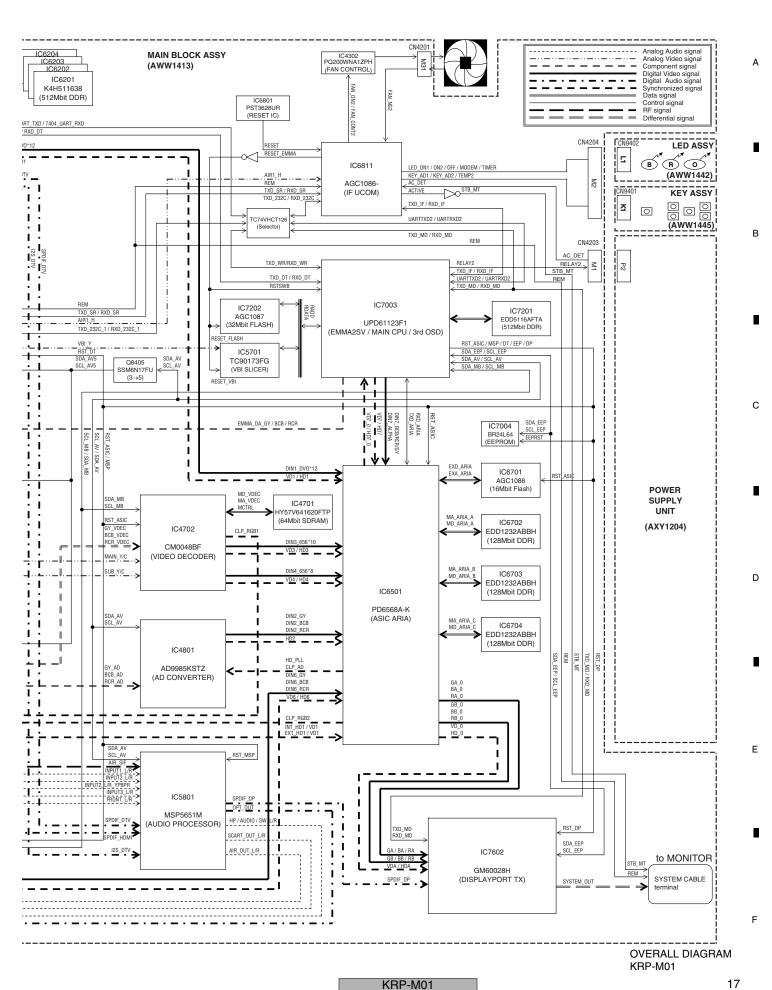


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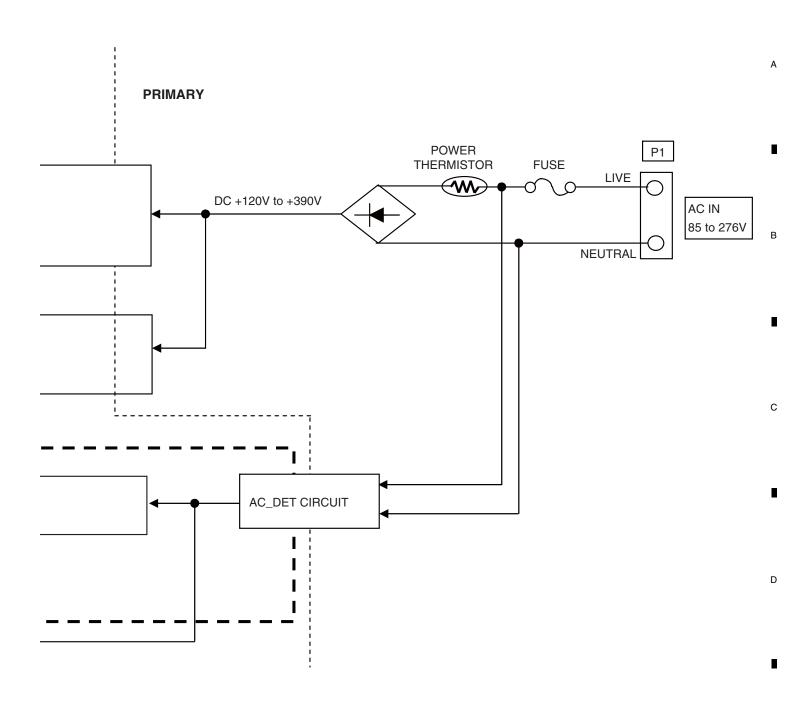
3



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SECONDARY POWER SUPPLY UNIT P2 <PKG1> V+5.1V_STB V+17V VCC V+12V DC/DC **CONVERTOR** V+6.5V V+3.4V_STB REGULATOR STB DC/DC **CONVERTOR** PD_TRG AND **MICRO** COMPUTER **RELAY POWER** AC_DET **SUPPLY UNIT** <PKG2> <Protection function> Overcurrent Protection (OCP) V+3.4V_STB, V+5.1V_STB, VCC 3 outputs Overvoltage Protection (OVP) V+6.5V, V+12V, V+17V V+3.4V_STB (latches for long time) Under Voltage Protection (UVP) V+6.5V, V+12V, V+17V Thermal Shut Down (TSD) V+6.5V output diode: D351 block V+3.4V_STB (latches for long time)

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Logic Signal Specifications [Logic level] H: STB3.4 V \times (0.8 to 1.1), L: \leq STB3.4 V \times 0.2

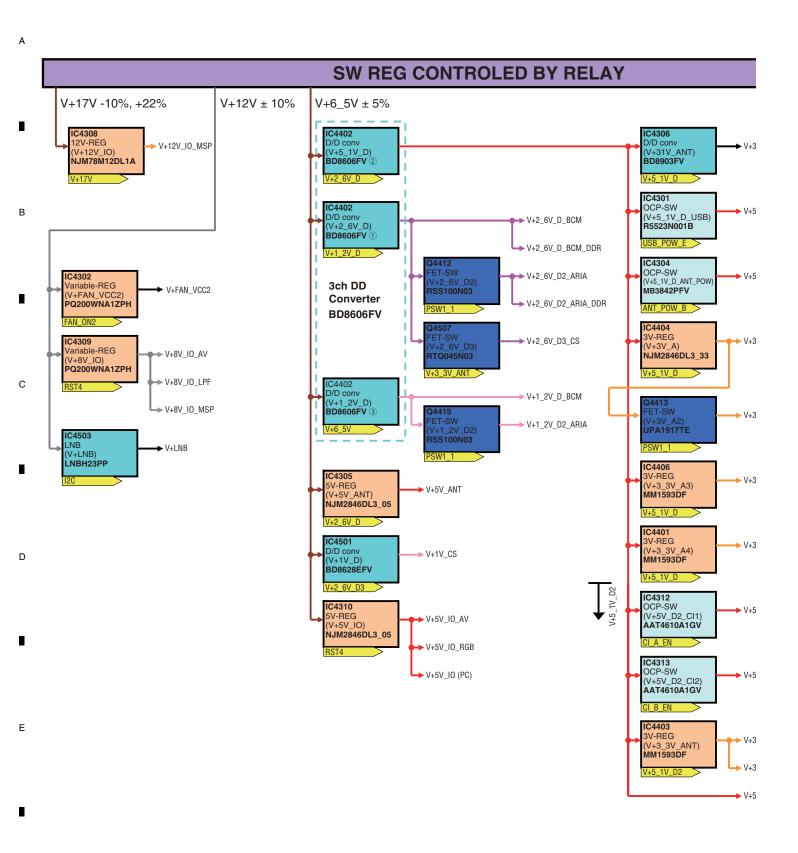
Signal Name	I/O	Function	Logic		Description	
RELAY	IN	Relay ON/OFF	Н	ON	For controlling ON/OFF of all output	
			L	OFF	signals other than STB signals	
			Open	OFF		
PD_TRG	OUT	Determination of abnormality	Н	Determination	For sending a deterministic signal when an abnormality is generated inside the POWER SUPPLY Unit to shut off any output signals other than STB signals	
		inside the POWER SUPPLY Unit		of abnormality		
			L	Normal		
AC_DET	OUT	AC detection	Н	Present	For detecting the presence of the AC input voltage	
			L	Absent	regardless of ON/OFF of STB 3.4 V output	

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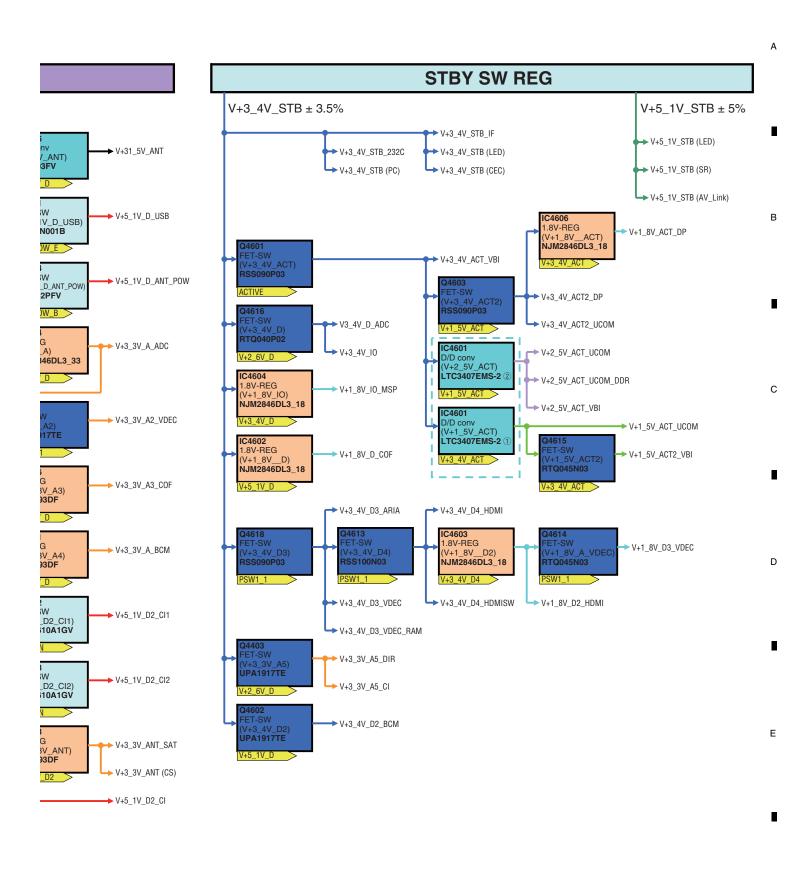
Ε

4.4 POWER SUPPLY BLOCK of MAIN BLOCK ASSY

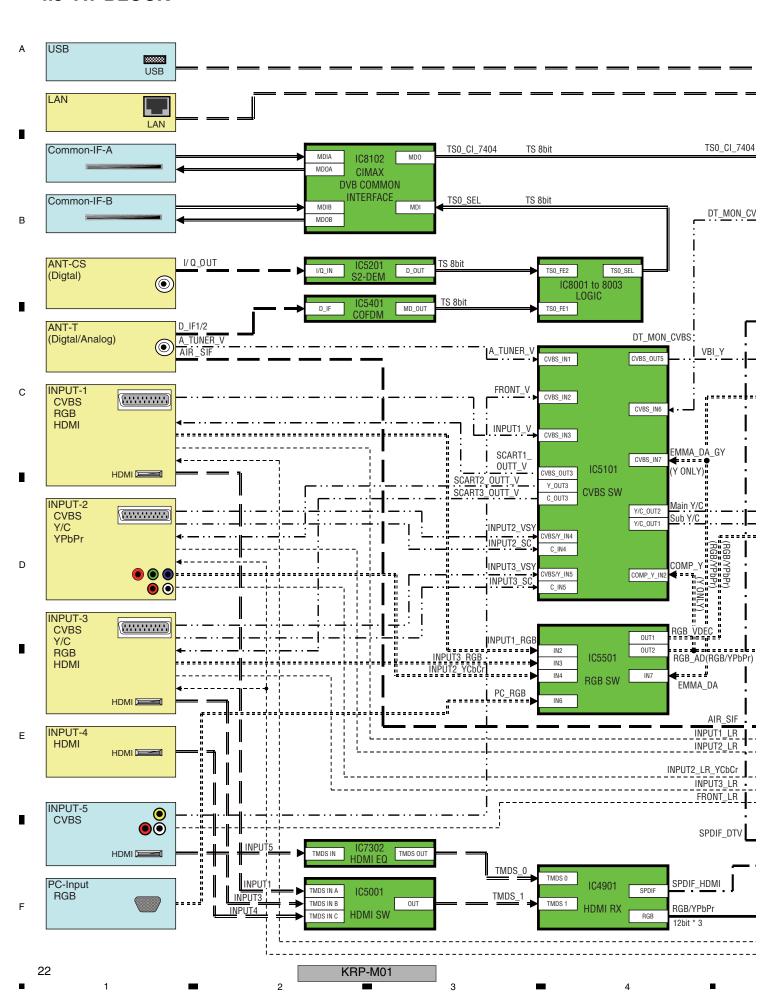


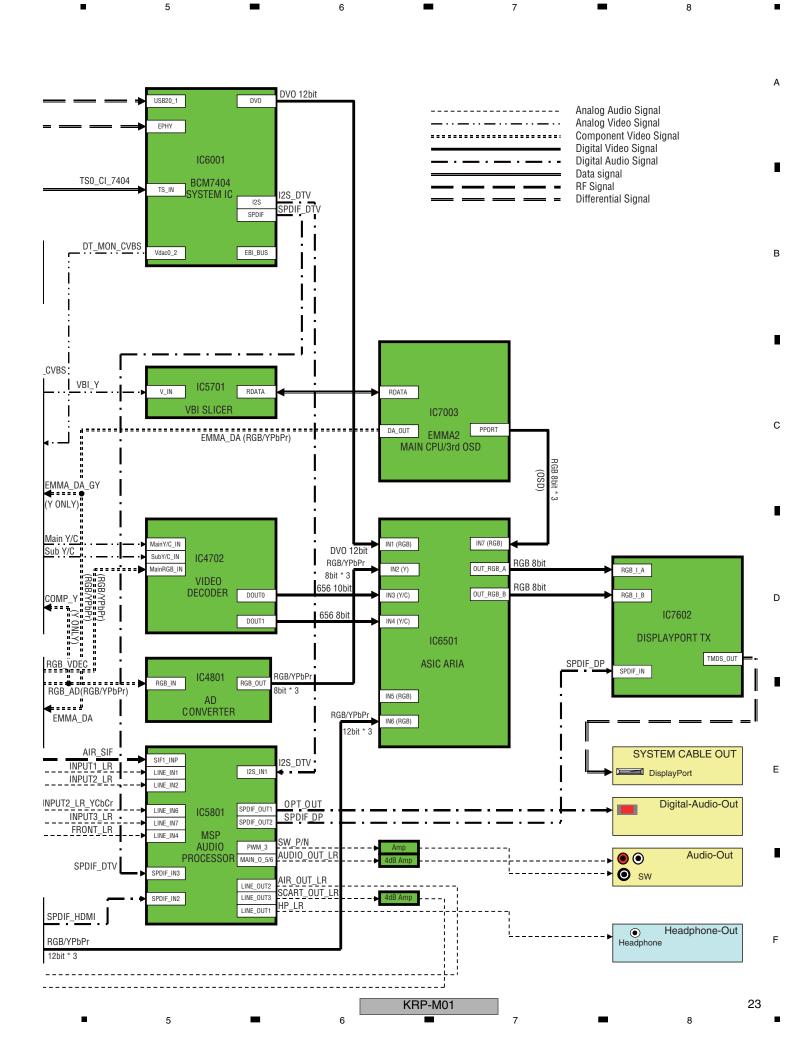
20

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5. DIAGNOSIS **5.1 POWER SUPPLY OPERATION**

[1] LED DISPLAY INFORMATION

■ LED Pattern

Status	LED	LED Pattern / Remarks	
Standby Power	Blue Red		
Management	Orange		
Power On	Blue		
Power On	Red Orange		
	Blue	Once Twice n times 2.5s Once *	*1
Power-Down	Red	500ms	
	Orange Blue	500ms *	*2
Shutdown	Red	Once Twice n times 2.5s Once	_
	Orange Blue	500ms *	*0
Shutdown	Red		*2 *3
(Subcategory flashing)	Orange	500ms	
No digital adjustment	Blue Red	200ms	
data copied for backup	Orange		
50	Blue	100ms	
Updating the PC	Red Orange	100ms	
	Blue		_
During factory operation	Red		
	Orange Blue	100ms	\dashv
During DTB communication inhibit	Red		
Communication inflibit	Orange		_
During USB update	Blue Red	100ms	
	Orange		
Updating of USB is	Blue Red	100ms	
finished normally.	Orange	TOOMS	
Updating of USB is	Blue		*4
abnormally finished.	Red Orange	100ms	
Power ON of standalone	Blue	1000msec 1000msec	_
mode (Screen ON)	Red	1000msec 1000msec	
,	Orange Blue		_
Mode switch of system / standalone operation	Red	200ms	
/ standatorie operation	Orange		_
Sleep timer	Blue Red		
	Orange		
During reservation video	Blue		
recording (Unit: Standby)	Red Orange		
During reservation video	Blue		
recording (Unit: ON)	Red		
	Orange		_

*1: Notify upon the power-down content by Red LED flashing number of times. *2: Notify upon the shutdown content by Blue LED flashing number of times

*3: Notify upon the subcategory number by Orange LED flashing number of times.

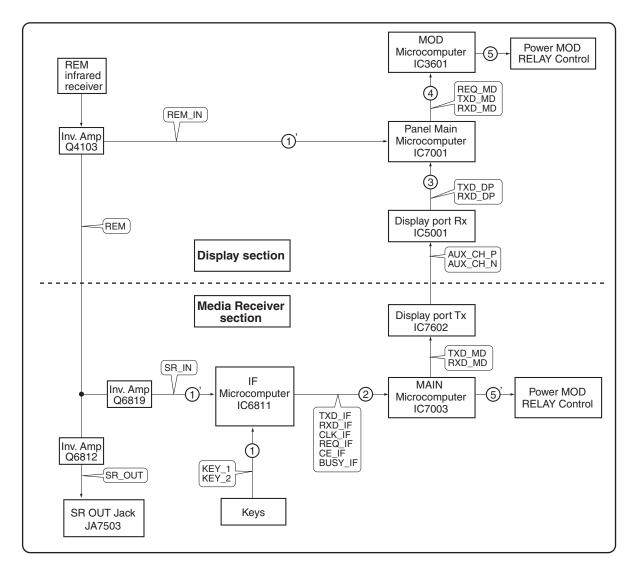
*4: Notify upon the abnormal state by Orange LED flashing number of times.

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[2] POWER ON SEQUENCE



- ①: The KEY signal is input to the IF microcomputer.
- ①': The remote control signal is input to the IF microcomputer and Panel main microcomputer.
- ②: The IF microcomputer sends the operation data of the remote control unit key to the main microcomputer.
- ③: The main microcomputer issues a startup command (PON) to the panel main microcomputer through DP Tx and DP Rx.
- ④: The panel main microcomputer issues a startup command (PON) to the MOD microcomputer.
- ⑤: The MOD microcomputer controls a MOD relay of the POWER SUPPLY Unit (Display section), then the power is turned on.
- (5): The main microcomputer controls a MOD relay of the POWER SUPPLY Unit (Media Receiver section), then the power is turned on.

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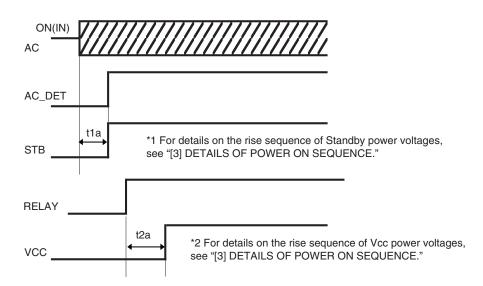
D

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OUTLINE OF POWER ON SEQUENCE

The rise of the output voltage is defined as the point at which 10% output voltage is reached, and the fall is defined as the output supply stop point.

Sequence of AC ON (IN)



(a) Relay signal: When the POWER key on the remote control unit is pressed after that on the unit is set to ON

AC ON				
Item	Specified Time			
AC to STB	t1a ≦ 0.8s			
RELAY to VCC	t2a ≦ 0.5s			

(b) Relay signal: When the POWER key on the remote control unit is pressed while the unit is OFF (in Standby mode)

AC ON				
Item	Specified Time			
AC to STB	t1a ≦ 0.8s			
Relay to VCC	t2a No specification			

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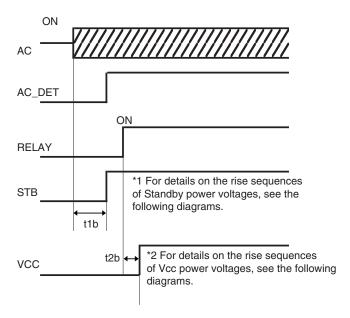
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[3] DETAILS OF POWER ON SEQUENCE

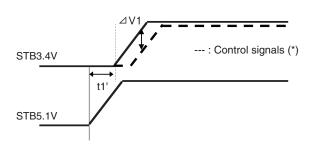
The rise of the output voltage is defined as the point at which 10% output voltage is reached.

1. Sequence of Relay ON (IN)



Relay ON				
Item	Specified Time			
AC to STB	t1b ≤ 0.8s			
RELAY to VCC	t2b ≦ 0.5s			

2. Rise sequence of Standby power voltages

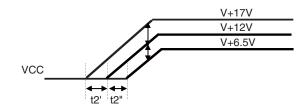


<Specified time and difference of voltages>

Rise				
Item	Specified Time			
STB5.1V to STB3.4V	-50ms ≤ t1' ≤ 50ms			
Item	Specified difference of voltages			
STB3.4V - Control signal (*)	0 V ≦ ΔV 1			

(*) Control signals (output signals) denote AC_DET and PD_TRG signals.

3. Rise sequences of Vcc power voltages



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<Specified time of voltages>

Rise				
Item Specified time (at nominal load				
V+17V to V+12V	0ms ≤ t2' ≤ 10ms			
V+12V to V+6.5V	0ms ≤ t2" ≤ 10ms			

4. Specifications of the rise time of the output voltages (common to all sequences)

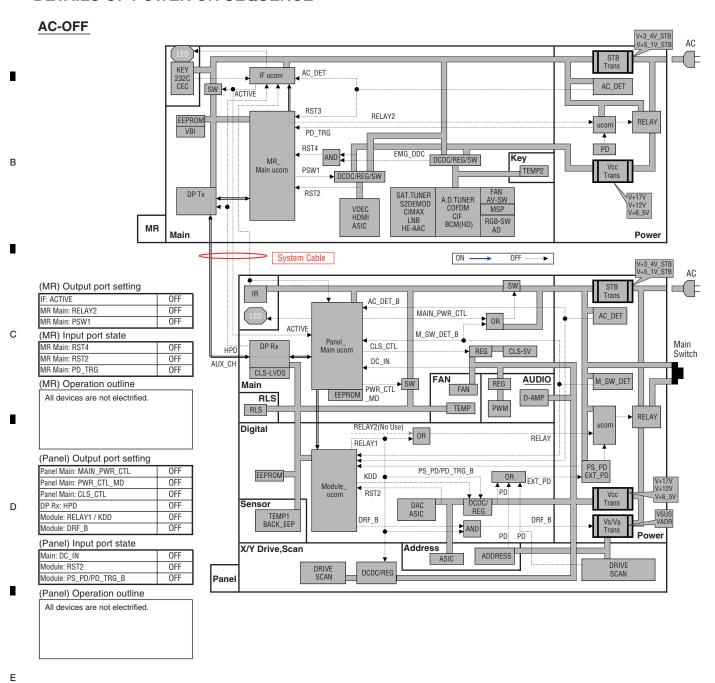
Note that there must not be any temporary voltage drop during rising.

Rise time (time required for reaching from 10% to 90% output voltage)					
Item Specified time					
STB 10% to STB 90%	tr_STB ≦ 100ms				
VCC 10% to VCC 90% tr_VCC ≦ 200ms					

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■ 2 **■** 3 **■** 4

A DETAILS OF POWER ON SEQUENCE

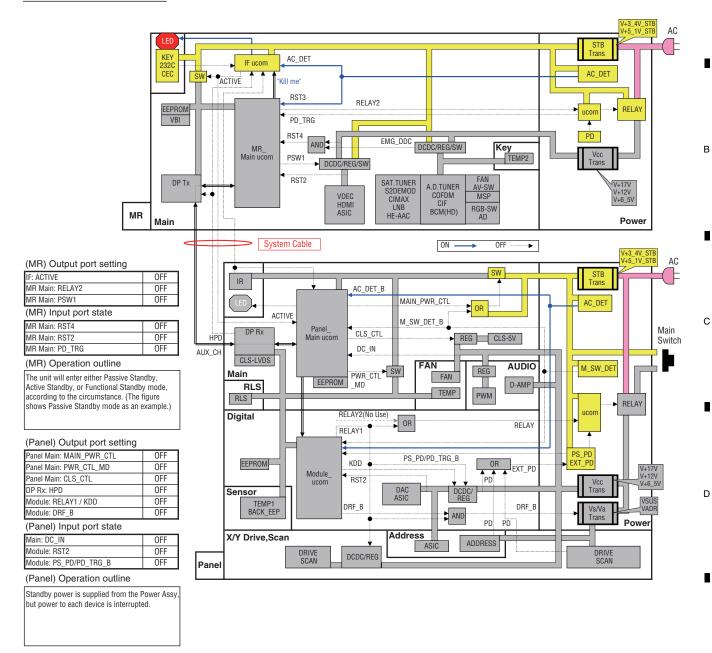


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■ 3 ■ 4

Panel Main Power OFF



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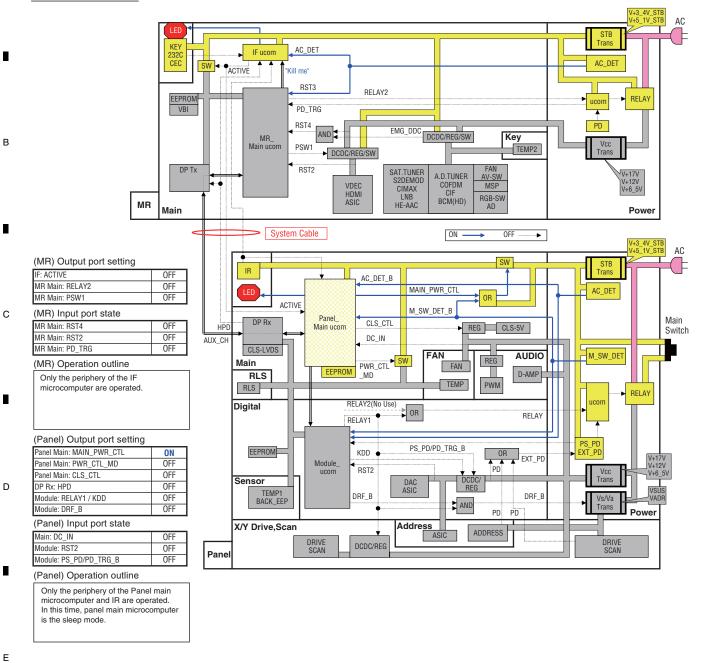
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Passive Standby

Α

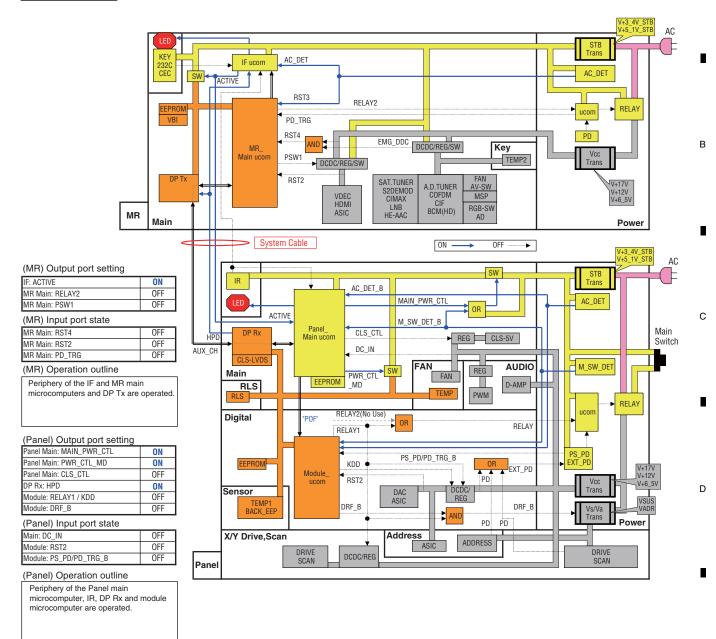


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1 2 3 4

Active Standby



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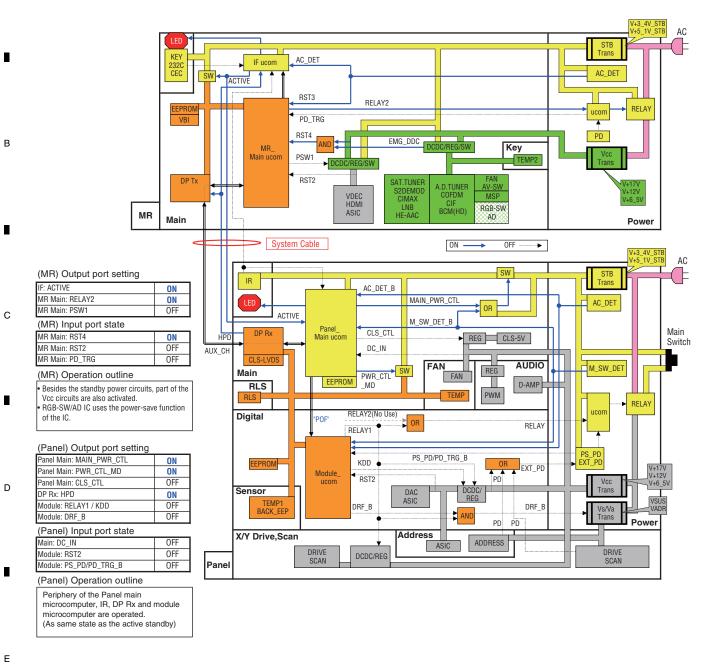
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Function Standby

Α

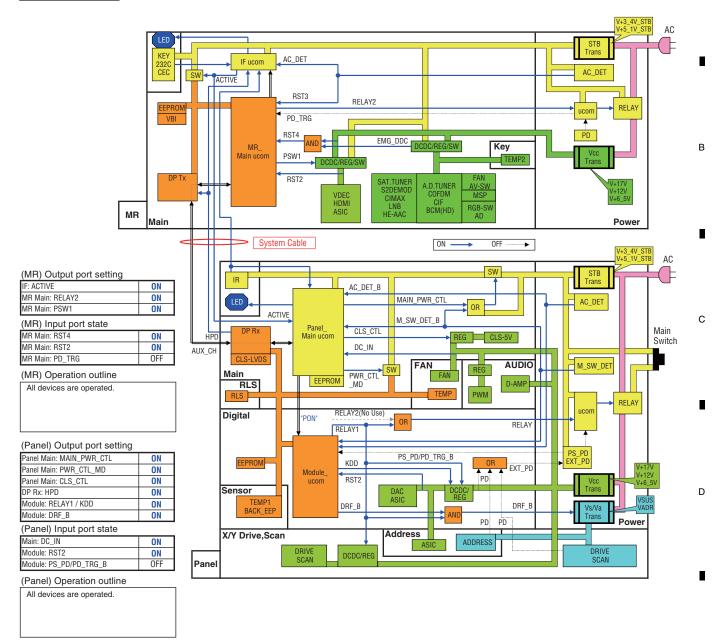


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2 3 4

PDP Screen ON



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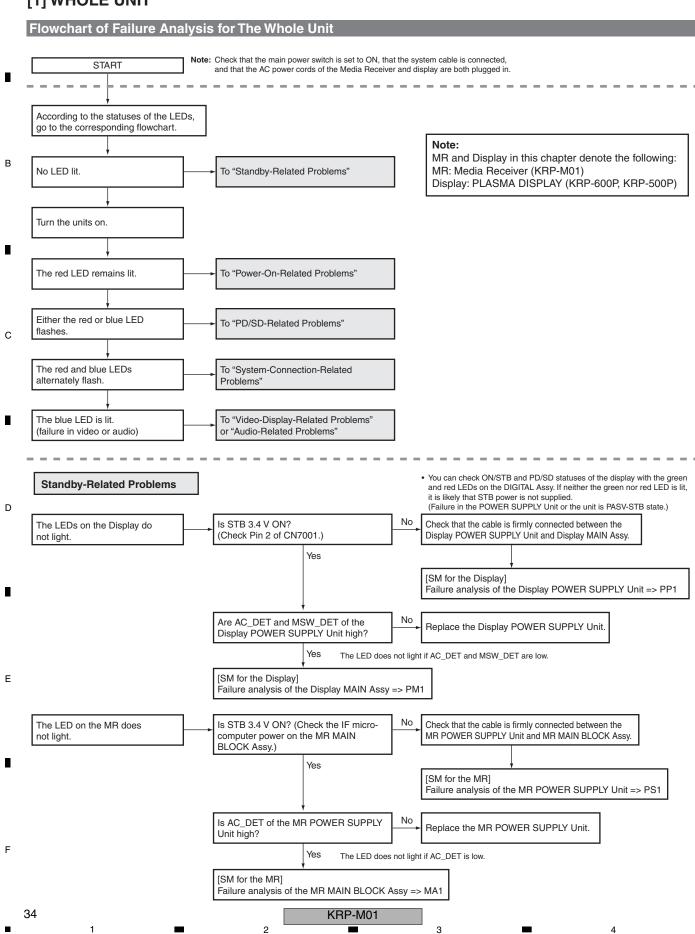
33

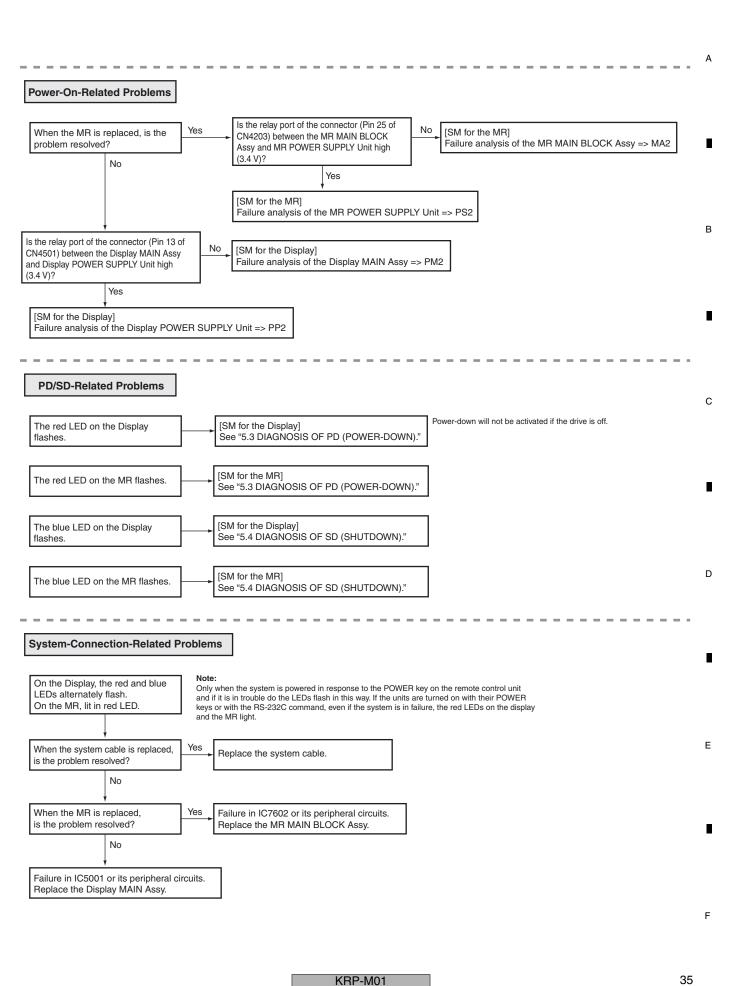
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5.2 DIAGNOSIS FLOWCHART OF FAILURE ANALYSIS

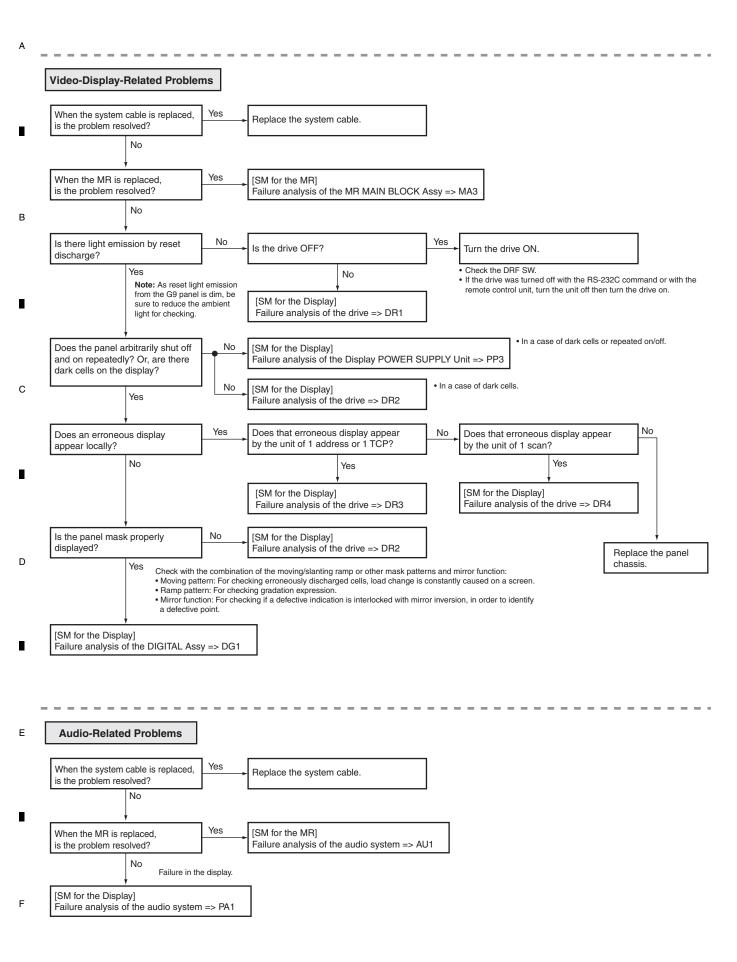
[1] WHOLE UNIT





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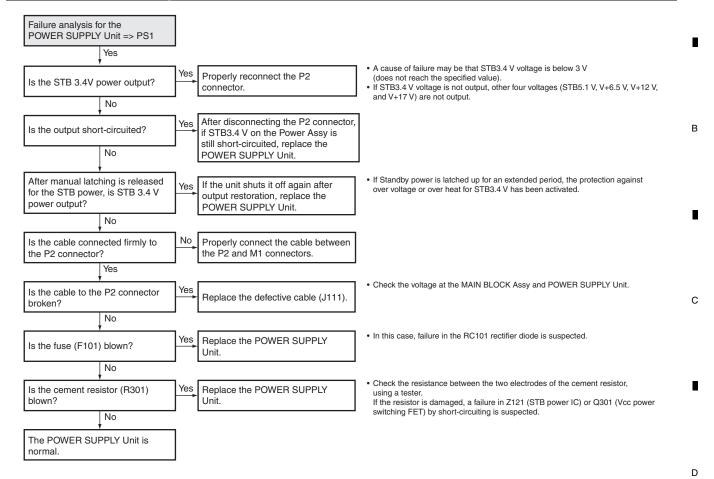
- 6 - 7

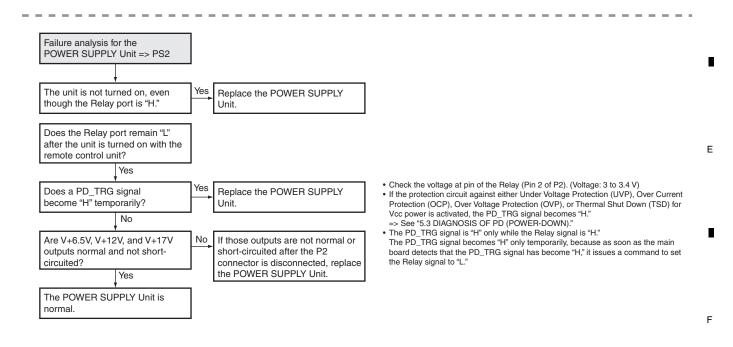


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[2] POWER SUPPLY UNIT

Flowchart of Failure Analysis for The POWER SUPPLY Unit





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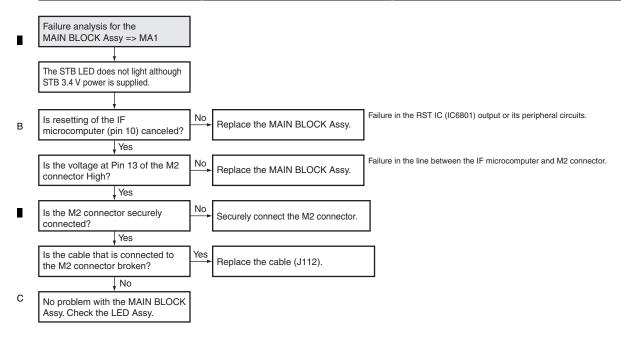
37

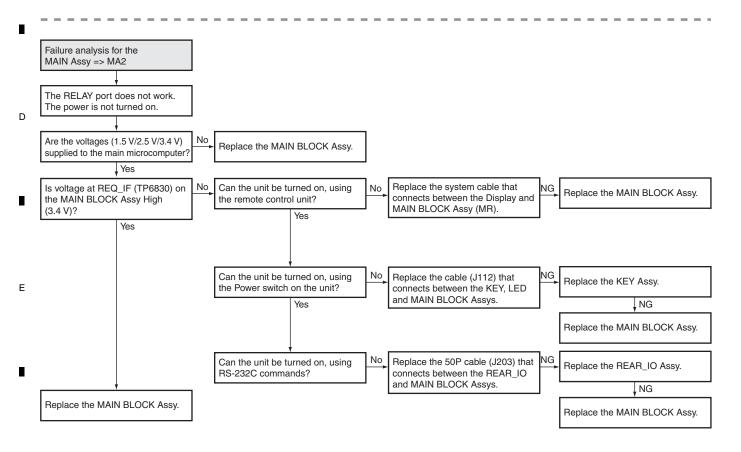
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[3] MAIN BLOCK ASSY

Flowchart of Failure Analysis for The MAIN BLOCK Assy



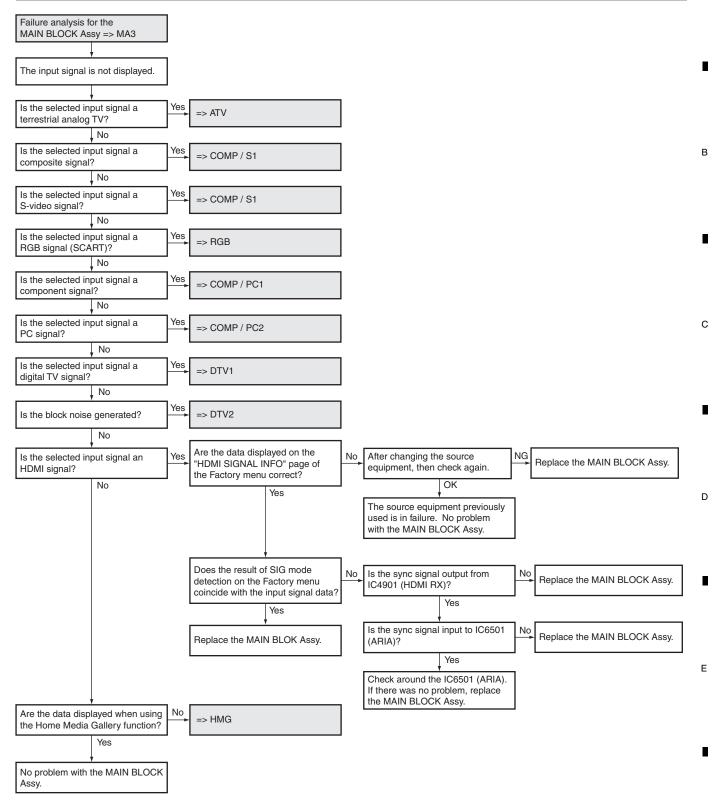


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2 3 4

Flowchart of Failure Analysis for The MAIN BLOCK Assy

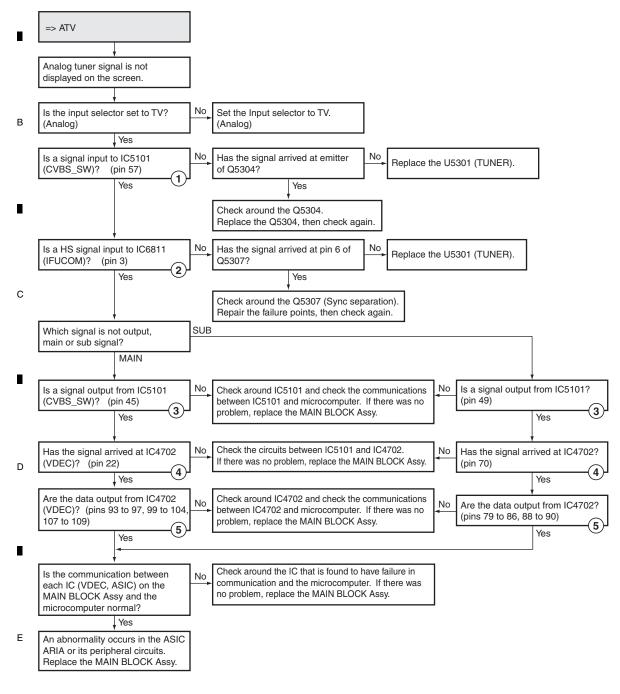


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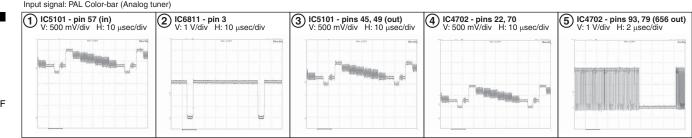
[4] VIDEO SYSTEM

Flowchart of Failure Analysis for The Video System



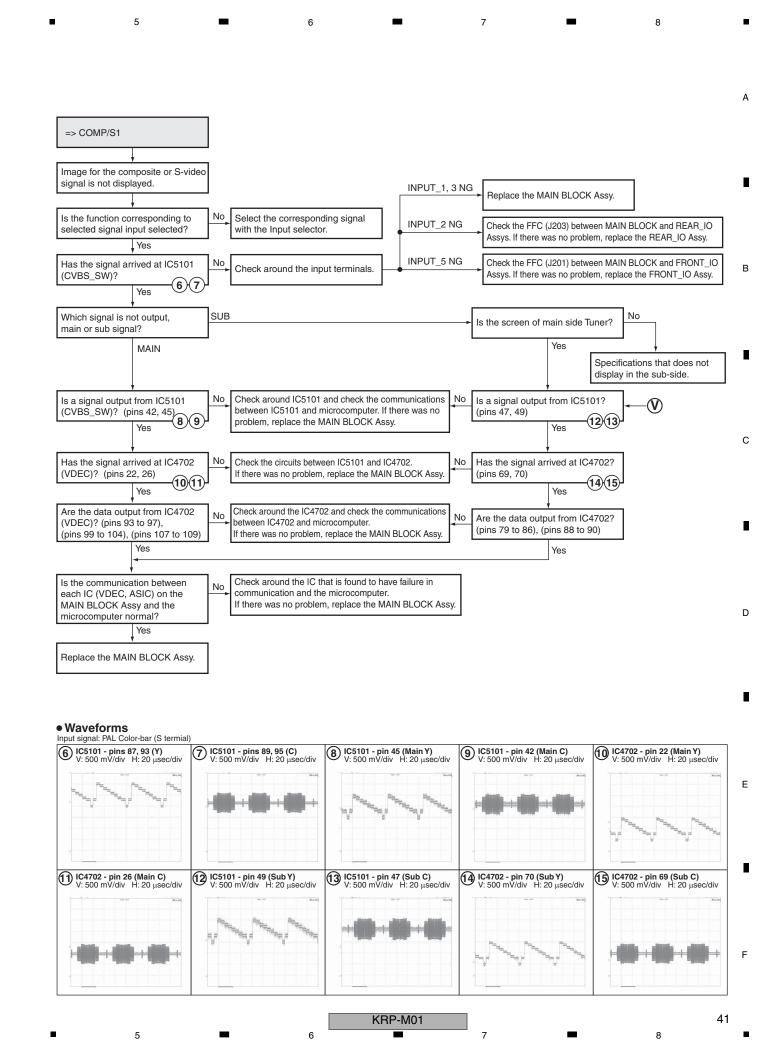
Waveforms

Input signal: PAL Color-bar (Analog tuner)

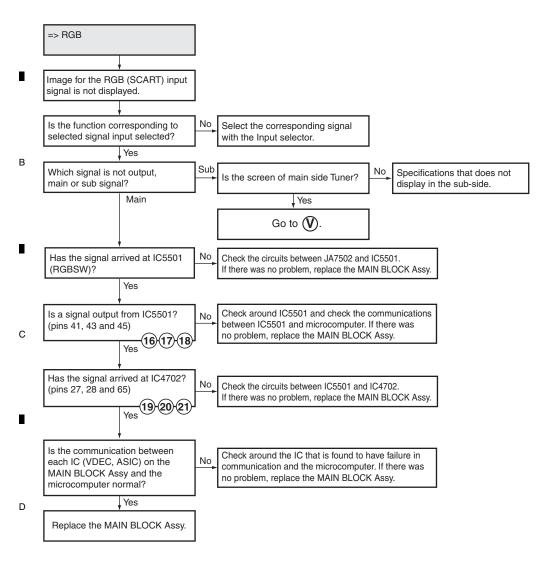


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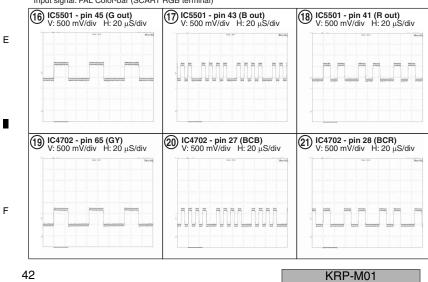
1 2 3 4



Waveforms

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Input signal: PAL Color-bar (SCART RGB terminal)



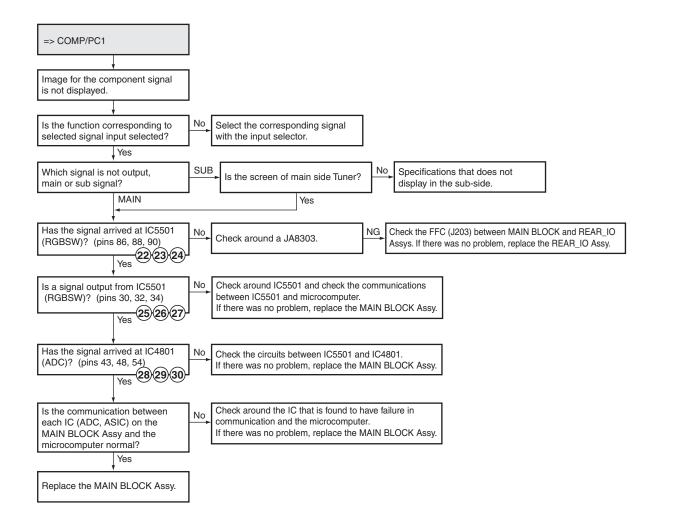
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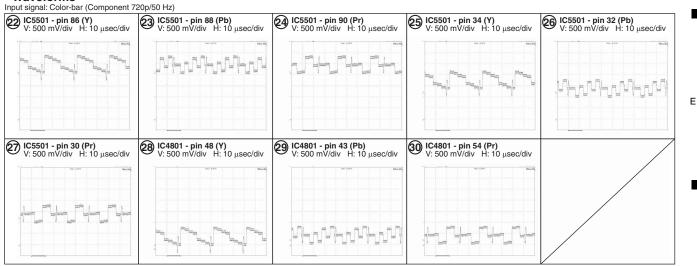
2

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Waveforms



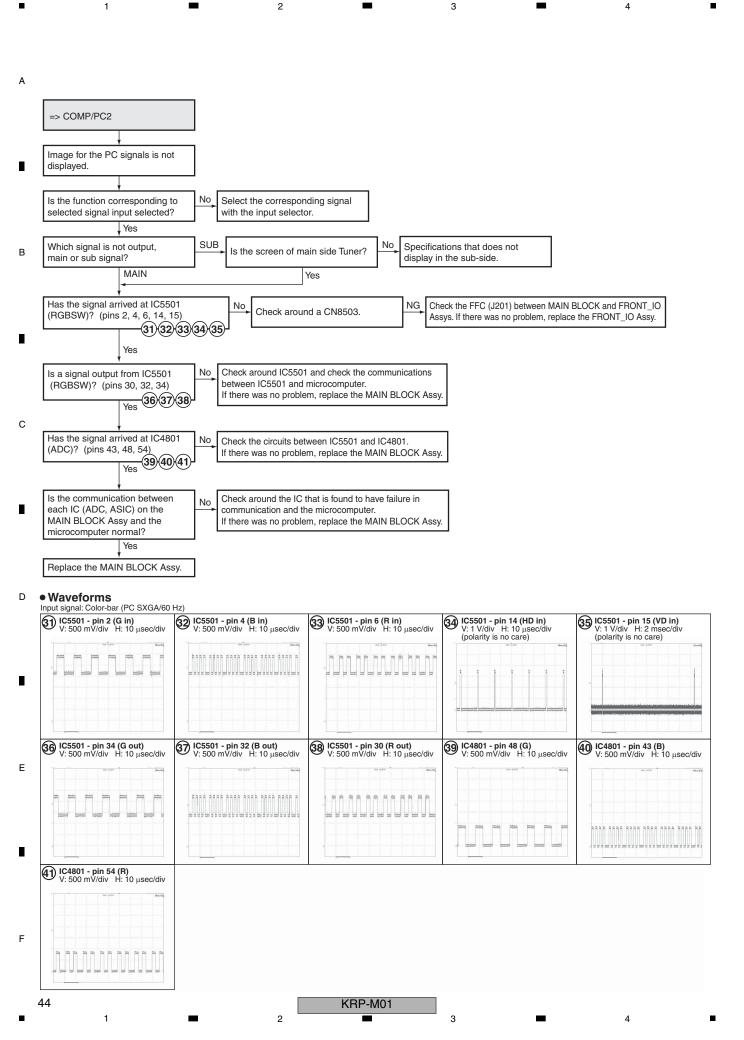
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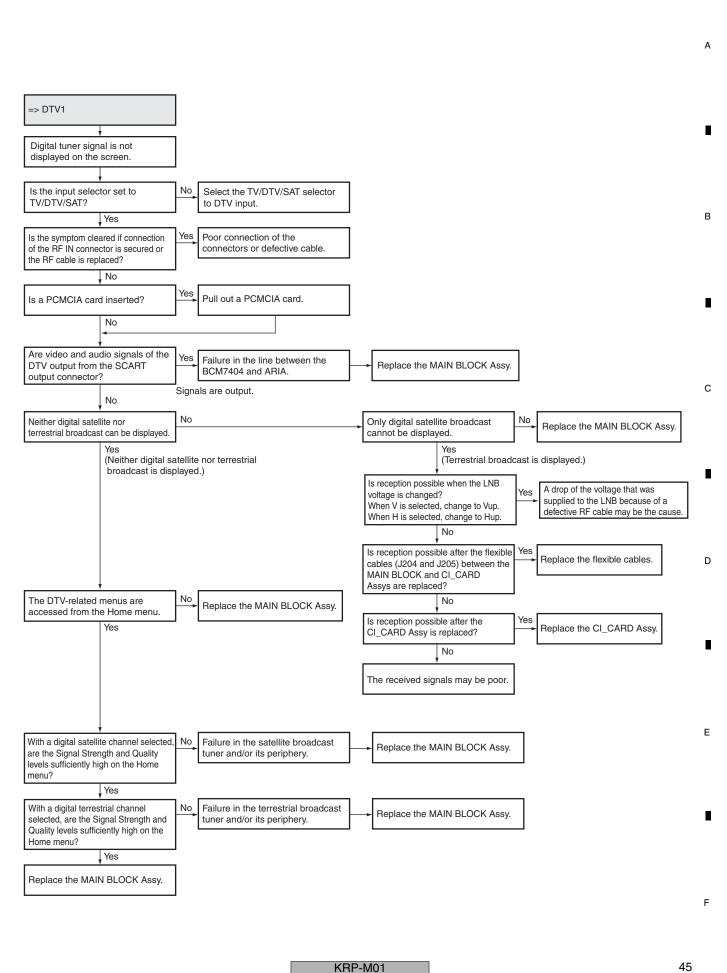
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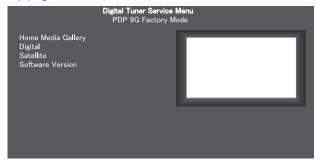


[Common to the DTVs 1 and 2] How to Display the DTB Service Menu

As you can display the DTB Service Menu from Factory mode, you should have a remote control unit that supports Factory mode.

- Step 1: Press the FACTORY key on the remote control unit to display the INFORMATION screen in Factory mode.
 - Step 2: Press the MUTING key on the remote control unit 4 times to display the INITIALIZE screen.
 - Step 3: Press the ↓ key on the remote control unit twice so that DTB SERVICE MODE (+) is displayed at the bottom of the screen.
 - Step 4: Press the ENTER/SET key on the remote control so that MODE SHIFT <=> :No is displayed at the bottom of the screen.
- Step 5: Press the ← or → key on the remote control so that MODE SHIFT <=> :YES is displayed at the bottom of the screen.
- Step 6: Press the ENTER/SET key on the remote control unit for 5 sec or more to display the DTB Service Menu.

Top page of the DTB Service Menu



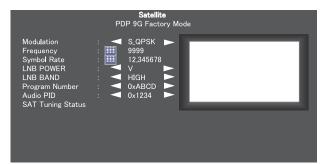
Digital : Service menu for digital terrestrial broadcast reception Satellite : Service menu for digital satellite broadcast reception

■ How to Change the LNB Voltage on the DTV Service Menu

On the Satellite screen of the DTV Service menu below, move the cursor to LNB POWER by using the ↓ key on the remote control unit then change the LNB voltage, using the ← or → key.

The LNB voltage values are as shown below:

V: 13 V (Typ.) H: 18 V (Typ.) Vup: V+1 V Hup: H+1 V



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=> DTV 2 Block noise is generated. **Digital Terrestrial Broadcast** An error may be generated in the B.E. block Is a Packet Error generated? (after the demodulation TS is output). Yes Replace the MAIN BLOCK Assy No Replace the MAIN BLOCK Assy. and check if the symptom persists. Yes [Checking of the reception environment] Is the input level high enough? • Does the input level become high when the antenna is reoriented? (IF_AGC check) • Is the signal loss large because multiple distributors are used? Yes • When a booster or attenuator is added, is the symptom cleared? • Is the RF cable defective? • When the dressing of the RF cable is modified, does the level fluctuate? [Checking of the reception environment] Is the C/N value high enough? No (MER check) • Is the C/N value affected by a wireless LAN or other ambient electromagnetic waves? • When the dressing of the RF cable is modified, does the C/N value fluctuate? • When the RF cable is shortened as much as possible, is the symptom cleared? Yes If IF_AGC level and C/N value are high enough, block noise is not usually generated. Digital satellite Broadcast Is a Packet error generated? An error may be generated in the B.E. block (PER check) (after the demodulation TS is output). Replace the MAIN BLOCK Assy Replace the MAIN BLOCK Assy. and check if the symptom persists. [Checking of the reception environment] Is the input level high enough? • Is the signal loss large because multiple distributors are used? (RF Level check) • When a booster or attenuator is added, is the symptom cleared? Yes • Is the RF cable defective? • When the dressing of the RF cable is modified, does the level fluctuate? Is the C/N value high enough? No [Checking of the reception environment] • Is the C/N value affected by a wireless LAN or other ambient electromagnetic waves? (C/N check) • When the dressing of the RF cable is modified, does the C/N value fluctuate? • When the RF cable is shortened as much as possible, is the symptom cleared? Yes If IF_AGC level and C/N value are high enough, block noise is not usually generated.

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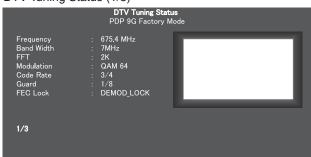
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■ How to Confirm the DTV Tuning Status on the Digital Tuner Service Menu

If block noise is generated, it is necessary to acquire the DTV Tuning Status for the reception frequency of the signal in which block noise is generated. For comparison, it is also necessary to acquire the DTV Tuning Status for another reception frequency of the signal in which block noise is not generated. The DTV Tuning Status page to be acquired is shown below:

DTV Tuning Status (1/3)



Frequency : Frequency of the signal currently being received.

Band Width : Bandwidth of the signal currently being received.

FFT : FFT mode of the signal currently being received.

FFT : FFT mode of the signal currently being received (2K or 8K).

Modulation : Modulation method for the signal currently being received.

Code Rate : Code Rate of the signal currently being received.

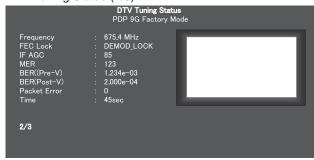
Guard : Guard Interval of the signal currently being received.

FEC Lock : Current lock status of the receiver. The available lock

statuses are as shown below:

DEMOD_LOCK FEC_LOCK DRX_LOCK UNLOCK

DTV Tuning Status (2/3)



IF AGC: IF AGC level of the signal currently being received.

The AGC-level limits in normal reception are shown below.

Use the following values only as a guide, because they may be affected by the reception environment.

Modulation	Code Rate	Signal-level Limit in Normal Reception	
QPSK	1/2	100	
1	2/3	100	
1	3/4	100	
1	5/6	100	
1	7/8	100	
16QAM	1/2	100	
1	2/3	100	
1	3/4	100	
1	5/6	100	
1	7/8	100	
64QAM	1/2	100	
1	2/3	58	
	3/4	56	
	5/6	55	
	7/8	54	

BER (Pre-V): Pre-Viterbi Bit Error Rate of the signal currently being

BER (Post-V): Post-Viterbi Bit Error Rate of the signal currently being

received. If the value is 2.000E-04, block noise is not caused by a problem in the tuner.

Desired by a problem in the turier.

Packet Error: Packet error count of the signal currently being received.

If the packet error count is "0," block noise caused by

the tuner will not be generated.

Time : Measured duration of BER (Pre-V), BER (Post-V), or

Packet Error. To reset the value to 0 and restart measuring, press the ← or → key on the remote

control unit.

: Quality of the signal currently being received. The signal qualities in normal reception are shown below. Use the following values only as a guide.

Modulation	Code Rate	MER Limit in Normal Reception		
QPSK	1/2	93		
	2/3	85		
	3/4	67		
	5/6	76		
	7/8	82		
16QAM	1/2	98		
	2/3	116		
	3/4	127		
	5/6	138		
	7/8	145		
64QAM	1/2	140		
	2/3	170		
	3/4	184		
	5/6	197		
	7/8	206		

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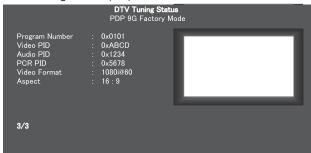
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DTV Tuning Status (3/3)



Program Number: No. of the program currently being received.

Video PID : Video PID of the program currently being received.

Audio PID : Audio PID of the program currently being received.

PCR PID : PCR PID of the program currently being received.

Video Format : Video Format of the program currently being received.

Aspect : Aspect ratio of the program currently being received.

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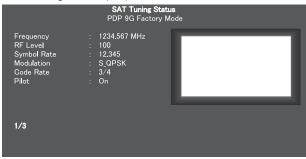
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■ How to Confirm the SAT Tuning Status on the Digital Tuner Service Menu

If block noise is generated, it is necessary to acquire the SAT Tuning Status for the reception frequency of the signal in which block noise is generated. For comparison, it is also necessary to acquire the SAT Tuning Status for another reception frequency of the signal in which block noise is not generated. The SAT Tuning Status page to be acquired is shown below:

SAT Tuning Status (1/3)



Frequency RF Level : Frequency of the signal currently being received.

: Level of the signal currently being received.

The signal-level limits in normal reception are shown below. Use the following values only as a guide, because they may be affected by the reception environment.

Modulation	Signal-level Limit in Normal Reception
S2_QPSK	50 to 75
S2_8PSK	50 to 75
S_QPSK	50 to 75

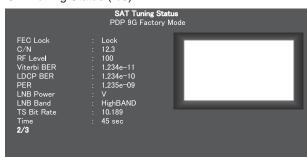
Modulation : Modulation method for the signal currently being received.

Symbol Rate : Symbol Rate of the signal currently being received.

Code Rate : Code Rate of the signal currently being received.

Pilot : On/off status of the Pilot signal currently being received.

SAT Tuning Status (2/3)



FEC Lock: Current lock/unlock status of the error-correction function

of the receiver.

C/N : Current reception C/N. The limit C/Ns in normal reception are shown below. Use the following values only as a guide.

Limit C/N in normal reception

Modulation	Code Rate	Limit C/N in Normal Reception	Modulation	Code Rate	Limit C/N in Normal Reception
S2_QPSK	1/2	1.1	S2_8PSK	3/4	8.1
S2_QPSK	3/5	2.4	S2_8PSK	5/6	9.6
S2_QPSK	2/3	3.2	S2_8PSK	8/9	11.0
S2_QPSK	3/4	4.2	S2_8PSK	9/10	11.3
S2_QPSK	4/5	4.8	S_QPSK	1/2	5.2
S2_QPSK	5/6	5.3	S_QPSK	2/3	7.0
S2_QPSK	8/9	6.4	S_QPSK	3/4	8.0
S2_QPSK	9/10	6.6	S_QPSK	5/6	9.1
S2_8PSK	3/5	7.9	S_QPSK	7/8	9.8
S2_8PSK	2/3	8.0			

Viterbi BER : Bit error rate while the S QPSK signal is being received.

While the S2_QPSK or S2_8PSK signal is received,
**** is displayed. If the value is 2e-4 or less, block noise

is not caused by a problem in the tuner.

LDCP BER : Bit error rate while the S2_QPSK or S2_8PSK signal is being received. While the S_QPSK signal is received,

**** is displayed.

PER : Packet error rate during reception.

If the value is 0.000e-00, block noise is not caused by a

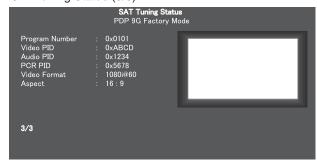
problem in the tuner.

LNB POWER: Voltage currently being supplied to the LNB
LNB BAND : Frequency band that is currently set to the LNB
TS Bit Rate : TS Bit Rate of the signal currently being received
Time : Measured duration of Viterbi BER. LDCP BER. or F

: Measured duration of Viterbi BER, LDCP BER, or PER. To reset the value to 0 and restart measuring, press the

← or → key on the remote control unit.

SAT Tuning Status (3/3)



Program Number: No. of the program currently being received.

Video PID : Video PID of the program currently being received.
Audio PID : Audio PID of the program currently being received.
PCR PID : PCR PID of the program currently being received.
Video Format : Video Format of the program currently being received.
Aspect : Aspect ratio of the program currently being received.

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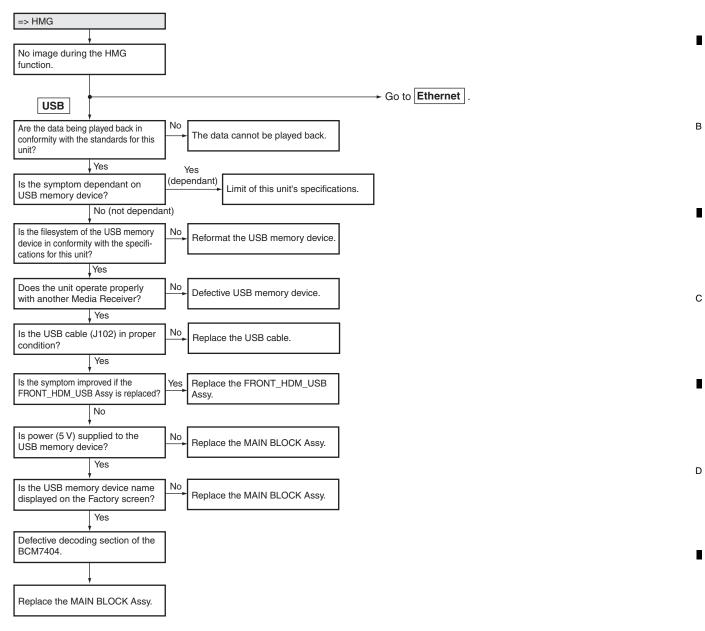
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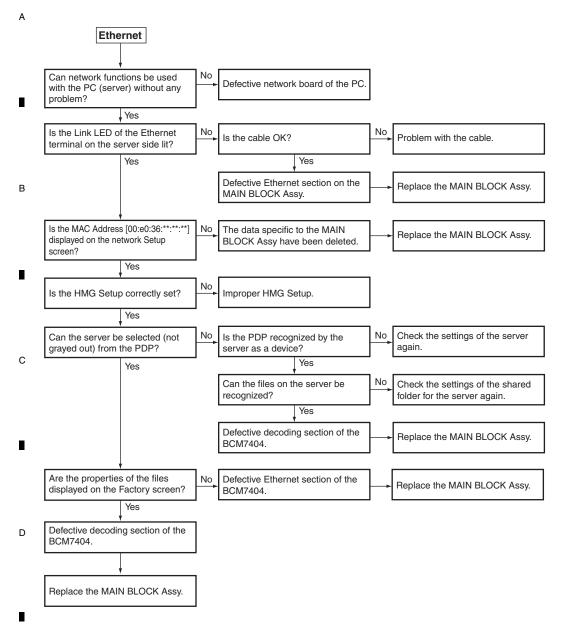
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Flowchart of Failure Analysis for The HMG



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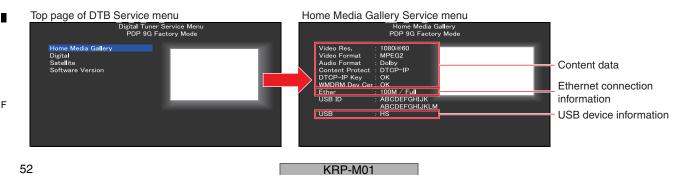


[HMG] How to enter DTB Service menu

Note: Use the remote control unit that supports Factory mode, because the DTB Service menu is accessible from Factory mode.

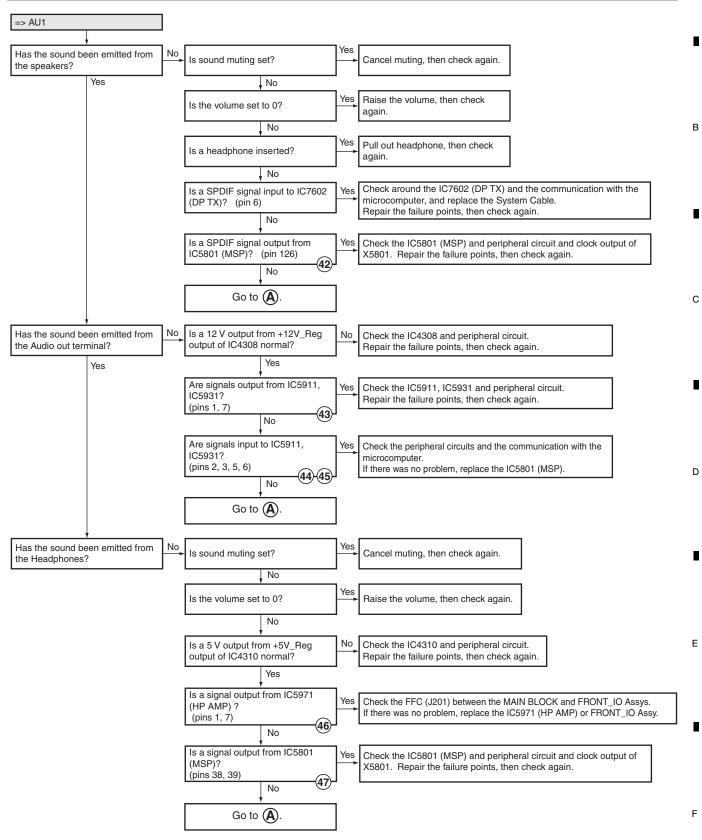
- Step 1: Press the FACTORY key on the remote control unit to display the INFORMATION screen of Factory mode.
- Step 2: Press the MUTING key on the remote control unit 4 times to display the INITIALIZE screen.
- Step 4: Press the ENTER/SET key on the remote control unit to display the "MODE SHIFT <=>: No" indication at the bottom of the screen.
- Step 5: Press the ← or → key on the remote control unit until the "MODE SHIFT <=>: YES" indication is displayed at the bottom of the screen.
- Step 6: Press and hold the ENTER/SET key on the remote control unit pressed for 5 seconds or more to activate DTB Service menu.

The Home Media Gallery (HMG) Service menu is indicated below:



[5] AUDIO SYSTEM

Flowchart of Failure Analysis for The Audio System

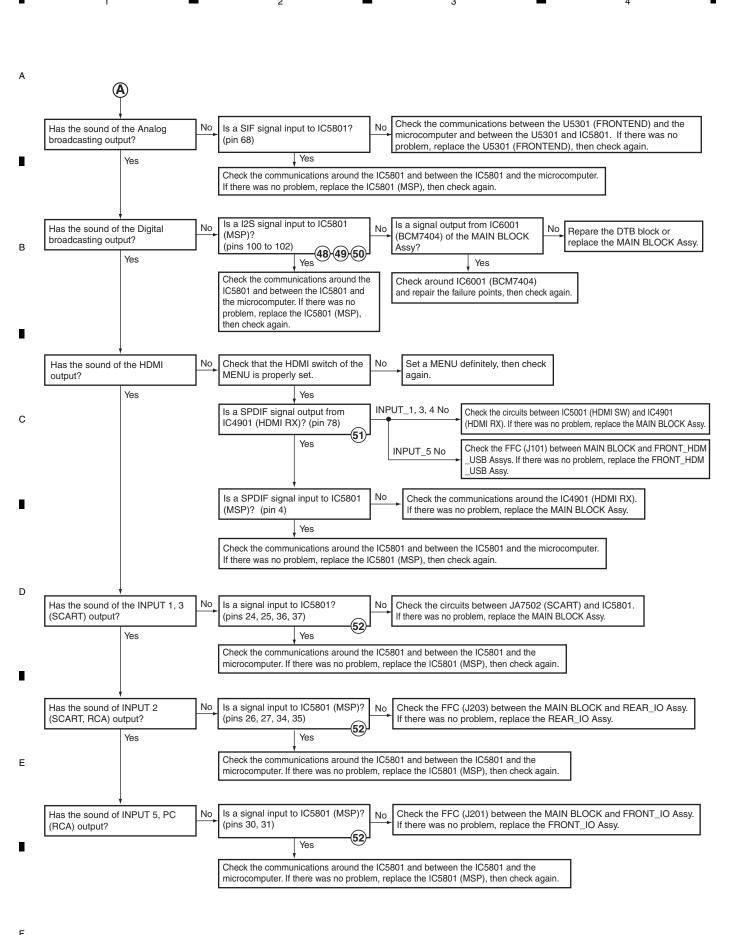


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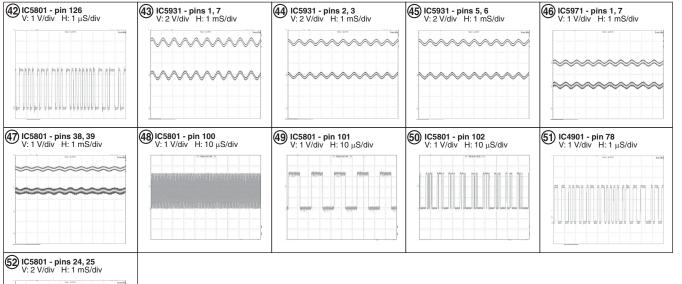
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• Waveforms
Input signal: L/R 1 kHz, 0.5 Vrms (VOL 30)



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5.3 DIAGNOSIS OF PD (POWER-DOWN)

[1] BLOCK DIAGRAM OF THE POWER-DOWN SIGNAL

Note:

The figure ① indicate the number of times the Red LED flashes when power-down occurs in the corresponding route.

MAIN BLOCK ASSY M1 V+5.1V_STB V+17V V+12V V+6.5V V+3.4V_STB $(\mathbf{1})$ PD_TRG INV. IC7003 MAIN_ucom **RELAY** (EMMA2) AC_DET RST3 INV. IC6811 IF_ucom

POWER SUPPLY UNIT <PKG 1> P2 STB5.1V +17V +12V +6.5V STB3.4V PD TRG **RELAY** AC_DET <Protection function> Overcurrent Protection (OCP) V+3.4V_STB, V+5.1V_STB, VCC 3 outputs Overvoltage Protection (OVP) V+6.5V, V+12V, V+17V V+3.4V_STB (latches for long time) Under Voltage Protection (UVP) V+6.5V, V+12V, V+17V Thermal Shut Down (TSD) V+6.5V output diode: D351 block V+3.4V_STB (latches for long time)

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SECONDARY PRIMARY POWER THERMISTOR **FUSE ⋘** VCC DC+120V to +390V AC IN DC/DC 85 to 276V CONVERTOR NEUTRAL REGULATOR STB DC/DC CONVERTOR AND MICRO AC_DET CIRCUIT COMPUTER **POWER SUPPLY** UNIT <PKG 2>

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[2] PD (POWER-DOWN) DIAGNOSIS OF FAILURE ANALYSIS

■ How to Distinguish the PD (Power-Down)

About the LED for checking causes of power-down

No LED for checking causes of power-down is provided for the POWER SUPPLY Unit of the MR. However, by checking the waveforms at terminals of the microcomputer, whether a power-down was caused by failure in the POWER SUPPLY Unit, and if it was, which power system among the four was in failure can be inferred. The points at which to check waveforms and how to distinguish power-down causes are described below:

<Points at which to Check Waveforms>

Waveforms between Pin 3 of CN801 and GND (secondary radiator, display chassis, etc.)
Refer to the section "Note on Removing the POWER SUPPLY Unit from the Chassis and Method for Resettig Standby Power Latchup" in the "7.2 DISASSEMBLY".

<How to Distinguish>

If a power-down was caused by failure in the POWER SUPPLY Unit, a pulse waveform is output at the above-mentioned points. (It is assumed that STB3.4 V power is properly output.)

By counting the frequency of "Lo" in the pulse waveform, the cause of power-down can be identified.

Frequency		Cause		
of "Lo"	Output Voltage	Overvoltage (OV) or Undervoltage (UV)		
Once	+12V	OV or UV *		
Twice	+17V	OV or UV *		
3 times	+6.5V	OV or UV *		
4 times	Protection against overheat			

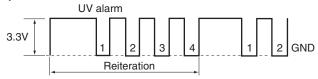
*How to distinguish OV and UV:

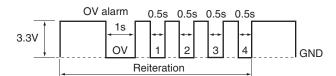
If the first "Lo" duration of a pulse is long (1 s), the cause is OV.

As the three output voltages are electromagnetically linked and interact with one another,

the frequency may vary among 1-3, depending on the type of power-down.

Examples:





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■ How to Diagnose the PD

Frequency of LED Flashing	History Indication in Factory Mode	Assy	Cause of power- down (activated protection circuit)	Point to be Checked	Possible Defective Parts	
Red,	MR-PWR	MAIN	Overcurrent in	5V_ANT-REG	IC4305, C4305	
once		BLOCK Assy	6.5 V power	5V_IO-REG	IC4310, C4301	
		Addy		3CH-DD	IC4402	
				converter	C4405, C4406, C4409, C4463, C4464, C4466 to C4468	
				FET	Q4417, Q4416, Q4411	
				1CH-DD converter	IC4501, C4517	
			Overcurrent in	FAN-REG	IC4302, C4342	
			12 V power	8V_IO-REG	IC4309, C4315	
				LNB	IC4503	
			Overcurrent in 17 V power	12V_IO-REG	IC4308, C4303	
			Overcurrent in	1.8V_IO-REG	IC4604, C4609	
			3.4 V power	1.5V_10-11EG	C4820, C8103	
		POWER SUPPLY Unit	V+6.5V UVP	TP V+6.5V	Voltage drop due to overcurrent on the load side	
		Unit	V+12V UVP	TP V+12V	Voltage drop due to overcurrent on the load side	
			V+17V UVP	TP V+17V	Voltage drop due to overcurrent on the load side	
			STB3.4V OCP	TP STB3.4V	C151, C153, C152, D152, or Z152, and abnormal current on the load side that is connected to STB3.4 V power	
			STB5.1V	TP STB5.1V	C155 and abnormal current on the load side that is connected to STB5.1 V power	
			OCP		And abnormal current on the load side that is connected to STB5.1 V power	
			VCC	TP V+6.5V	D351, C351, C352, C353, and abnormal current on the load side that is connected to V+6.5V power	
			OCP	TP V+12V	D352, C357, C358, and abnormal current on the load side that is connected to V+12V power	
				TP V+17V	D353, C359, and abnormal current on the load side that is connected to V+17V power	
			STB3.4V OVP	TP STB3.4V	PC121	
			VCC	TP V+6.5V	PC301, Breakage in the line to/from the P2 output connector	
			OVP	TP V+12V		
			STB3.4V TSD		Z121 control IC and abnormal current on the load side that is connected to STB3.4 V power	
			V+6.5V Rectifier diode (D351) TSD		D351 or D352, and abnormal current on the load sides that is connected to V+6.5 V and V+12 V	

Note: Although replacement of the whole POWER SUPPLY Unit is required (replacement of only defective parts on the POWER SUPPLY Unit is not possible), the circuit symbols are described for reference

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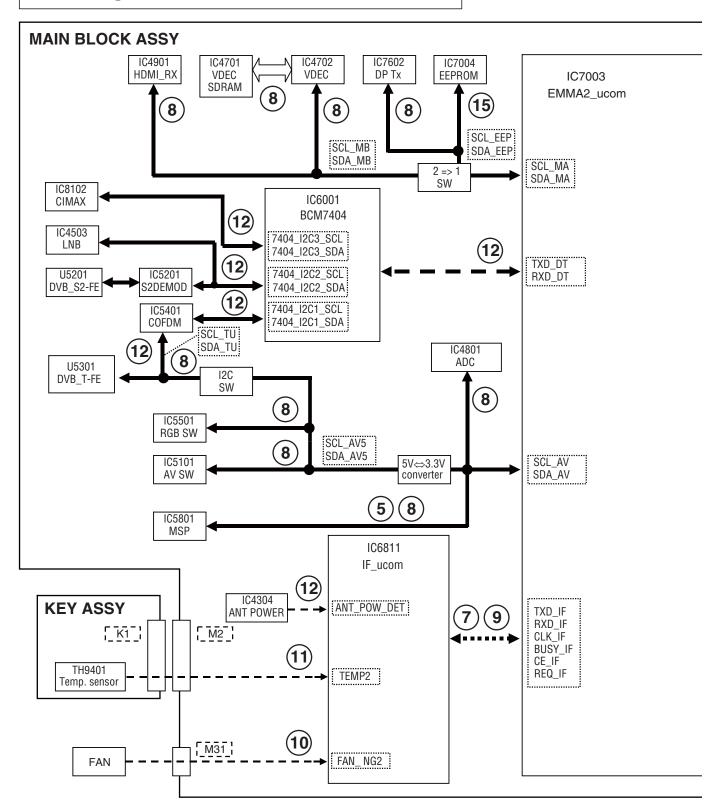
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5.4 DIAGNOSIS OF SD (SHUTDOWN)

[1] BLOCK DIAGRAM OF THE SHUTDOWN SIGNAL

Note: The figures ① to ⑤ indicate the number of times the Blue LED flashes when shut-down occurs in the corresponding route. ② LED is not flashed.

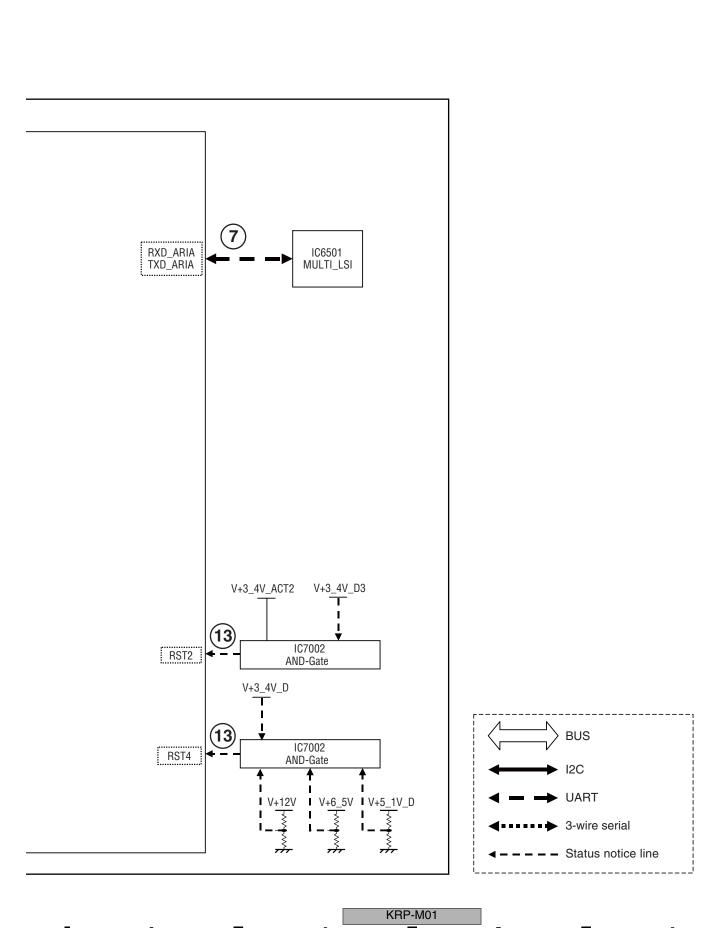


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[2] SD (SHUTDOWN) DIAGNOSIS

Frequency of	Major Type	Detailed Type	Log Indication in Factory Mode		
LED Flashing	iwajor Type	Detailed Type	MAIN	SUB	
Blue 5	Audio	Abnormality in MSP	AUDIO	MSPMAP	
Blue 7	Failure in 3-wire serial communication	IF microcomputer	MA-3L	IF	
Dide /	with the main microcomputer MULTI		IVIA-3L	MULTI	
		Tuner1		FE1	
		MSP/MAP		MSPMAP	
		AV Switch		AV-SW	
		RGB Switch		RGB-SW	
Blue 8	Failure in IIC communication with	Main VDEC	MA-IIC	VDEC	
	the main microcomputer	VDEC SDRAM		SDRAM	
		AD/PLL		ADC	
		HDMI	1	HDMI	
		DisplayPort Tx	1	DP-TX	
Blue 9	Failure in communication with the	-	MAIN	-	
	main microcomputer				
Blue 10	Abnormality in FAN	FAN2	FAN	FAN2	
Blue 11	High temperature of the unit	-	TEMP2	_	
		DTV start up error		PS/RST	
		DTV communication error		RETRY	
		DEVICE ERR	1	DEVICE	
		Tuner1		DE-FE	
		DTV Antenna	1	D-ANT	
Blue 12	Digital Tuner	Application	DTUNER	DTVAPP	
(Actually, Blue 12		COFDM		DEMOD	
LED is not flashed.)		Tuner S2		DE-FES	
		S2DEMOD		DEMODS	
		LNB		DE-LNB	
		S2 Antenna		S-ANT	
		DC-DC Converter power decrease		M-DCDC	
			1		
Blue 13	Failure in the power supply	POWER SUPPLY	RST-MA	RELAY	
Blue 15	Main EEPROM	Main EEPROM communication error	MA-EEP	-	

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unication line between IF and MAIN IC7003, IC6811 Check the communication lines (TXD_IF/RXD_IF/CLK_IF/BUSY_IF/CE_IF/REC unication line between MULTI and MAIN IC7003, IC6501 Check the communication lines (TXD_ARIA/RXD_ARIA) DESCRIPTION OF THE SECTION OF THE	Checkpoint	Possible Defective Part	Remarks
unication line between MULTI and MAIN IC7003, IC6501 Check the communication lines (TXD_ARIA/RXD_ARIA) munication line between Tuner and MAIN IC5001, IC7003 Check the communication lines (SCL_TU/SDA_TU or SCL_AV/SDA_AV) munication line between MSP/MAP and MAIN IC501, IC7003 Check the communication lines (SCL_AV/SDA_AV) Check the communication lines (SCL_AV/SDA_AV) Check the communication lines (SCL_AV/SDA_AV) Check the communication lines (SCL_AV/SDA_AV5) Check the communication lines (SCL_MB/SDA_MB) Unication line between M_DEC and MAIN IC4701, IC4702 Check the communication lines (SCL_MB/SDA_AV5) Check the communication lines (SCL_MB/SDA_AV6) Check the communication lines (S	Power supply for MSP and MSP	IC5801, IC4604, Q4616	Check the MSP, its power and periphery parts (e.g. reset line).
munication line between Tuner and MAIN U5301, IC7003 Check the communication lines (SCL_TU/SDA_TU or SCL_AV/SDA_AV) munication line between MSP/MAP and MAIN IC5801, IC7003 Check the communication lines (SCL_AV/SDA_AV) munication line between RGB_SW and MAIN IC5101, IC7003 Check the communication lines (SCL_AV/SDA_AV5) munication line between RGB_SW and MAIN IC5501, IC7003 Check the communication lines (SCL_AV5/SDA_AV5) munication line between M_VDEC and MAIN IC4702, IC7003 Check the communication lines (SCL_MS/SDA_MB) munication line between VDEC and SDRAM IC4701, IC4702 Check the communication lines (SCL_MS/SDA_MB) munication line between ADC and MAIN IC4801, IC7003 Check the communication lines (SCL_AV5/SDA_AV5) munication line between HDMI_RX and MAIN IC4901, IC7003 Check the communication lines (SCL_AV5/SDA_AV6) munication line between DP_TX and MAIN IC4901, IC7003 Check the communication lines (SCL_AV5/SDA_AV6) munication line between DP_TX and MAIN IC4901, IC7003 Check the communication lines (SCL_MS/SDA_MB) munication line between DP_TX and MAIN IC6901, IC7003 Check the communication lines (SCL_EFP/SDA_EFP) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_EFP/SDA_EFP) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_EFP/SDA_EFP) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_MS/SDA_MB) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_MS/SDA_MB) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_MS/SDA_MB) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_MS/SDA_MB) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_MS/SDA_MB) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_MS/SDA_MB) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_MS/SDA_MB) munication line between IF and MAIN I	Communication line between IF and MAIN	IC7003, IC6811	Check the communication lines (TXD_IF/RXD_IF/CLK_IF/BUSY_IF/CE_IF/REQ_IF)
munication line between MSP/MAP and MAIN IC5801, IC7003 Check the communication lines (SCL_AV/SDA_AV) munication line between AV_SW and MAIN IC5101, IC7003 Check the communication lines (SCL_AV/SDA_AV5) munication line between RGB_SW and MAIN IC5501, IC7003 Check the communication lines (SCL_AV5/SDA_AV5) munication line between M_VDEC and MAIN IC4702, IC7003 Check the communication lines (SCL_MB/SDA_MB) munication line between VDEC and SDRAM IC4701, IC4702 Check the communication lines (SCL_AV/SDA_AV5) munication line between ADC and MAIN IC4801, IC7003 Check the communication lines (SCL_AV/SDA_AV) munication line between HDMI_RX and MAIN IC4901, IC7003 Check the communication lines (SCL_AV/SDA_AV) munication line between DP_TX and MAIN IC7602, IC7003 Check the communication lines (SCL_MB/SDA_MB) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_EEP/SDA_EEP) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_EEP/SDA_EEP) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_EEP/SDA_EEP) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_EEP/SDA_EEP) munication line between IF and MAIN IC6811, IC7003 Check the fan. (SD10 does not detect it at the temperature that fans do not the serve of the FAN ICANON I	Communication line between MULTI and MAIN	IC7003, IC6501	Check the communication lines (TXD_ARIA/RXD_ARIA)
munication line between AV_SW and MAIN IC5101, IC7003 Check the communication lines (SCL_AV5/SDA_AV5) munication line between RGB_SW and MAIN IC5501, IC7003 Check the communication lines (SCL_AV5/SDA_AV5) munication line between M_VDEC and MAIN IC4702, IC7003 Check the communication lines (SCL_MB/SDA_MB) munication line between VDEC and SDRAM IC4701, IC4702 Check the communication lines (SDRAM), Failure in SDRAM munication line between ADC and MAIN IC4801, IC7003 Check the communication lines (SCL_AV/SDA_AV) munication line between HDMI_RX and MAIN IC4901, IC7003 Check the communication lines (SCL_MB/SDA_MB) munication line between DP_TX and MAIN IC7602, IC7003 Check the communication lines (SCL_EEP/SDA_EEP) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (SCL_EEP/SDA_EEP) munication line between IF and MAIN IC6811, IC7003 Check the communication lines (TXD_IF/RXD_IF/CLK_IF/BUSY_IF/CE_I/REQ_IF) munication line between IF and MAIN IC6811, IC7003 Check the fan. (SD10 does not detect it at the temperature that fans do not the fan motor Check the fan motor Check the fan control regulator IC4302 Check that the voltage outputs it. TEMP2 A shutdown occurs because of high temperature. TEMP2 A shutdown occurs because of high temperature. TEMP2 are of the cable between M2 and K1 CN4204, CN9401 Check if cables are firmly connected. Of BCM7404 IC6001 Check the startup of the BCM7404 and the communication line with MAII check if cables are firmly connected. Check the startup of the BCM7404 and the communication line with MAII check if cables are firmly connected.	IC communication line between Tuner and MAIN	U5301, IC7003	Check the communication lines (SCL_TU/SDA_TU or SCL_AV/SDA_AV)
munication line between RGB_SW and MAIN IC5501, IC7003 Check the communication lines (SCL_AV5/SDA_AV5) munication line between M_VDEC and MAIN IC4702, IC7003 Check the communication lines (SCL_MB/SDA_MB) Check the communication lines (SCL_MB/SDA_MB) Check the communication lines (SDRAM), Failure in SDRAM IC4701, IC4702 Check the communication lines (SDRAM), Failure in SDRAM IC4801, IC7003 Check the communication lines (SCL_AV/SDA_AV) Check the communication lines (SCL_AV/SDA_AV) Check the communication lines (SCL_MB/SDA_MB) Check the communication lines (SCL_MB/SDA_MB) Check the communication lines (SCL_MB/SDA_MB) Check the communication lines (SCL_EEP/SDA_EEP) Check the communication lines (SCL_EEP/SDA_EEP) Check the communication lines (TXD_IF/RXD_IF/CLK_IF/BUSY_IF/CE_I/REQ_IF) Check the fan. (SD10 does not detect it at the temperature that fans do not the serve of the FAN FAN_NG Check if cables are firmly connected. Check that the voltage outputs it. TEMP2 A shutdown occurs because of high temperature. TEMP2 are firmly connected. Check if cables are firmly connected. Check the startup of the BCM7404 and the communication line with MAII cunication line between BCM7404 and MAIN IC6001 Check the startup of the BCM7404 and the communication line with MAII check the BCM7404 and the communication line with MAII check the BCM7404 and the communication line with MAII check the BCM7404 and the communication line with MAII check the BCM7404 and the communication line with MAII check the BCM7404 and the communication line with MAII check the BCM7404 and the communication line with MAII check the BCM7404 and the communication line with MAII check the BCM7404 and the communication line with MAII check the BCM7404 and the communication line with MAII check the	IC communication line between MSP/MAP and MAIN	IC5801, IC7003	Check the communication lines (SCL_AV/SDA_AV)
Check the communication lines (SCL_MB/SDA_MB) Unication line between M_VDEC and SDRAM IC4701, IC4702 IC4801, IC7003 IC4801, I	IC communication line between AV_SW and MAIN	IC5101, IC7003	Check the communication lines (SCL_AV5/SDA_AV5)
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	Communication line between BCM7404 and MAIN	IC6001	Check the startup of the BCM7404 and the communication line with MAIN
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	Front-end block	IC6001, U5301	Check the BCM7404, terrestrial tuner and periphery devices.
as supply voltage IC4304 Check the IC4304 (overcurrent detection IC), its periphery devices and antenna connect	Antenna supply voltage	IC4304	Check the IC4304 (overcurrent detection IC), its periphery devices and antenna connection lir
oplication IC6001	DTV application	IC6001	
M IC5401 Check the communication line between BCM7404 and COFDM	COFDM	IC5401	Check the communication line between BCM7404 and COFDM
S2 U5201 Check the communication line between S2DEMOD and F.E.	Tuner S2	U5201	Check the communication line between S2DEMOD and EE

IC5201

IC4503

IC4503

IC7002

IC7002

CN4203

IC7004, IC7003

POWER SUPPLY Unit

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Check the communication line between BCM7404 and S2DEMOD

Check the LNB IC and periphery parts, and antenna connection line.

Check if each voltages are started.

Check if each voltages are started.

Check if each voltages are started.

Check if cables are firmly connected.

Check the communication lines (SCL_EEP/SDA_EEP)

Check the communication line between BCM7404 and LNB IC, and check the periphery parts of LNB IC.

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S2DEMOD

Antenna supply voltage

V+12V, V+6_5V, V+17V

Check the cable M1

RST2 V+3_4V_ACT2, V+3_4V_D3

RST4 V+12V, V+6_5V, V+5_1V_D, V+3_4V_D

IIC communication line between EEPROM and MAIN

5.5 NON-FAILURE INFORMATION

[1] INFORMATION ON SYMPTOMS THAT DO NOT CONSTITUTE FAILURE

Symptom	Cause, item to check, information
HDMI: Symptoms concerning the input format and setting	s
The picture color for an INPUT 1 or 3 to 5 signal is not correct.	The color setting for INPUT 1 or 3 to 5 is not compatible with that of the output equipment. Check whether the color setting is YPbPr or RGB.
The video signal to INPUT 1 or 3 to 5 is not displayed, and a message is displayed.	A unsupported video signal is input.
The audio signal input to the INPUT 1 or 3 is not output. No HDMI signal is input.	The audio setting for INPUT 1 or 3 is any setting, and a video signal is not input. If the audio setting is any setting, to output an analog audio signal, the HDMI signal must be input. (If a DVI device is to be connected, use a DVI-HDMI conversion cable.) If the HDMI video signal is not input, the analog audio signal is not output.
No sound of signals to INPUT 1 or 3 to 5 is output.	The setting on the side of the HDMI output equipment is wrong. Example: Dolby Digital
The 1080p input signal is not displayed properly or at all, although the 1080i input signal is displayed properly.	Check that the connected cable supports HDMI Category 2. (As the clock frequency for the 1080p signal is triple that for the 1080i signal, signal degradation caused by a cable must not be neglected. A cable supporting HDMI Category 2 can be used for the 1080p signal. Although some conventional cables can support the 1080p signal, some others cannot.)
SCART video output	
The video output signal from the SCART connector is deteriorated. Or when the video output signal from the SCART connector is recorded, its playback picture is deteriorated.	The video signal output from the SCART connector is Macrovision protected.
The video signal is not output when the component signal is input to INPUT 2.	The video signal is not output from the SCART connector when the component signal is selected.
The video signal is not output when the video signal is input to INPUT 1 or 3 to 5.	The video signal is not output from the SCART connector when the HDMI signal is selected.
AUDIO OUT and SCART	
The image displayed on the PDP is not synchronized with the sound from the SCART.	The audio signal from the SCART connector is synchronized with the video output signal from the SCART connector. And the audio signal from the AUDIO OUT is synchronized with the video signal that is currently displayed.
DIGITAL OUT	
Playback of the signal from the DIGITAL audio output connector is possible, but recording is not possible.	The video signal output from the DIGITAL connector is copy-protected.
The digital audio output signal from the DIGITAL connector is not synchronized with that from the SCART video output.	The digital audio output signal from the DIGITAL connector is synchronized with the video signal that is currently displayed, and not with the SCART video output.
Miscellaneous	
The no-signal off function is not activated.	The no-signal off and no-operation off functions are effective only if video (composite, S video,
The no-operation off function is not activated.	component, HDMI [excluding PC]) input or TV input is selected.
Power management does not function.	Power Management is effective only while an analog PC signal is being input. It is not effective with HDMI-PC signal input.
The AUTO SETUP function is not activated.	The Auto Setup function is effective only while an analog PC signal is being input. This function does not work if an analog PC signal is not input, even if the INPUT PC is selected.
Control via the SR connector is not possible.	Wrong connection of the cable to the PC INPUT (AUDIO) connector is suspected.
The audio signal from the PC is not output.	Wrong connection of the cable to the SR connector is suspected.
The picture-quality setting (AV Selection) is not stored.	The picture-quality setting is stored for each input. As the setting is changed when another input is selected, the user may have a false idea that the setting is not stored.
The picture size changes arbitrary.	The Auto Size setting is set to ON.
The display position of the screen changes slightly while the screen is on.	The orbiter function for minimizing the effects of phosphor burn is activated. Although the setting for this function can be changed on the Home menu, retaining the factory setting is strongly recommended.
The video signal to the S video connector is not displayed.	As the signal input to the connector that has been selected on the INPUT SELECT submenu of the Home menu is selected (this does not apply to the connectors located on the side of the unit), check the menu setting. If the output signal is not a will be completely a signal to all the connectors located on the side of the unit), check the menu setting. If the output signal is not a will be completely as a signal to all the connectors located on the side of the unit), check the menu setting. If the output signal is not a will be connectors located on the side of the unit), check the menu setting.
The video signal to the composite video connector is not displayed.	is not available even if the input signal is properly selected, input a signal to other input functions, check the connecting cables, or check the settings for the connected equipment. Note that if cables are connected to both the HDMI connector and composite video connector of INPUT 5, the HDMI connector will have priority over the composite video connector.

SUPPLEMENT: On the video setting for HDMI

There are three types of HDMI output formats: color difference 4:4:4, color difference 4:2:2, and RGB4:4:4.

(The proportions, such as 4:4:4 and 4:2:2, represent those of the amount of data for video signal components. For example, as for color difference 4:4:4, the proportion of the amount of data as for Y, Cb, and Cr is 4:4:4.)

It is required to make the settings of the PDP according to the settings of the output equipment. For usual operation, however, set them to AUTO. If the color is inappropriate, make the settings manually.

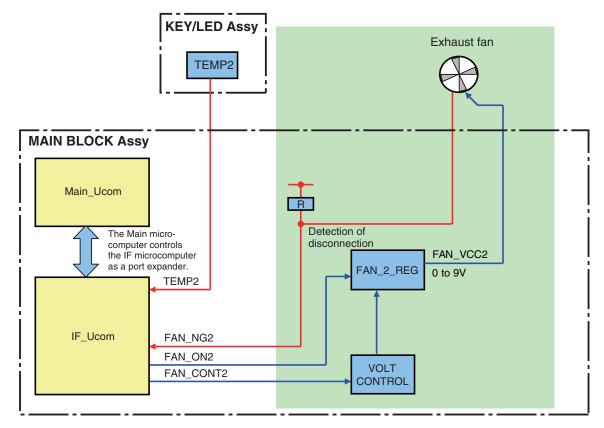
In the HDMI system, video signals are coded at 24 bits per pixel and transmitted as a series of 24-bit pixels. In a case of color difference 4:4:4, Y, Cb, and Cr use 8 bits each. In a case of color difference 4:2:2, Y, Cb, and Cr use 12 bits each, but Cb and Cr are transmitted at a half sampling rate of Y. This unit is capable of processing the upper 10 bits out of 12 bits of video data. Recent high-end DVD players, such as Pioneer DV-79AVi, are capable of outputting 10-bit color-difference signals. In general, it is said that picture quality for color difference 4:2:2 format is assumed to be higher, because human eyes are more sensitive to luminance than to colors. In the case of RGB4:4:4, R, G, and B use 8 bits each.

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5.6 OUTLINE OF THE OPERATION

[1] SPECIFICATION OF THE FAN CONTROL

■ Block diagram

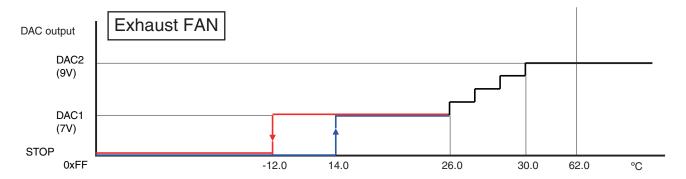


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■ Operation specifications



Notes:

- The operating temperature of the fan is different from the ambient temperature, because the sensor temperature is read by the microcomputer.
- The fan may not start rotating until the internal temperature of the unit reaches a certain level, such as immediately after the unit is turned on.
- When the temperature rises, the sensor voltage of TEMP2 decreases.
- When the voltage of the DAC output for exhaust FAN decreases, rotation speed of FAN rises.

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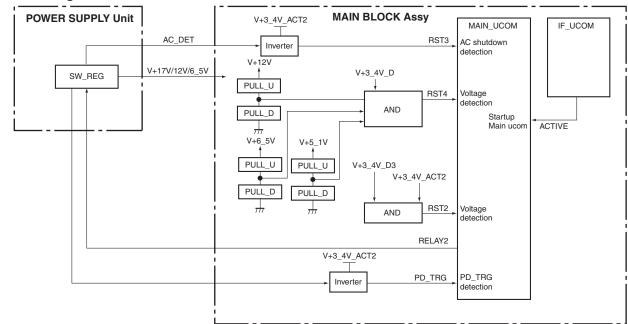
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[2] PROCESSING IN ABNORMALITY

Power supply and DC-DC converter

Circuit configuration



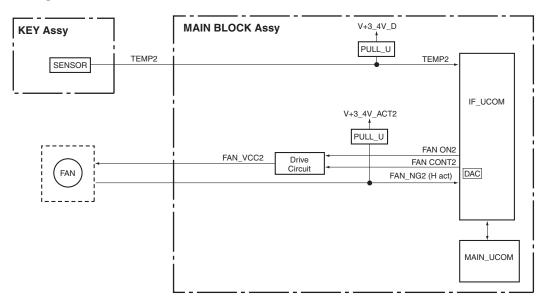
Specifications for port monitoring

Port Name	SD/PD Indication	Determination Condition	Monitoring conditions	Operation
RST2	ASIC power (M-DCDC)	Shutdown occurs when the signal is "L." for 5 sec after PSW1 is ON. or for 2 sec while the unit is ON.	Panel screen ON (RST4 = H and PSW1 = H) While awaiting restoration of RST2 (RST2 = L)	Shutdown occurs immediately Blue LED flashes 13 times
RST3	_	_	Excepting passive standby	If "RST3 = H" (AC_OFF) is detected under the monitoring conditions, a power-off process starts. Monitoring of the RST3 port is continued, and monitoring of other ports is interrupted. Communication is controlled only by the IF microcomputer. The port outputs are set as specified. If the signal at the RST3 port continues to be H after 30 mS of waiting, monitoring is continued. If RST3 is L, a restoration process starts according to the latest power-on/-off status.
RST4	MAIN power (RELAY)	Shutdown occurs if the signal is "L." for 5 sec after RELAY2 is ON. or for 2 sec while the unit is ON or in Functional STB.	RELAY2 = ON (High)	Shutdown occurs immediately Blue LED flashes 13 times
PD_TRG	VCC power (MR-PWR)	Shutdown occurs when the signal is continuously "L" for 30msec * 3 times after RELAY2 is ON.	RELAY2 = ON Monitor it after 3 sec.	Power-down occurs immediately Red LED flashes once

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Fan and temperature sensor

Circuit configuration



Specifications for port monitoring

Port Name	SD/PD Indication	Determination Condition	Monitoring conditions	Operation
FAN_NG2	FAN	Shutdown occurs when the signal is "H." 1 S * 3 times	RST4 = H and FAN_ON2 = H (Monitoring starts 3 sec after the above conditions are established.)	Shutdown occurs immediately Blue LED flashes 10 times
TEMP2	High temperature at MR	values equal to or	RST4 = H (Monitoring starts 1 sec after the above conditions are established.)	In the Panel screen ON: Shutdown occurs after the warning indication is displayed for 30 sec. In the Functional STB: Shutdown occurs immediately Blue LED flashes 11 times

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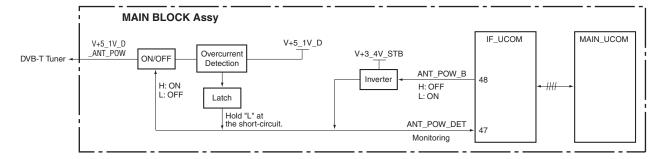
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Power supply for DVB-T Antenna for Europe

Circuit configuration

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Specifications for port monitoring

Port Name	SD/PD Indication	Determination Condition	Monitoring conditions	Operation
ANT_POW_DET		Warning message is displayed when the signal is L (100 mS, 3 times)		Output of a warning message for 60 sec.

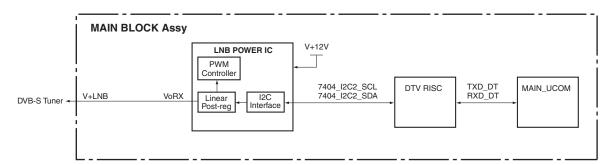
Conditions of circuit reset

The circuit can be reset by unplugging then plugging the power cord back in (it will not be reset by Standby ON/OFF).

Power supply for DVB-S Antenna for Europe

Circuit configuration

Note: Specifications for the output of warning-message indication will be added in the future.



Specifications for port monitoring

Port Name	SD/PD Indication	Determination Condition	Monitoring conditions	Operation
	S2 antenna short-circuited	Notification from DTV (at 7404_I2C2, OR of OLF bit and OTF bit of the LNB IC System Register is 1)	reception of satellite	Output of a warning message for 60 sec. Only while a satellite broadcast program is displayed on the main screen.

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Conditions of circuit reset

The circuit will be automatically reset after an error, such as short-circuiting of the antenna, is resolved and the unit is restored.

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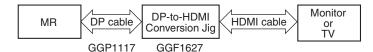
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[3] HOW TO OPERATE THE MEDIA RECEIVER SEPARATELY

Necessary items for operation

- Media Receiver
- DP-to-HDMI conversion jig: GGF1627 (with the AC adaptor) AC adaptor INPUT: 100 V to 240 V, 50/60 Hz, 0.3 A OUTPUT: DC 6 V, 1.8 A (-)—(+)
- Monitor or TV (with which an image with resolution of 1920 × 1080 p, 60 Hz can be displayed, with HDMI input) Note: When checking with DVI monitor, setting change of this jig is required.
- DP cable (GGP1117) and HDMI cable
- G8 or G9 remote control unit (in case of controlling by remote control unit)
- PC and RS-232C straight cable (in case of controlling by PC)
- HDMI -DVI cable (in case of connecting with DVI monitor)

Connection



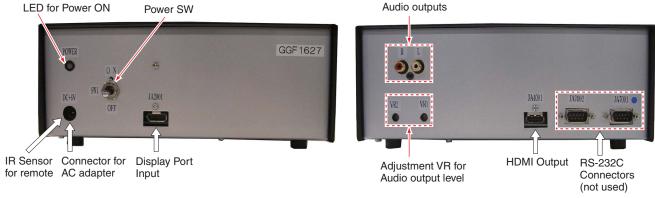


Fig.1 DP - HDMI Conversion tool (Front side)

Fig.2 DP - HDMI Conversion tool (Rear side)

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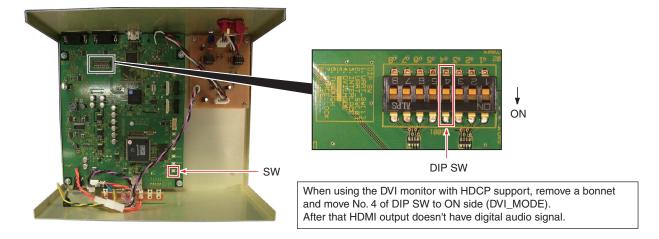


Fig.3 DP - HDMI Conversion tool DIP SW Setting (output mode setting for HDMI connector)

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Preparation

• Set the MR from System Operation mode to Standalone Operation mode.

The MR is normally set to System Operation mode. If the MR is turned on in this mode, an error warning is issued (the red and blue LEDs alternately flash), and it cannot be operated properly.

To change to Standalone Operation mode, proceed as follows:

[With an RS-232C command]

- 1. Turn the MR on. (The red and blue LEDs alternately flash to warn of an error.)
- 2. In this state, send the MRMS01 command via RS-232C ports.
- 3. Turn the MR off.

When the MR is turned on next time or after, it will be in Standalone Operation mode.

[With the keys on the MR]

- 1. Set the MR to Standby mode.
- 2. Press and hold the INPUT key of the MR pressed for at least 5 seconds.

 (This step is for giving a startup trigger in a case where the MR was in Passive Standby mode.)
- 3. Within 5 seconds after the INPUT key is released, press and hold the CHANNEL key of the MR for at least 10 seconds.
- 4. After the modes are changed, the red LED flashes twice then is lit (the unit enters Normal Standby mode).
- 5. Turn the unit off.

When the MR is turned on next time or after, it will be in Standalone Operation mode.

Operation

After the setting in Preparation is completed, turn the units on in the following order then perform analysis:

- 1. Turn the monitor or TV on. (Set the input mode to HDMI.)
- 2. Turn the DP-to-HDMI conversion jig on.
- 3. Turn the MR on.

If no image is displayed on the monitor or TV after the MR is turned on, press and hold the switch on the DP-to-HDMI conversion jig for about 1 sec.

How to control the MR

With the remote control unit:

The infrared receiver (IR) sensor for remote control unit is placed inside of the jig. Please point the remote towards the AC adaptor connector on the jig.

Unlike normal products, sensor reception of this tool is not so sensitive due to reduce interference with another Pioneer Plasma TV.

Please keep the distance between the remote control unit and the sensor less than 15cm.

• With RS-232C commands:

Connect a PC to the MR via their RS-232C ports and send RS-232C commands from the PC. (Baud rate: 9600 bps)

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After analysis is finished

After analysis in Standalone Operation mode is finished, before returning the MR to the customer, be sure to return the unit to System Operation mode, as shown in the procedures below.

If it remains in Standalone Operation mode, when it is connected with the customer's monitor, the monitor will detect a connection error and not operate properly, and no image will be displayed.

To set the MR to System Operation mode, proceed as follows:

[With an RS-232C command]

- 1. Turn the MR on.
- 2. Send the MRMS00 command via RS-232C ports.
- 3. Turn the MR off.

When the MR is turned on next time or after, it will be in System Operation mode.

4. Connect the MR directly with the monitor and check that they operate properly.

[With the keys on the MR]

- 1. Set the MR to Standby mode.
- 2. Press and hold the INPUT key of the MR pressed for at least 5 seconds. (This step is for giving a startup trigger in a case where the MR was in Passive Standby mode.)
- 3. Within 5 seconds after the INPUT key is released, press and hold the CHANNEL + key of the MR for at least 10 seconds.
- 4. After the modes are changed, the red LED flashes twice then is lit (the unit enters Normal Standby mode).
- 5. Turn the unit off.

When the MR is turned on next time or after, it will be in Standalone Operation mode.

Products whose proper operation has been proved when HDMI connection is performed with this MR

Model Number	Manufacturer	Built-in Audio AMP
PDP-5000EX	Pioneer	O (SP is required)
G8	Pioneer	O (SP is required except 42 inch)
FP241WJ	BenQ	× (External audio amp and SP is required)
3008WFP	DELL	× (External audio amp and SP is required)
HD2441W	EIZO NANAO	× (External audio amp and SP is required)

Attention point for audio volume

Audio output level is connected with MR volume level. If VR level of a MR is normal (around 10 - 15) and displayed HDMI TV or audio AMP is not so high level, sound level is very low. Please turn up the volume to appropriate level either or both units.

In case of turning up volume of MR to very high level during testing, turn down it to normal level and then turn off the unit. Otherwise when connecting the MR with panel, very loud sound is output from speakers and it might be a danger.

Attention point when using another Pioneer Plasma TV

Please pay attention to interference of IR signal when using Pioneer plasma TV as HDMI monitor.

If remote signal is also received to Pioneer plasma TV when operating MR with this tool and remote, you might confuse of which unit is controlled by the remote.

The following methods are some of suggestions to control only MR with the conversion tool.

Using the remote control unit and the conversion tool (AC adaptor connector) as nearly as possible hiding remote sensor of the plasma TV temporally.

• Setting Method to connect with DVI monitor with HDCP support (DVI mode)

- 1. Open bonnet with power off condition.
- 2. Refer to Fig. 3, move the DIP SW No. [4] to ON side.

After this setting, DVI mode signal is output from HDMI output connector of HDMI.

Note: 1. Some of DVI monitors might not display output signal from this conversion tool.

2. Output signal does not contain digital audio signal.

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5.7 OUTLINE OF RS-232C COMMAND

[1] PREPARED TOOLS

It is necessary to prepare the following one to use 232C command.

PC

В

- Application for control
- 232C cable (straight)
- * The setting of the Com port cannot be communicated if it doesn't do correctly. (Please follow a set explanation of PC in the Com port)

[2] USING RS-232C COMMANDS

Individual ports are provided for RS-232C and SR+ connectors with this model. Therefore, unlike the case of previous models, which required switching of exclusive operation between these connectors on the Integrator menu, switching is no longer required.

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5.8 LIST OF RS-232C COMMANDS

■ RS-232C command list

Na	mand me	Function		Effective only in Factory mode	Remarks
Α					
AMT	S00	Audio mute OFF			
	S01	Audio mute ON			
С			ı		
CHN	FWD	Changing tuner preset channel (1 step forward)			
	REV	Changing tuner preset channel (1 step reverse)			
CHM		Clearing data of the hour meter		•	Last memory is performed to the panel side.
CHR		Clearing data of the hour meter of MTB/MR side			Clear the hour meter of screen display of MAIN NG.
CNG		Clearing data of the SD history of MTB/MR side			
D					
DPT		Rewriting the Display Port Tx			
DW*		To subtract * to the adjustment value (* = 0 to 9, subtract 10 with DW0 and set to minimum value with DWF)			
F					
FAN		Factory mode: OFF		•	
FAY		Factory mode: ON			
FST	S35	Set each memory setting of MTB/MR side to the shipment state.		•	
I					
INA	***	Switching the terrestrial analog signal, direct tuning (***: channel number)	MAIN		
		Switching the terrestrial analog signal (Channnel is in the last.)	MAIN		
INC	***	Switching the terrestrial digital signal, direct tuning (***: channel number)	MAIN		
		Switching the terrestrial digital signal (Channnel is in the last.)	MAIN		
IND	***	Switching the satellite digital signal, direct tuning (***: channel number)	MAIN		
		Switching the satellite digital signal (Channnel is in the last.)	MAIN		
INH		Switching the Home Media Gallery / Home Gallery			
INP	S01	Input: INPUT1	MAIN		
	S02	Input: INPUT2	MAIN		
	S03	Input: INPUT3	MAIN		
	S04	Input: INPUT4	MAIN		
	S05	Input: INPUT5	MAIN		
	S06	Input: INPUT6 (PC)	MAIN		
М					
MRM	S00	Setting the mode to normal operation	MAIN	•	
	S01	Setting the mode to standalone operation	MAIN	•	
MST	S00	Display one screen			
	S01	PsideP (Main size: normal)			
	S02	PinP (Right down)			
	S03	PinP (Right up)			
	S04	PinP (Left down)			
	S05	PinP (Left up)			
	S08	SWAP (Exchanging sub-screen)			
0					
OSD	S00	OSD setting: OFF	MAIN		
	S01	OSD setting: ON	MAIN		
Р					
POF		Power: OFF	MAIN		
PON		Power: ON	MAIN		
PUC	S00	PURE CINEMA: OFF	MAIN	•	
	S01	PURE CINEMA: Standard	MAIN	•	
	S02	PURE CINEMA: Advance	MAIN	•	
	S03	PURE CINEMA: Smooth	MAIN	•	
Q					
QMT		Acquiring temperature of MTB/MR side and Fan speed			
	1	Acquiring shutdown information of MTB/MR side			
QNG		Acquiring strateown information of wire him to side			
		Acquiring unit data, such as the software version			

■ 2 **■** 3 **■** 4

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Command Name		Function	Last Memory	Effective only in Factory mode	Remarks
s					
SDF	S00	SRS DEFINITION: OFF			
	S01	SRS DEFINITION: DEFINITION1			
	S02	SRS DEFINITION: DEFINITION2			
	S03	SRS DEFINITION: DEFINITION3			
SML	***	Adjustment of the side mask level	MAIN	•	
SRS	S00	SRS: OFF			
	S01	SRS: SRS1			
	S02	SRS: SRS2			
	S03	SRS: SRS3			
SZM	S00	Setting the screen size to Dot by Dot	MAIN		
	S01	Setting the screen size to 4:3	MAIN		
	S02	Setting the screen size to FULL or FULL 1080i	MAIN		
	S03	Setting the screen size to ZOOM	MAIN		
	S04	Setting the screen size to CINEMA	MAIN		
	S05	Setting the screen size to WIDE or WIDE1	MAIN		
	S06	Setting the screen size to FULL 14:9	MAIN		
	S07	Setting the screen size to CINEMA 14:9	MAIN		
	S11	Setting the screen size to AUTO	MAIN		
	S12	Setting the screen size to WIDE2	MAIN		
Т					
TBS	S00	TRUBASS: OFF			
	S01	TRUBASS: TRUBASS1			
	S02	TRUBASS: TRUBASS2			
	S03	TRUBASS: TRUBASS3			
U					
UP*		To add * to the adjustment value (* = 0 to 9, add 10 with UP0 and set to maximum value with UPF)			
V					
VOL	UP*, DW*, ***	To adjust the volume			Use this command by designating the adjustment value *** (=000 to 060).

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ZME *** Initializing the video EEPROM data of the MTB/MR side

5.9 DETAILS OF RS-232C COMMANDS

[1] QS1 (Software Version Information of the Microcomputer)

Model information and version information are returned.

Command Format	Effective Operation Modes	Function	Remarks
[QS1]	Every Time	Output of status	Return data: 3 (ECO) + 112 (DATA) + 2 (CS) = 117 Byte

	Data Arrangement	Data Length	Output Example
ECO		3 byte	QS1
1	Resolution/Size	1 byte	F
2	Panel Generation	1 byte	9
3	Destination	1 byte	*
4	Grade	1 byte	*
5	Product Form	1 byte	А
6	Boot version of Module microcomputer	3 byte	-01A
7	Program version of Module microcomputer	8 byte	-01A'''
8	Boot version of sequence processor	3 byte	-01Z
9	Program version of sequence processor	8 byte	-01Z'''
10	Panel information	8 byte	G9_50F_2
11	Derivative operation identification	1 byte	*
12	Reserved (panel section)	7 byte	*****
13	, (comma)	1 byte	,
14	MTB generation	1 byte	9
15	MTB destination	1 byte	Α
16	MTB grade	1 byte	Н
17	MTB product form	1 byte	В
18	Program version of IF microcomputer	8 byte	-01A
19	Boot version of IF microcomputer	4 byte	01A
20	Program version of Main microcomputer	8 byte	-01A
21	Boot version of Main microcomputer	4 byte	01A
22	Common version of ASIC	8 byte	-01A
23	Boot version of ASIC	8 byte	01A
24	PRS version of ASIC	8 byte	-01A
25	PIC version of ASIC	8 byte	-01A
26	Common version of the Digital Tuner	8 byte	-0A
27	Boot version of the Digital Tuner	4 byte	01A
CS	2 Byte	2 byte	4A

11: D	11: Derivative Operation Identification		
*	Standard model operation		
1	Derivative model operation		

14: MTB Generation	
9 G9	

15: MTB Destination		
Α	North America	
С	China	
Е	Europe	
G	General	
J	Japan	
U	Australia	

16: M	16: MTB Grade		
Н	Elite/One body Europe HD /System Europe HD/One body Australia		
Т	Regular/One body Europe SD		
D	Derivative Model		
*	No Grade (Japan/General/China)		

17: MTB Product Form		
В	One body model	
S	System model	

1: Resolution/Size		
F	50-FHD (1920*1080)	
G	60-FHD (1920*1080)	

2: Panel Generation	
9	G9

3: Destination		
*	Commonness	
4: Gra	de	

Commonness Evaluation

5: Not used	
Α	"A" fixed

10: Panel Information (8 Byte)		
1 to 2nd byte	G9	Generation information
4 to 5th byte	50	50 inch
	60	60 inch
6th byte	F	FHD
8th byte	3	50 inch 2nd PLANT (Reserved)
	2	50 inch 2nd PLANT
	1	50 inch 1st PLANT
	-	Others

^{&#}x27;= space

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[2] QSE (DESTINATION PECULIAR INFORMATION)

Induce it peculiar, individual information is acquired.

Command Format	Effective Operation Modes	Function	Remarks	
[QSE]	Every time	Output of status	Return data: 3 (ECO) + 32 (DATA) + 2 (CS) = 37 Byte	

	Data Arrangement		Output Example
ECO	ECO		QSE
1	Check flag for production	1 byte	E
2	Reserved	3 byte	***
3	DTB hardware version	4 byte	0342
4	User setting password	4 byte	1234
5	DP Tx firmware version	16 byte	123456789ABCDEFG
6	DP Tx hardware version	4 byte	ABCD
CS	Check Sum	2 byte	13

[3] QMT (STATUS INFORMATION OF MTB/MR SECTION)

Temperature information on the MTB/MR section is acquired.

Command Format	Effective Operation Modes	Function	Remarks
[QMT]	Every time	Output of status	Return data: 3 (ECO) + 8 (DATA) = 11 Byte

	Data Arrangement		Output Example
ECO		3 byte	QMT
1	A/D value of temperature of MTB/MR section	3 byte	276
2	Reserved (*1)	1 byte	1
3	Reserved	4 byte	***

^{*1} Although the numerics 0, 1, and 2 can be input, those input values are invalid.

[4] QNG (SHUTDOWN INFORMATION OF MTB SECTION)

The command QNG is for acquiring the data from the 8 latest shutdown (SD) logs of the MTB section.

Command Format	Effective Operation Modes	Function	Remarks
[QNG]	Every time	To acquire data on the shutdown (NG) logs of MTB side	Return data: 3 (ECO) + 96 (DATA) + 2 (CS) = 101 Byte

	Data Arrangement	Data Length	Output Example
ECO		3 byte	QNG
1	Latest SD data	1 byte	1
2	Latest SD subcategory data	1 byte	0
3	Data from the MTB hour meter for the latest SD	7 byte	0752013
4	Reserved	3 byte	000 fixed
5	Second latest SD data	1 byte	5
6	Second latest SD subcategory data	1 byte	1
7	Data from the MTB hour meter for the second latest SD	7 byte	0495204
8	Reserved	3 byte	000 fixed
9	Third latest SD data	1 byte	Α
10	Third latest SD subcategory data	1 byte	2
11	Data from the MTB hour meter for the third latest SD	7 byte	0365814
12	Reserved	3 byte	000 fixed
13	Fourth latest SD data	1 byte	5
14	Fourth latest SD subcategory data	1 byte	0
15	Data from the MTB hour meter for the fourth latest SD	7 byte	0256612
16	Reserved	3 byte	000 fixed
17	Fifth latest SD data	1 byte	7
18	Fifth latest SD subcategory data	1 byte	2
19	Data from the MTB hour meter for the fifth latest SD	7 byte	0105628
20	Reserved	3 byte	000 fixed
21	Sixth latest SD data	1 byte	В
22	Sixth latest SD subcategory data	1 byte	0
23	Data from the MTB hour meter for the sixth latest SD	7 byte	0003009
24	Reserved	3 byte	000 fixed
25	Seventh latest SD data	1 byte	С
26	Seventh latest SD subcategory data	1 byte	1
27	Data from the MTB hour meter for the seventh latest SD	7 byte	00002A9
28	Reserved	3 byte	000 fixed
29	Eighth latest SD data	1 byte	С
30	Eighth latest SD subcategory data	1 byte	4
31	Data from the MTB hour meter for the eighth latest SD	7 byte	0000012
32	Reserved	3 byte	000 fixed
CS	2 Byte	2 Byte	7D

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< SD Information No. >

Frequency *	Shutdown Factor	Remarks (Operation)
1	Failure of Power Supply of VCC	Immediately Shutdown
5	Abnormality in MSP	Go to No. 5 Subcategory Information
6	Failure of communication with Module microcomputer	Immediately Shutdown
7	Failure in 3-wire serial communication of Main microcomputer	Go to No. 7 Subcategory Information
8	Failure in IIC communication of Main microcomputer	Go to No. 8 Subcategory Information
9	Failure in Communication of Main microcomputer	Immediately Shutdown
10(A)	Abnormality in FAN	Go to No. 10 Subcategory Information
11(B)	Abnormality in high temperature	Immediately Shutdown
12(C)	Failure in Digital Tuner	Go to No. 12 Subcategory Information
13(D)	Failure in Power Supply at MTB section	Go to No. 13 Subcategory Information
15(F)	Failure in Main EEPROM	Immediately Shutdown

*: Indicates the frequency of Blue LED flashing when the shutdown is occurred.

< No. 5 Subcategory Information on "Shutdown signal from D-Amp./short-circuit of speaker terminal" >

Value	Shutdown Factor	Remarks (Operation)
3	MSPMAP	Immediately Shutdown

< No. 7 Subcategory Information on "Failure in 3-wire serial communication of Main microcomputer" >

Value	Shutdown Factor	Remarks (Operation)
1	Communication error of IF microcomputer	Immediately Shutdown
2	Communication error of ARIA	Immediately Shutdown

< No. 8 Subcategory Information on "Failure in IIC communication of Main microcomputer" >

Value	Shutdown Factor	Remarks (Operation)
1	Tuner 1	Immediately Shutdown
2	MSP/MAP	Immediately Shutdown
3	AV-Switch	Immediately Shutdown
4	RGB-Switch	Immediately Shutdown
5	Main VDEC	Immediately Shutdown
6	VDEC-SDRAM	Immediately Shutdown
7	AD/PLL	Immediately Shutdown
8	HDMI	Immediately Shutdown
9	DisplayPortTx	Immediately Shutdown
В	US-MAP	Immediately Shutdown
С	GCR	Immediately Shutdown
D	COFDM	Immediately Shutdown

< No. 10 Subcategory Information on "Abnormally in FAN" >

Value	Shutdown Factor	Remarks (Operation)
1	FAN 1	Immediately Shutdown
2	FAN 2	Immediately Shutdown

< No. 12 Subcategory Information on "Failure in Digital Tuner" >

Value	Shutdown Factor	Remarks (Operation)
1	Starting error of the digital tuner	Communication stop
2	Communication error with the digital tuner	
3	DTB device error	
4	Abnormmally in BCM7038	
5	Fugue	
6	Audio Chip	
7	Tuner 1/Tuner 1 or 2	
8	Card I/F IC	
9	VBI Slicer	
В	Flash	
С	EEPROM	
D	EEPROM	
F	DTV Antenna	
G	Home Gallery	
I	Application	
J	DEMOD(US)/COFDM(EU)	
K	Tuner 2	
L	S2DEMOD	
М	LNB	
0	DTB ERROR	
Р	Abnormally in DTB (S2) antenna	

< No. 13 Subcategory Information on "Failure in Power supply at MTB section" >

Value	Value Shutdown Factor Remarks (Operation	
1	RST 2	Immediately Shutdown
2	RST 4	Immediately Shutdown

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[5] FAY/FAN (ADJUSTMENT COMMANDS PERMISSION/PROHIBITION)

The commands FAY/FAN are for prohibiting/permitting panel/MTB-adjustment commands.

	Operation		
Command Format	Effective Operation Modes	Control	Remarks
[FAY]	Normal operation mode while the power is on		For details, refer to the section "6.1 [3] FUNCTIONS WHEN ENTERING THE SERVICE FACTORY MODE".
[FAN]	During FAY	Adjustment command is invalid.	

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6. SERVICE FACTORY MODE 6.1 DETAILS OF THE SERVICE FACTORY MENU

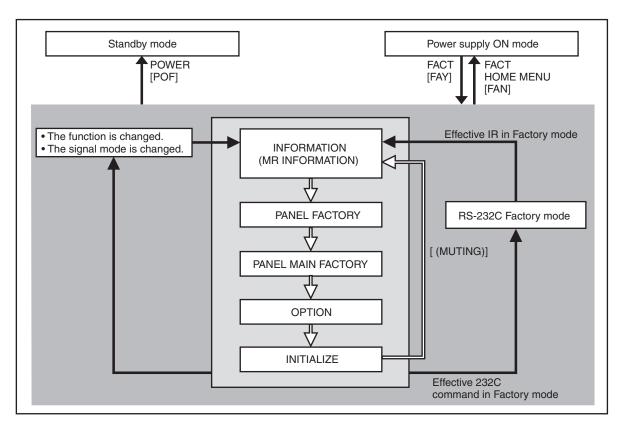
Operations during Service Factory mode are described here.

Before entering Factory mode of the PDP, make sure that the "HD AV Converter" setting on the PDP menu is set to "Disable." If it is set to "Enable," change it to "Disable" then enter Factory mode.

To confirm the "HD AV Converter" setting on the PDP menu, proceed as follows: Select HOME MENU, Option, then HD AV Converter in HDMI Control Setting.

Note: If "HD AV Converter" is set to "Enable," the video/audio signals will not be displayed/output even if external equipment is connected via input connectors other than INPUT 4 of the PDP.

[1] SERVICE FACTORY MODE TRANSITION CHART



[2] HOW TO ENTER/EXIT SERVICE FACTORY MODE

■ How to enter Service Factory Mode

By using a PDP service remote control)

- PDP service remote control : Press [FACTORY] key. By issuing RS-232C commands)
- During normal Standby mode : Issue [PON] then [FAY].
- During normal operation mode : Issue [FAY].

■ How to exit Service Factory Mode

By using a PDP service remote control)

- PDP service remote control : press [FACTORY] key.
- Supplied remote control unit: press [HOME MENU] key.

By issuing RS-232C commands)

Issue [FAN].

- How to enter Service Factory Mode by Using the supplied Remote Control Unit
- From this model, can not enter the Service Factory Mode by operating the supplied remote control unit keys.

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[3] FUNCTIONS WHEN ENTERING THE SERVICE FACTORY MODE

■ Fuctions whose setting are set to OFF

The settings for the following functions are set to OFF when Service Factory mode is entered (including when the "FAY" command is received):

Function	Remarks
2-Screen Operation	Input function set on the main side is selected.
FREEZE	
Auto size, Side Mask	It is not performed during Factory mode.
ORBITER, Mask control	Central value operation (ORBITER)
Sleep Timer	Cancel the operation.
Room light sensor	Turn off the detecting operation (Setting data will be retained.)
Blue LED dimmer	Turn off the operation (Setting data will be retained.)
Setting of Parental Control	When this is turned off, the block of the screen is released.
Power Control	Turn off the operation (However, the setting maintains it.)
Image Position	Central value operation

Note: Enter the factory after cancelling ACI because the ACI operation setting OFF and not done.

User data

User data will be treated as follows:

- User data on picture-quality and audio-quality adjustments are not reflected, and factory-preset data are output (user data will be retained in memory). When the unit enters Service Factory mode, the current audio-quality adjustment data will be still be retained in memory.
- User-setting data will be applied to the various settings (items on the menus), signal formats, and the items that are associated with path change (HDMI settings, etc.).
- Data on screen (i.e., screen position; meaning clock dividers, and not including data on screen size).
 Are reset to the default values (data stored in memory will be retained).
 Screen size will be retained.

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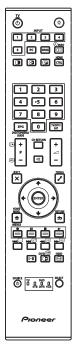
[4] REMOTE CONTROL CODE IN SERVICE FACTORY MODE

Remote Control Keys	Basic Functions	Remarks
MUTING	Switching the main items.	Shifting to the next main item (top).
↓ (DOWN)	Switching the subtitled items.	Shifting downward to the next subtitiled item.
↑ (UP)	Switching the subtitled items.	Shifting upward to the next upper layer.
← (LEFT)	Decreasing the adjustment value.	Decreasing the adjustment value.
→ (RIGHT)	Increasing the adjustment value.	Increasing the adjustment value.
ENTER/SET	Switching the layers.	Shifting downward or upward to the next lower or upper layer.
INPUT	Selecting INPUT.	Shifting the INPUT to the next function.
INPUTxx	Selecting INPUT.	Switching the INPUT to xx. (xx=1 to 5)
CH+/P+	Increasing the channel number.	
CH-/P-	Decreasing the channel number.	
Numeric Keys	Function: TV	Function: TV (previously selected channel number is selected)
POWER	Power OFF.	Turning the power off.
FACTORY	Factory OFF (Factory mode)	In Factory mode, turning Factory mode off.
FACTORY	Factory ON (Non-Factory mode).	In Non-Factory mode, turn Fuctory mode on.
HOME MENU	Menu ON.	In Factory mode, turn Factory mode off.
VOLUME+	Volume UP.	Increasing 10 the adjustment value. (PANEL FACTORY)
VOLUME- Volume DOWN. Decreasing 10 the adjustment value. (PAN		Decreasing 10 the adjustment value. (PANEL FACTORY)
DRIVE OFF (Note1)	Drive Mode OFF.	Turning Drive mode off.
INTEGRATOR	INTEGRATOR MENU ON.	Enter INTEGRATOR MODE.

(Note 1) When ten seconds have passed since the [DRIVE OFF] key was pressed at the standby, it becomes invalid. Please press [POWER] key from the [DRIVE OFF] key pressing within ten seconds when you do power supply ON while driven OFF.



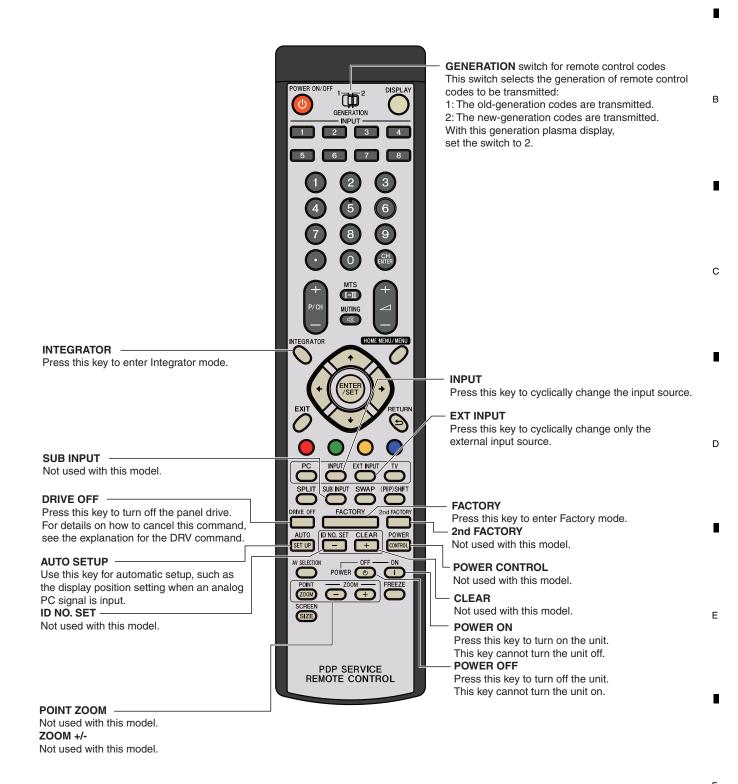
PDP service remote control



Supplied remote control

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- The keys labeled with the same names on the service remote control unit have the same functions as those of the supplied remote control unit. (See "2.3 PANEL FACILITIES.")
- For the keys not provided on the supplied remote control unit, see the explanations below:



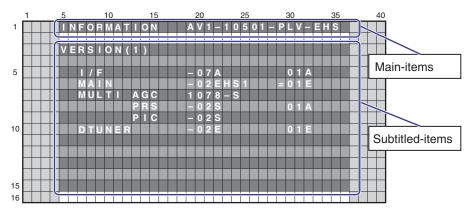
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[6] FACTORY HIERARCHICAL TABLE

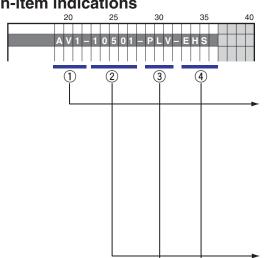
Large Item				
Middle Item	[a	Variable / Adjustment Range	Remarks	
	Small Item			
6.2 [1] INFORMATION [1-1] VERSION (1)				
[1-1] VERSION (1)				
[1-2] VERSION (2)				
[1-4] MAIN NG	CLEAR <=>	NO <=> YES		
[1-5] TEMPERATURE	OLEAN <=>	NO <=> TES		
[1-6] HOUR METER	CLEAR <=>	NO <=> YES		
[1-7] HDMI SIGNAL INFO 1	OLEAN (->	140 <=> 120		
[1-8] HDMI SIGNAL INFO 2				
[1-9] VDEC SIGNAL INFO 1				
[1-10] VDEC SIGNAL INFO 2				
6.2 [2] PANEL FACTORY (+) (*2)				
[2-1] PANEL INFORMATION				
[2-2] PANEL WORKS				
· ·				
[2-3] POWER DOWN				
[2-4] SHUT DOWN				
[2-5] PANEL-1 ADJ (+)				
[2-6] PANEL-2 ADJ (+)				
[2-7] PANEL FUNCTION (+)				
[2-8] ETC (+)				
[2-9] RASTER MASK SETUP (+)				
[2-10] PATTERN MASK SETUP (+)				
[2-11] COMBI MASK SETUP (+)				
6.2 [3] PANEL MAIN FACTORY (+) (*2)				
[3-1] PM NG INFO				
[3-2] PM STATE INFO				
[3-3] DP_RX INFO				
[3-4] PM_SETUP (+)				
6.2 [4] OPTION				
[4-1] CH PRESET <=>		DISABLE <=> ENABLE	Exclusively used for production line	
[4-2] Digital AFT <=>		DISABLE <=> ENABLE	Exclusively used for production line	
[4-3] SYNC DET (+)			for the technical analysis	
[4-4] CTI (+)			for the technical analysis	
6.2 [5] INITIALIZE				
[5-1] SIDE MASK LEVEL (+)	SIDE MASK LEVEL <=>			
[5-2] FINAL SETUP	DATA RESET <=>	NO <=> YES		
[5-3] DTB SERVICE MODE	MODE SHIFT <=>	NO <=> YES	for the technical analysis (*1)	
[5-4] Wide XGA AUTO <=>		DISABLE <=> ENABLE	for the technical analysis	
[5-5] AUTO ADJUST. <=>	ALITO AD ILIOT		ioi the technical allalysis	
[5-5] AUTU ADJUST. <=>	AUTO ADJUST. <=>	NO <=> YES		

^{(*1):} Exit the Service Factory Menu and enter the Digital Tuner Service menu.
(*2): For details on the setting items, refer to the Service manual of the PLASMA DISPLAY (KRP-600P, KRP-500P).

[7] INDICATIONS IN SERVICE FACTORY MODE



■ Main-item indications



① Input function

Input Functions	OSD
AV 1 to 5	AV 1 to 5
Terrestrial Wave (Analog)	AIR
Terrestrial Wave (Digital)	ARD
Satellite didital broadcasting	SAT
Cable (Digital)	CBD
Home Media Gallery	HMG
PC	PC

2 SIG mode and Screen size

Note: See SIG-Mode Tables. (See next page.)

③ Color system and Signal type

	09	SD	
Color System and Signal Type	At Composite Input	At S-connector Input	
NTSC	NTV	NTS	
PAL	PLV	PLS	
PAL M	PMV	PMS	
PAL N	PNV	PNS	
PAL 60	P6V	P6S	
SECAM	SCV	SCS	
4.43 NTSC	4NV	4NS	
BLACK/WHITE	BWV	BWS	
Y/CB/CR	CBR		
Y/PB/PR	PBR		
RGB	RGB		
Digital Video signal	DIG		

4 Option (Destination, Panel Generation, etc.)

Options	OSD
KRP-500P/WYSIXK5	FHS
KRP-600P/WYSIXK5	LIIO

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2 SIG Mode and Screen size (by User is displayed)

1st and 2nd characters: Resolution of the input signal 3rd and 4th characters: Refresh rate of the input signal 5th character: Selection of the screen size

■ Input signal mode table for video signals (resolutions and V frequencies)

1st to 4th	Character	Signal Type	Fv (Hz)	Fh (kHz)
10	50	SDTV*625i	50.000	15.750
10	60	SDTV*525i	60.000	15.750
20	50	SDTV*625p	50.000	31.500
20	60	SDTV*525p	60.000	31.500
30	50	HDTV*1125i	50.000	33.750
30	60	HDTV*1125i	60.000	33.750
40	50	HDTV*750p	50.000	45.000
40	60	HDTV*750p	60.000	45.000
	24	HDTV*1125p	24.000	27.000
50	50	HDTV*1125p	50.000	56.250
	60	HDTV*1125p	60.000	67.500

Fv: Vertical Frequency, Fh: Horizontal Frequency

■ Input signal mode table for PC signals (resolutions and V frequencies)

1st to 4th Character		Signal Type	Fv (Hz)	Fh (kHz)
C1	70	720 x 400	70.087	31.469
C2	60	640 x 480	59.940	31.469
C4	60	800 x 600	60.317	37.879
C6	60	1280 x 720	60.000	44.800
C7	60	1024 x 768	60.004	48.363
C9	60	1360 x 768	60.015	47.712
D6	60	1280 x 1024	60.000	64.000

Fv: Vertical Frequency, Fh: Horizontal Frequency

■ Current selection of the screen size

5th Character	GUI Notation	VIDEO	PC	Remarks
0	DOT BY DOT	•		
1	4:3	•	•	
2	FULL	•	•	
3	ZOOM	•		
4	CINEMA	•		
5	WIDE	•		
6	FULL 14:9	•		
7	CINEMA 14:9	•		
9	WIDE1	•	=	
Α	WIDE2	•	=	

●: supported, -: unsupported

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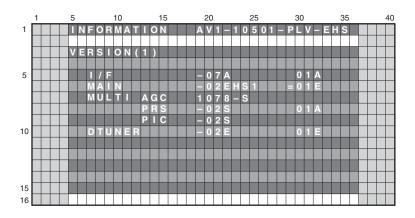
6.2 DETAILS OF THE FACTORY MENU

[1] INFORMATION

■ Operation items

No.	Function	Content	RS-232C Command
[1-1]	VERSION (1)	The Flash memory versions for each device are displayed.	QS1
[1-2]	VERSION (2)	The Flash memory versions for each device are displayed.	QSE
[1-3]	VERSION (3)	The Flash memory versions for each device are displayed.	QSB
[1-4]	MAIN NG	The Shutdown NG information and Event Times in the MTB section are displayed.	QNG
[1-5]	TEMPERATURE	The present temperature and the FAN rotating status are displayed.	-
[1-6]	HOUR METER	The accumulation power ON count of the panel is displayed.	-
[1-7]	HDMI SIGNAL INFO 1	The status registers of HDMI receiver are displayed with hexadecimal.	
[1-8]	HDMI SIGNAL INFO 2	The status registers of Fibral receiver are displayed with nexadecimal.	_
[1-9]	VDEC SIGNAL INFO 1	Display the signal information input to VDEC	
[1-10]	VDEC SIGNAL INFO 2	Display the signal information input to VDEC.	_

[1-1] VERSION (1)



Display Item	Meaning	Display Example (Program)	Display Example (Boot)
I/F	I/F microcomputer	-07A	01A
MAIN	Main microcomputer	-02EHS1	=01E
MULTI AGC	AGC data of Multi processor	1078-S	
MULTI PRS	Program of Multi processor	-02S	01A
MULTI PIC	Picture quality data of Multi processor	-02S	
DTUNER	Software program of the Digital tuner	-02E	01E

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[1-2] VERSION (2)

	1		5					10					15				20					25					30					35			4	0
1			П	N	F	0	R	M	Α	П	П	0	Ν			Α	٧	1		1	0	5	0	1		P	Ш	٧		Е	н	S		T	П]
							Г							Г	Г						Г													П	Т	1
			٧	Ε	R	S		0	Ν	(2)																							П	
5					D	П	В		Η	Α	R	D				0	3	4	2															П	Т	1
																																			П	
					Р	Α	S	S	W	0	R	D				1	2	3	4															T	Т	1
																																		П	Т	1
					D	P		П	Х							1	2	3	4	5	6	7	8	9	Α	В	С	D	囯	F	G			П	Т	1
10					D	P		П	Х		Н	Α	R	D		2	С	1	3															T	Т	1
									П																									П	Т	1
																																		T	Т	1
																																			Т	1
								П																										П	Т	1
15															Г						Г													П	Т	1
16																																				

Display Item	Meaning	Display Example
DTB HARD	DTB Hardware Version	0342
PASSWORD	User setting password	1234
DP TX	DP TX Firmware Version	123456789ABCDEFG
DP TX HARD	DP TX Hardware Version	2C13

[1-3] VERSION (3)

	1		5					10					15				20					25					30					35	j		2	10
1			П	N	F	0	R	М	Α	T	П	0	N			Α	٧	1		1	0	5	0	1		P	L	٧		Ε	Н	S				
	L		L			L	L																				L						L			
	L		۷	Ε	R	S	П	0	N	(3)																								
	L						L																												Ш	
5	L				P	_	М	Α	Ш	N							0	2	Α	S							0	1	Α							
	L				M	0	D	U		目							0	6	Α								0	1	Α							
					S	E	Q		P	R	S						0	3	Υ								0	1	Α							
					D	P		R	Х							1	2	3	4	5	6	7	8	9	Α	В	С	D	囯	F	G					
10	Г				D	P		R	Х		Н	Α	R	D		2	С	1	2													П			П	
	Г																																		П	
	Г				P	Α	Ν	固	Ш		П	N	В	0		Х	Х	Х	Х	Х	Х	Х	Х								П	П				1
	Г																																			
	Г																																			1
15																																Г				
16	Г																															Г	Г			1

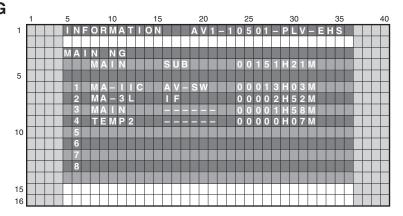
Display Item	Meaning	Display Example (Program)	Display Example (Boot)
P_MAIN	Panel Main microcomputer	-02AS	01A
MODULE	Module microcomputer	-06A	01A
SEQ PRS	Program of the sequence processor	-03Y	01A
Display Item	Meaning	Display	Example
DP RX	DP RX Firmware Version	123456789	ABCDEFG
DP RX HARD	DP RX Hardware Version	20	12
Display Item	Meaning		
PANEL INFO	ne type of the panel.		

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[1-4] MAIN NG



Shutdown NG information

Error Display: MAIN	Error Display: SUB	Cause of Shutdown
MR-PWR		Abnormally in VCC power
AUDIO	MSPMAP	Short-circuit of the speaker terminal or failure signal of audio amplifier (MSP)
MA-3L		3-wire Serial Communication error of Main microcomputer.
	IF	Communication error of IF microcomputer
	MULTI	Main communication error of Multi Processor
MA-IIC		IIC Communication error of Main microcomputer
	FE1	Tuner 1
	MSPMAP	MSP/MAP
	AV-SW	AV Switch
	RGB-SW	RGB Switch
	VDEC	Main VDEC
	SDRAM	VDEC - SDRAM
	ADC	AD/PLL
	HDMI	HDMI
	DP-TX	DisplayPort Tx
MAIN		Communication error of Main microcomputer
FAN		FAN abnormal
	FAN1	FAN1 abnormal stop
	FAN2	FAN2 abnormal stop
TEMP2		Abnormally high temperature
DTUNER	1	Failure in Digital Tuner
	PS/RST	DTB Starting error
	RETRY	Communication error with DTB
	DEVICE	DTB device error
	DE-FE	DTB device error (Tuner 1)
	D-ANT	Abnormally in DTB antenna
	DTVAPP	DTB device error (Application)
	DEMOD	DTB device error (DEMOD)
	DE-FES	DTB device error (Tuner S2)
	DEMONS	DTB device error (S2DEMOD)
	DE-LNB	DTB device error (LNB)
	DTVERR	DTB error
	S-ANT	Abnormally in DTB (S2) antenna
RST-MA		Abnormally in MTB power
	M-DCDC	Abnormally in ASIC power (DC-DC)
	RELAY	Power decrease of RELAY power

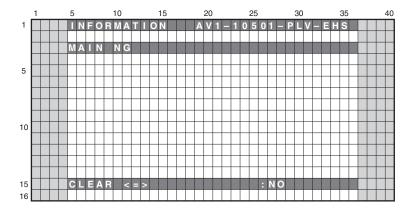
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• Clear the MAIN NG history

To shift to the MAIN NG history clear screen, while the MAIN NG screen is displayed, press the ENTER/SET key.



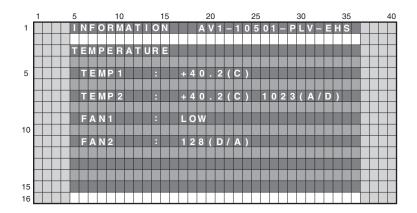
Operation:

- Even if [←] key or [→] key is pressed, {CLEAR <=> :YES} \Leftrightarrow {CLEAR <=> :NO} is repeated.
- Selecting <NO> then pressing the ENTER/SET key will return the screen to the next higher layer, without doing anything.
- Selecting <YES> then holding the ENTER/SET key pressed for 5 seconds will clear the NG log data that are managed in MTB then return the screen to the next higher layer.

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[1-5] TEMPERATURE

A present temperature and the FAN rotation are displayed. If either [←] key or [→] key is pressed, the display data is refreshed.



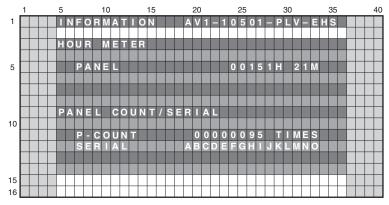
Display Item	Meaning
TEMP1	The temperature of the sensor on the panel side is displayed by the Centigrade (C).
TEMP2	The temperature conversion display is done with 10 bit the A/D input value of IF microcomputer. It is displayed by both the Centigrade (C) and 8 bit A/D value. Note: When temperature (C) of the sensor becomes more than a specified temperature, the shutdown start of processing.
FAN1	Although STOP, LOW, or HIGH may be displayed, they are meaningless. Ignore those displays.
FAN2	The value of the rotation state of FAN is displayed. During a rotation of FAN, 8bit D/A value output from IF microcomputer is displayed. It is displayed with OFF during a stop.

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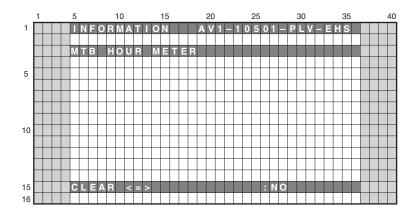
[1-6] HOUR METER



Display Item	Meaning	Display Example
PANEL	HOUR METER of the panel	00151H 21M
P-COUNT	Accumulation power ON count of the panel	00000095 TIMES
SERIAL	Serial number of the Display (panel)	ABCDEFGHIJKLMNO

• MTB HOUR METER

In HOUR METER screen on Factory Menu, press the [ENTER/SET] key, and then it moves to the screen to clear MTB HOUR METER. (MTB HOUR METER is cleared only.)



Operation:

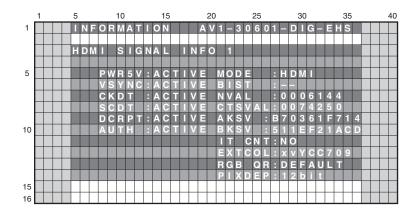
- Even if [←] key or [→] key is pressed, {CLEAR <=> :YES} ⇔ {CLEAR <=> :NO} is repeated.
- Selecting <NO> then pressing the ENTER/SET key will return the screen to the next higher layer, without doing anything.
- Selecting <YES> then holding the ENTER/SET key pressed for 5 seconds will clear the HOUR METER (HOUR METER while the MAIN NG screen is displaed) data that are managed in MTB then return the screen to the next higher layer.

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[1-7] HDMI SIGNAL INFO 1



Displays the input signal information of HDMI terminal

Display Item	Meaning
PWR5V	+5 V power detection (18 pin of HDMI terminal)
VSYNC	VSYNC detection
CKDT	Clock detection
SCDT	SYNC detection
DCRPT	HDCP decryption status
AUTH	HDCP authentication status
MODE	HDMI mode status
BIST	HDCP Key status (Always display it with "".)
NVAL	N value
CTSVAL	CTS value
AKSV	Shadow AKSV value
BKSV	Shadow BKSV value
IT CNT	IT content (AVI info)
EXTCOL	Extension colorimetry (AVI info)
RGB QR	RGB range (AVI info)
PIXDEP	Number of pixel/bit

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[1-8] HDMI SIGNAL INFO 2

	1		5					10					15					20					25					30					35			4	0
1	Г		П	Ν	F	0	R	М	Α	Т		0	Ν				Α	٧	1		3	0	6	0	1		D		G		Е	Н	S			Т	٦
			Н	D	М			S		G	Ν	Α	L			Ν	E	0		2																	
	Г																																		П	П	
5						Ε		R	囯	S	:	2	2	0	0				С	0	L		S	Р	:	4	2	2									
						٧		R	固	S	1	0	5	6	3				С	0	Ш	M	В	Т	:	7	0	9									
	Г					Ε		D	Ξ		8	1	9	2	0	П			Α	s	P	Ξ	С	Т	8	1	6		9						П	П	
	Г					٧		D	囯		8	0	5	4	0				Α	С	П	П	٧	Е	8										П	П	1
						П	Ν	П	R	L	8	П	N	П					S	а	m	е		а	s		р		С								
10	Г					٧		P	0	L	В	P	0	S					٧		В	M	П		В										П	П	1
	Г					Ε		P	0	L	В	P	0	S					1	9	2	0	Х	1	0	8	0	1	@	6	0				П	П	
						Α	U	D		0	1	4	8	k					P	П	Х		R	Р	:	0	0										
	Г											P	С	М					S	0	U	R	С	Е		Р		0	Ν	Ε	E	R				П	
	Г											2	0	b	П	t			D	٧	R	П	D	Т	9	0									П	П	
15																																					
16																																					

Displays input signal status of HDMI terminal

Display Item	Meaning
H RES	Number of horizontal pixels
V RES	Number of vertical lines
H DE	Number of effectively horizontal pixels
V DE	Number of effectively vertical lines
INTRL	Interlace (=INT) or progressive (=PRG)
V POL	VSYNC polarity
H POL	HSYNC polarity
AUDIO (first line)	Sampling frequency. (ex. DVD: 48kHz, CD: 44.1kHz) *1
AUDIO (second line)	Audio format PCM (PCM) or No PCM (no PCM)
AUDIO (third line)	Quantization bit
COL SP	Color space (AVI Info) 422 or 444 or RGB *2
COLMET	Colorimetry (AVI Info)
ASPECT	Aspect (AVI Info)
ACTIVE	Active format (AVI Info)
V FMT	Video format (AVI Info)
PIX RP	Pixel count
SOURCE (first line)	Vendor name of the emission device
SOURCE (second line)	Model name of the emission device

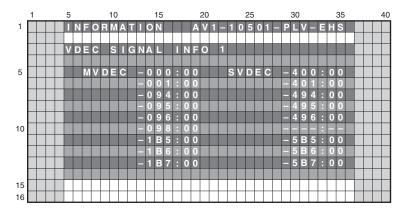
Display of HDMI FACTORY and correspondence of resolution Please confirm the following items when the picture doesn't come out.

Input			FACTORY	/ Display	
Signal	H RES	V RES	H DE	V DE	V FMT
480i (525i)@60	858	262 or 263	720	240	720x480i@60
480p (525p)@60	858	525	720	480	720x480p@60
1080i (1125i)@60	2200	562 or 563	1920	540	1920x1080i@60
720p (750p)@60	1650	750	1280	720	1280x720p@60
1080p (1125p)@60	2200	1125	1920	1080	1920x1080p@60
1080p (1125p)@24	2750	1125	1920	1080	1920x1080p@24
576i (625i)@50	864	312 or 313	720	288	720x576i@50
576p (625p)@50	864	625	720	576	720x576p@50
1080i (1125i)@50	2640	562 or 563	1920	540	1920x1080i@50
720p (750p)@50	1980	750	1280	720	1280x720p@50
1080p (1125p)@50	2640	1125	1920	1080	1920x1080p@50

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^{*1:} Confirm if this item is displayed when the audio is not outputted.
*2: If may not match to the state of emission devices when the color is abnormal.

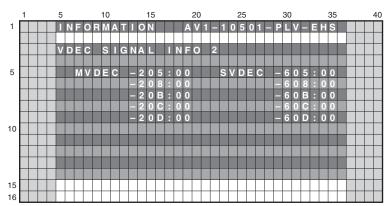
[1-9] VDEC SIGNAL INFO 1



Displays signal status that is input to VDEC.

Device	Sub Address (Main screen)	Sub Address (Sub screen)	Meaning
	000h	400h	Line system distinction result
	001h	401h	VTR distinction result
	094h	494h	Slot number
VDEC	095h	495h	Color system distinction result
	096h	496h	ACC coefficient
	098h		3D YC flag
	1B5h	5B5h	MV detection 1
	MV detection 2		
	MV detection 3		

[1-10] VDEC SIGNAL INFO 2



Displays signal status that is input to VDEC.

Device	Sub Address (Main screen)	Sub Address (Sub screen)	Meaning
	205h	605h	CC detection 1
	208h	608h	CC detection 2
VDEC	20Bh	60Bh	CC-CRI detection
	20Ch	60Ch	XDS content advisory 0
	20Dh	60Dh	XDS content advisory 1

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[2] PANEL FACTORY (+)

■ Operation Items

No.	Function	Content	RS-232C
[2-1]	PANEL INFORMATION		
[2-2]	PANEL WORKS		
[2-3]	POWER DOWN		
[2-4]	SHUT DOWN		
[2-5]	PANEL-1 ADJ (+)		
[2-6]	PANEL-2 ADJ (+)		
[2-7]	PANEL FUNCTION (+)		
[2-8]	ETC. (+)		
[2-9]	RASTER MASK SETUP (+)		
[2-10]	PATTERN MASK SETUP (+)		
[2-11]	COMBI MASK SETUP (+)		

Note: For details on the setting items, refer to the Service manual of the PLASMA DISPLAY (KRP-600P, KRP-500P).

[3] PANEL MAIN FACTORY (+)

■ Operation Items

No.	Function	Content	RS-232C
[3-1]	PM NG INFO		
[3-2]	PM STATE INFO		
[3-3]	DP_RX INFO		
[3-4]	PM_SETUP (+)		

Note: For details on the setting items, refer to the Service manual of the PLASMA DISPLAY (KRP-600P, KRP-500P).

[4] OPTION

Operation item

No.	Function	Content	RS-232C
[4-1]	CH PRESET <=>	Set the channel map for production line	SCP
[4-2]	Digital AFT <=>	Set AFT of the Satellite digital broadcasting	AFT
[4-3]	SYNC DET (+)	Set the synchronized signal detection of VDEC	
[4-4]	CTI (+)	Set the synchronized signal detection of VDEC	

[4-1] CH PRESET <=>

Exclusively used for production line.

[4-2] Digital AFT <=>

Exclusively used for production line.

[4-3] SYNC DET (+)

Exclusively used for technical analysis (details omitted).

[4-4] CTI (+)

Exclusively used for technical analysis (details omitted).

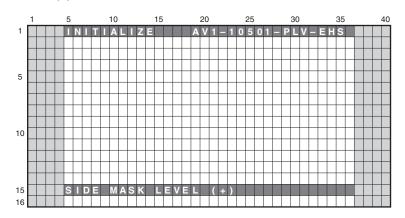
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[5] INITIALIZE

Operation item

No.	Function	Content	RS-232C
[5-1]	SIDE MASK LEVEL (+)	Configure the color of the side mask.	SML
[5-2]	FINAL SETUP	Initialize flash memorys on virgin product status	FST
[5-3]	DTB SERVICE MODE	Enter the Digital Tuner Service Menu	
[5-4]	Wide XGA AUTO <=>	Exclusively used for technical analsyis.	
[5-5]	AUTO ADJUST. <=>	Perform the auto-adjustment setting process	

[5-1] SIDE MASK LEVEL (+)

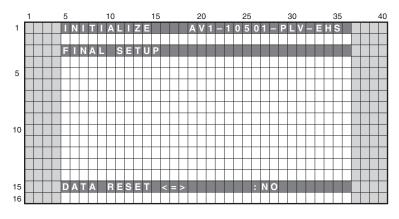


To configure sidemask level (To adjust the values, input signal is required).

Display Item	Content	RS-232C
SIDE MASK LEVEL <=>	Adjust Side Mask level (Adjustable range: 000 to 255, Initial value: 115)	SML

Note: In this mode (SIDE MASK LEVEL), adjustment value cannot changed with the VOLUME +/- keys.

[5-2] FINAL SETUP

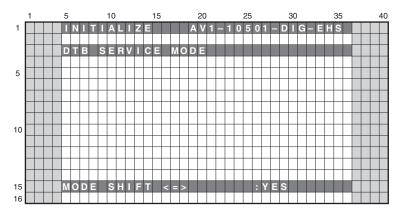


- To reset each memory values to factory default values. Factory command is "FST".
- When the configuration is set to <NO> and the [ENTER/SET] key is pressed, no action is taken and the menu returns to previous screen.
- When the configuration is set to <YES> and the [ENTER/SET] key is pressed for 5 seconds, the reset action executes.

Be sure to disconnect and connect the AC cable after FINAL SETUP. When replacing the MAIN BLOCK Assy, the FINAL SETUP is required.

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[5-3] DTB SERVICE MODE

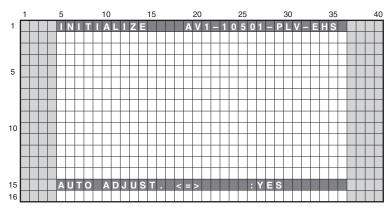


If the [ENTER/SET] key is kept on pressing for 5 second when the status of this menu is <YES>, shift to the DTB SERVICE mode screen. (Release from the SERVICE FACTORY mode.)

[5-4] WIDE XGA AUTO <=>

Exclusively used for technical analysis (details omitted).

[5-5] AUTO ADJUST. <=>



- When the configuration is set to <NO> and the [ENTER/SET] key is pressed, no action is taken and the menu returns to previous screen.
- When the configuration is set to <YES> and the [ENTER/SET] key is pressed for 5 seconds, the auto-adjustment action executes.
- Be sure to power off with the remote control unit or disconnect and connect the AC cable after the auto-adjustment is completed.
- When some ICs on the MAIN BLOCK Assy are replaced individually, auto-adjustment is required. For details on IC numbers, see the list "■ Parts whose replacement is difficult" in "8.1 ADJUSTMENT REQUIRED WHEN THE UNIT IS REPAIRED OR REPLACED."
- When this unit is used with the HD AV Converter, the interlocking setting with the HD AV Converter is released. Reset it after the auto adjustment is completed.

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6.3 DIGITAL TUNER SERVICE MENU

The Digital Tuner Service Menu is provided for collecting data for technological examination when the Digital Tuner has any problem in the market. This menu is introduced here just for reference.

[1] REMOTE CONTROL CODE IN DIGITAL TUNER SERVICE MENU

The following remote control cord is valid in the Digital Tuner Service Menu.

Remote Control Keys	Basic Functions	Remarks
↓ (DOWN)	Selecting the menu items and	Shifting downward to the next item. Moving to the next lower page.
↑ (UP)	shifting the pages.	Shifting upward to the next item. Moving to the next upper page.
← (LEFT)	Selecting the setting value.	Modifying the setting of selected items.
→ (RIGHT)	Delecting the setting value.	iniountying the setting of selected items.
ENTER/SET	Shifting the menu layers	Shifting to the next menu screen.
RETURN	Similing the menu layers	Shifting to the previous menu screen.
Numeric Keys	Numeric input	Input the numerical value.
POWER OFF	Power OFF	Turning the newer off
STANDBY/ON		Turning the power off.
FACTORY	Factory ON/OFF	Release the Menu, then enter the Service Factory menu.
EXIT	MENU exit	After you suit the many, the change that was calcuted on the many will be
MUTING	Muting	After you exit the menu, the channel that was selected on the menu will be displayed.
HOME MENU	HOME MENU ON/OFF	

[2] HIERARCHICAL TABLE OF DIGITAL TUNER SERVICE MENU

Iten	em					
	Large Item		Remarks			
	Middle Item					
6.3 [3] Digital Tuner S	ervice Menu				
	6.3 [4] HMG Ser	vice Menu				
			Exclusively used for technical analysis: HomeMediaGallery-related information indication			
	6.3 [5] Digital					
	Bandwidth		Exclusively used for technical analysis			
	Frequency		Exclusively used for technical analysis			
	Program Nu	ımber	Exclusively used for technical analysis			
	Audio PID		Exclusively used for technical analysis			
	DTV Tuning Status		Exclusively used for technical analysis: Terrestrial digital broadcasting-related information indication			
	6.3 [6] Satellite					
	Modulation		Exclusively used for technical analysis			
	Frequency		Exclusively used for technical analysis			
	Symbol Rat	e	Exclusively used for technical analysis			
	LNB POWE	:R	Exclusively used for technical analysis			
	LNB BAND		Exclusively used for technical analysis			
	Program Nu	ımber	Exclusively used for technical analysis			
	Audio PID		Exclusively used for technical analysis			
	SAT Tuning Status		Exclusively used for technical analysis: Satellite digital broadcasting-related information indication			
	6.3 [7] Software	Version				
		·	Exclusively used for technical analysis: The software revision information that consists of it in DTB software			

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[3] DIGITAL TUNER SERVICE MENU SCREEN



Fig.1 Digital Tuner Service Menu screen

Display a large item list of Digital Tuner Service Menu. Select each item, and shift to each setting / information display screen.

- ① Home Media Gallary-related information indication
- ② Terrestrial digital-related setting / information indication
- 3 Satellite digital-related setting / information indication
- 4 Digital Tuner-related detailed software version indication

[4] HOME MEDIA GALLERY SCREEN

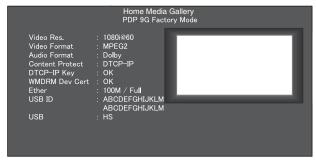
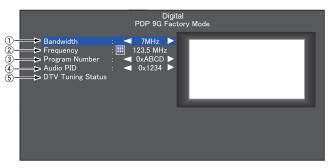


Fig.2 Home Media Gallery screen

Display the Home Media Gallary-related information.

[5] DIGITAL SCREEN

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Display the Digital broadcasting-related setting / information indication.(except the satellite digital)

- ① The Bandwidth for receiving a digital broadcast can be selected. (7 MHz/8 MHz)
- 2 The frequency can be set (up to 1 digit after the decimal point).
- 3 Program Number in the same stream: Service ID can be selected.
- 4 Audio PID in the same stream: Audio PID can be selected.
- **5** The DTV Tuning Status is displayed.

Fig.3 Digital screen

The data displayed on the DTV Tuning Status screen are as shown below:

The instructions for servicing using this screen is shown in "How to confirm the DTV Tuning Status on the Digital Tuner Service Menu" of section 5.2 [4]. Therefore, this screen is introduced here just for reference.

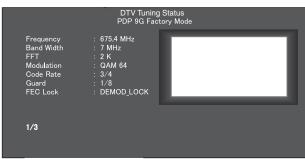


Fig.4 DTV Tuning Status (1/3) screen

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Fig.5 DTV Tuning Status screen (2/3) screen

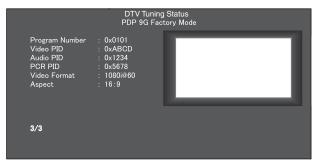


Fig.6 DTV Tuning Status screen (3/3) screen

[6] SATELLITE SCREEN

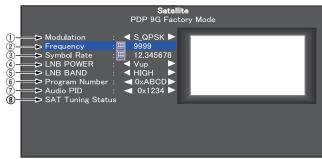


Fig.7 Satellite screen

Display the Satellite Digital broadcasting-related setting $\mbox{\prime}$ information indication.

- The modulation method can be selected. (S_QPSK/S2_QPSK/S2_8PSK)
- ② The frequency can be set (0001 to 9999).
- ③ The symbol Rate can be set (1.000000 to 99.999999)
- 4 The LNB power voltage can be selected. (OFF/V/H/Vup/Hup)
- 5 The LNB Bandwidth can be selected. (Low/High)
- 6 Program Number in the same stream: Service ID can be selected.
- ② Audio PID in the same stream: Audio PID can be selected.
- ® The Tuning Status of Satellite Digital is displayed.

The data displayed on the SAT Tuning Status screen are as shown below: The instructions for servicing using this screen will be provided as service information.

Therefore, this screen is introduced here just for reference.

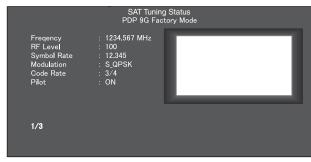


Fig.8 SAT Tunig Status (1/3) screen

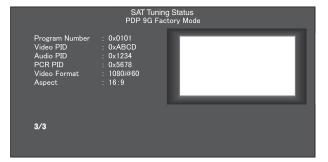


Fig.10 SAT Tunig Status (3/3) screen

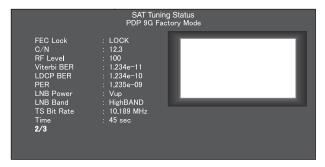


Fig.9 SAT Tunig Status (2/3) screen

[7] SOFTWARE VERSION SCREEN

The details are not described here, as this is provided for technical examination.

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7. DISASSEMBLY

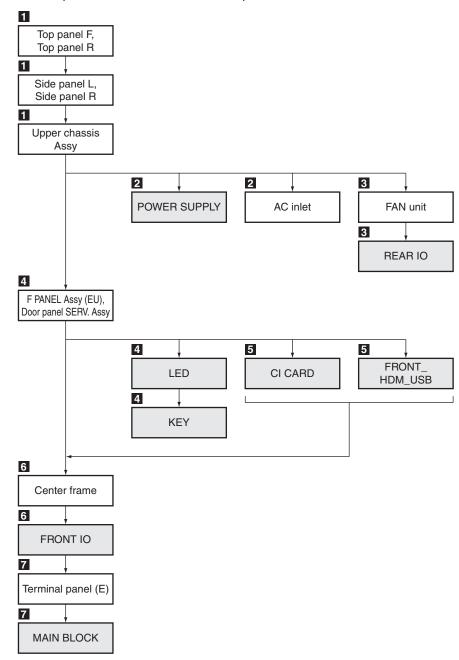
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7.1 FLOWCHART OF REMOVAL ORDER

Note: Even if the unit shown in the photos and illustrations in this manual may differ from your product, the procedures described here are common.

Flowchart of removal order for the main parts and boards

It is efficient to proceed with removal of the main parts and boards in the order shown in the chart below:



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1 Exterior Section

The high-gloss resin parts of the exterior of this product are easily scratched. During disassembly and reassembly of this product, be careful not to scratch the exterior.

Attach the protect film (GGP1121) to the inside surface of the door. (For details on the place at which the protect film is to be attached, see "1.2 NOTES SPECIFIC TO THIS PRODUCT.")

Top panel F and R

Remove the three collar and three screws. (ABA1383)



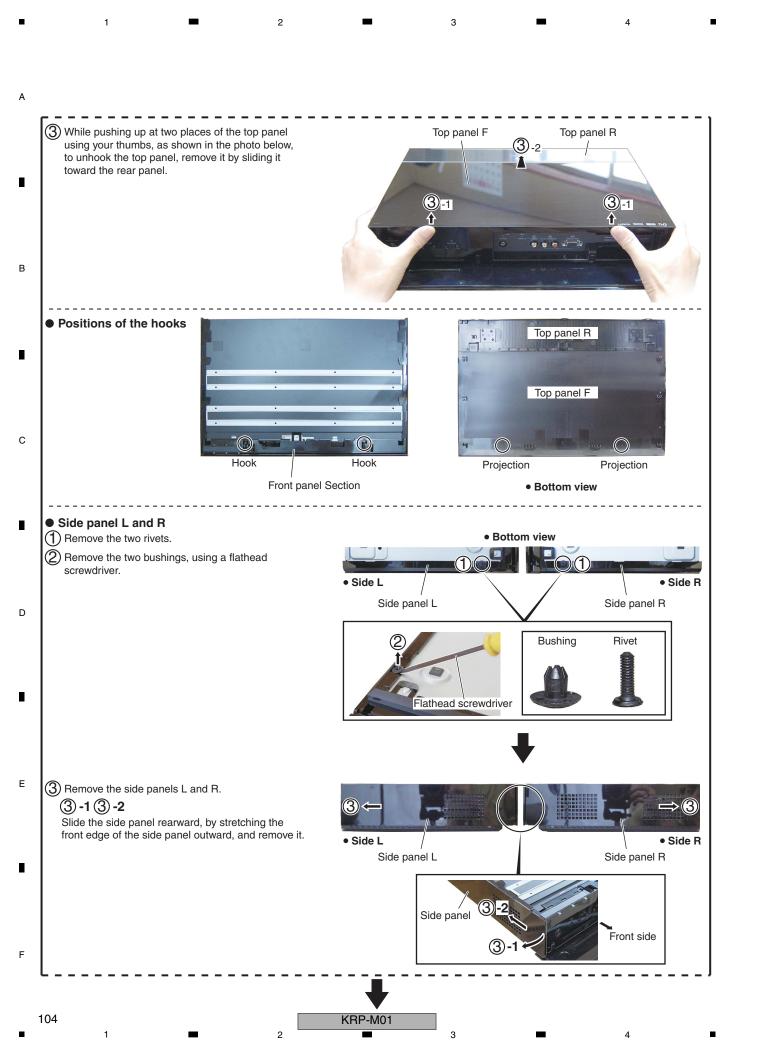


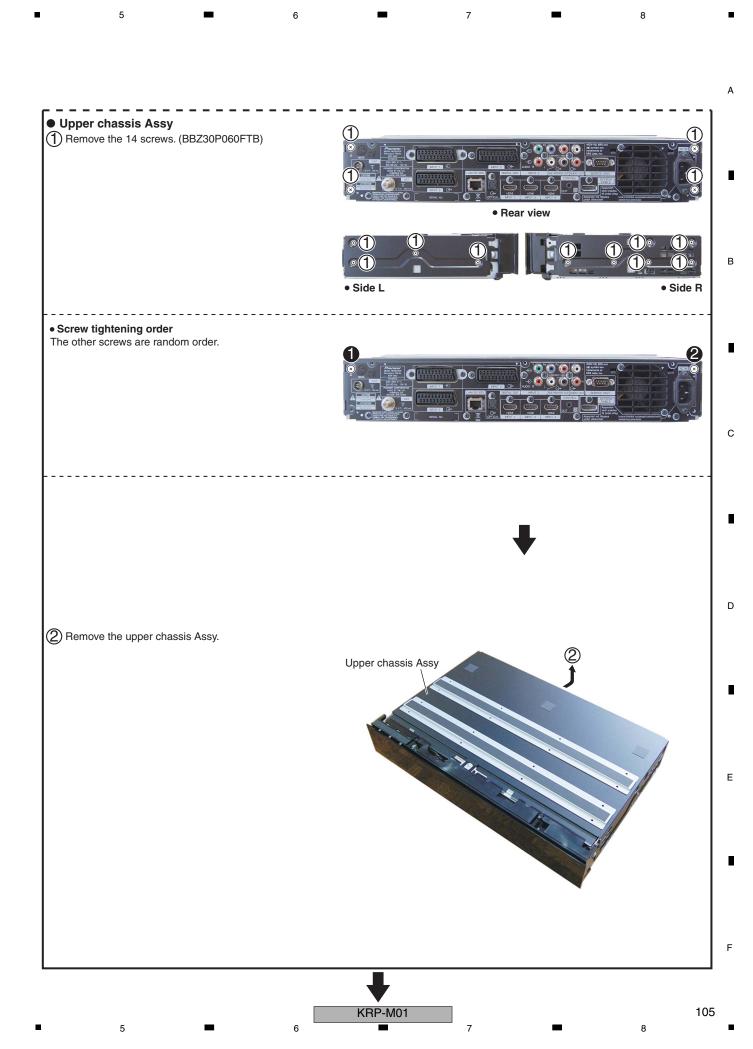
• Rear view





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1 2 3 4

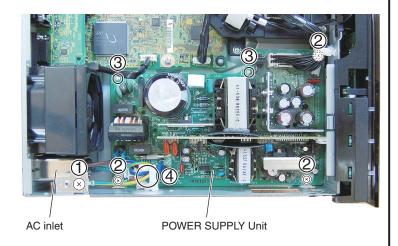
2 POWER SUPPLY Unit

- (1) Remove the one screw. (BMP40P080FSN)
- 2 Remove the three screws. (BBB30P080FSN)
- Remove the two circuit board spacers.
- (4) Release the jumper wire.

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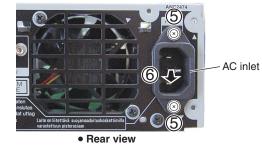
(5) Remove the two screws. (ABZ30P080FTB)

6 Remove the AC inlet.

• An installation direction of the AC inlet



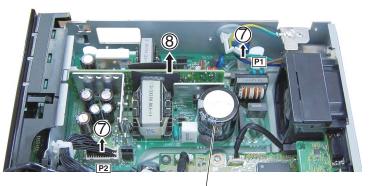






Disconnect the two connectors.

8 Remove the POWER SUPPLY Unit.



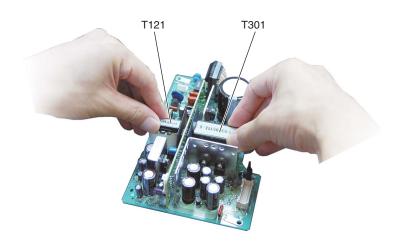
POWER SUPPLY Unit



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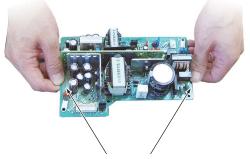
• How to lift up the POWER SUPPLY Unit

When you remove the POWER SUPPLY Unit from the chassis, first lift the board by pinching T121 and T301 transformers with your fingers. When the board is lifted up to a certain height, hold it by hand. NEVER hold the board by the radiator that is adjacent to the transformer.

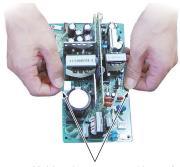


• How to hold the board after removing it from the chassis

The following two ways are recommended for holding the POWER SUPPLY Unit:

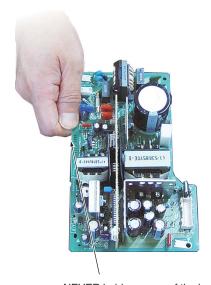


Hold at the center positions of both rims of the board.

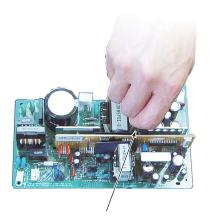


Hold at the center positions of both rims of the board.

Ways to be avoided:



NEVER hold a corner of the board with one hand.



NEVER hold the board by the radiator with one hand.

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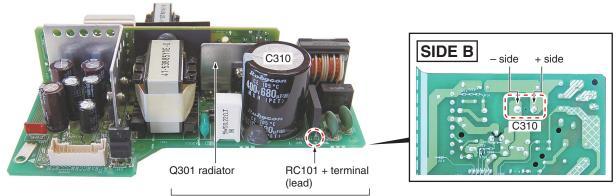
Note on Removing the POWER SUPPLY Unit from the Chassis and Method for Resetting Standby Power Latchup

For 3-5 minutes after the unit is turned off, residual electric charge remains in the C310 capacitor on the POWER SUPPLY Unit. Before removing the POWER SUPPLY Unit from the chassis, be sure to confirm that residual charge inside the POWER SUPPLY Unit has become sufficiently low. (Without forced discharge, residual charge that remains after 3-5 minutes will fall to one-tenth or less, which is still about 20 V. Therefore, even after the POWER SUPPLY Unit is removed, it is recommended to perform forced discharge on the POWER SUPPLY Unit, as shown below.)

For quick removal of residual charge, forced discharge is recommended, using two 220 ohm/10 W resistors (440 ohm/20 W).

How to remove the POWER SUPPLY Unit

- 1. Make sure that the AC power cord is unplugged. Using a tester, check the voltage between the + terminal of RC101 bridge diode and Q301 radiator (equivalent to the voltage between two electrodes of C310).
- 2. Let the unit sit for more than 5 minutes until the voltage equivalent to that between two electrodes of C310 falls to under 20 V.
- 3. After checking that the voltage is under 20 V, disconnect the connectors of the POWER SUPPLY Unit and remove the POWER SUPPLY Unit.
- 4. Using two resistors mentioned above, completely discharge residual charge from C310.

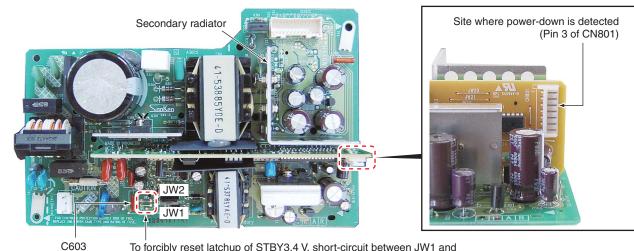


After checking that the voltage at the measurement points (equivalent to the voltage between two electrodes of C310) is under 20 V, remove the POWER SUPPLY Unit.

Then, completely discharge residual charge, using resistors.

How to reset Standby power latchup (In a case where the protection against Standby power excess voltage is activated)

- 1. After removing the causes of the malfunction, short-circuit between the JW1 and JW2 jumpers.
- 2. If the POWER SUPPLY Unit functions properly, after opening the above jumpers, the unit starts up.



To forcibly reset latchup of STBY3.4 V, short-circuit between JW1 and JW2 (near C603), using a flathead screwdriver or similar object.

If the causes of the malfunction are removed, after opening the jumpers, the unit starts up.

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3 REAR IO Assy

● FAN unit

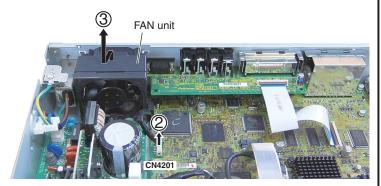
1 Remove the two screws. (BPZ30P080FTB)



Rear view



- 2 Disconnect the one connector.
- 3 Remove the FAN unit.





● REAR IO Assy

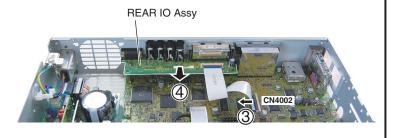
- 1 Remove the two hexagon headed screws. (ABA1382)
- (2) Remove the four screws. (BPZ30P080FTB)



Rear view



- 3 Disconnect the one flexible cable.
- A Remove the REAR IO Assy.



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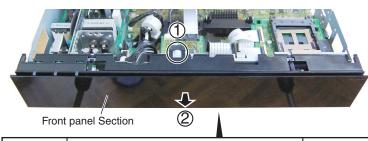
1 2 3 4

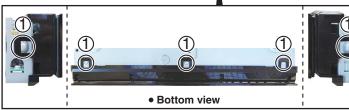
4 Front Panel Section

Front panel Section

1 Unhook the six hooks.

2 Remove the front panel Section.



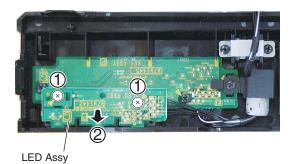




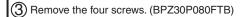
● LED and KEY Assys

(1) Remove the two screws. (BPZ30P080FTB)

(2) Remove the LED Assy.







(4) Remove the KEY Assy.

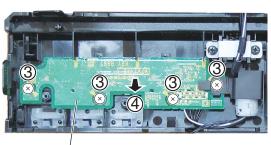
Note:

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Before tightening screws, make sure that the protect film has been attached.

(For details on the place at which the protect film is to be attached, see "1.2 NOTES SPECIFIC TO THIS PRODUCT.")



KEY Assy



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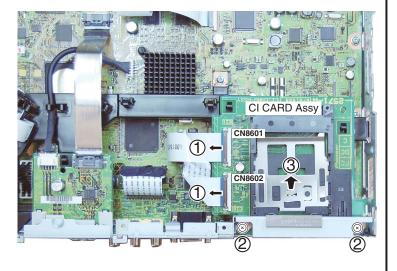
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5 CI CARD and FRONT_HDM_USB Assys

CI CARD Assy

- 1 Disconnect the two flexible cables.
- (2) Remove the two screws. (ABZ30P060FTC)
- Remove the CI CARD Assy.



● Note on Connection of the Flexible Flat Cable for the CI CARD Assy

How to Check for Inverse Connection

After connecting the FFC cable for the CI CARD Assy, make sure that the part number printed on the upper surface of the cable is ADD1567.

ADD1567	Correctly connected
ADD1566	Inversely connected

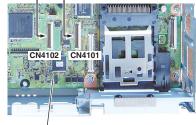
ADD1566



ADD1567



ADD1567 ADD1566

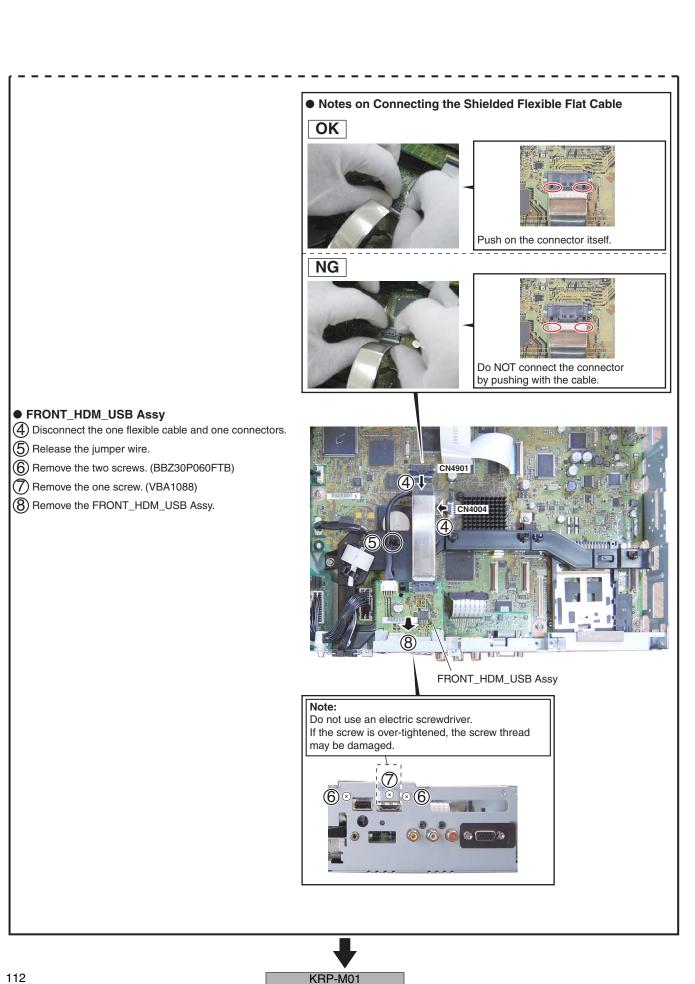


MAIN BLOCK Assy

Unit operation when the cable is inversely connected

Activated operation		Unit operation	
Unit When activated		It starts up properly.	
Slot 1: Lower slot	When the circuits in the Card block are activated	They operate properly.	
(mounted on the MAIN BLOCK Assy)	When a card is inserted in Slot 1	They operate properly.	
Slot 2: Upper slot	When the circuits in the Card block are activated	They are not activated (no risk of being damaged, though).	
(mounted on the CI CARD Assy)	When a card is inserted in Slot 2	They are not activated (no risk of being damaged, though).	

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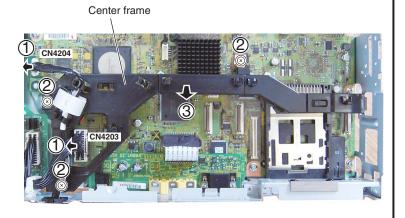
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6 FRONT IO Assy

Center frame

- 1 Disconnect the two connectors.
- Remove the three screws. (ABA1383)
- 3 Remove the center frame.





FRONT IO Assy

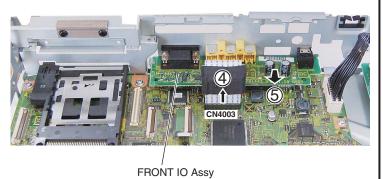
- 1 Remove the cover sheet.
- Remove the two hexagon headed screws. (ABA1382)
- (3) Remove the two screws. (BPZ30P080FTB)







- (4) Disconnect the one flexible cable.
- (5) Remove the FRONT IO Assy.



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7 MAIN BLOCK Assy

● Terminal panel (E)

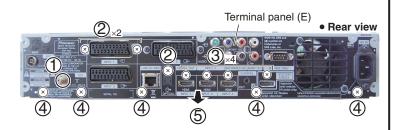
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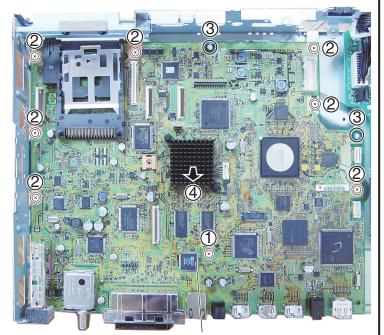
- Remove the one nut. (BBN1005)
- (2) Remove the three screws. (BPZ30P080FTB)
- Remove the four screws. (BMZ30P060FTB)
- (4) Remove the five screws. (BBZ30P060FTB)
- (5) Remove the terminal panel (E).





MAIN BLOCK Assy

- (1) Remove the one screw. (AMZ30P060FTB)
- 2 Remove the seven screws. (ABA1383)
- (3) Remove the two circuit board spacers.
- (4) Remove the MAIN BLOCK Assy.



MAIN BLOCK Assy

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8. EACH SETTING AND ADJUSTMENT



- 1. At shipment, the unit is adjusted to its best conditions. Normally, it is not necessary to readjust even if an assembly is replaced. If the adjustment is shifted or if it becomes necessary to readjust because of part replacement, etc., perform the adjustment as described below.
- 2. Any value changed in Service/Factory mode will be stored in memory as soon as it is changed. Before readjustment, take note of the original values for reference in case you need to restore the original settings.
- 3. Use a stable AC power supply.

8.1 ADJUSTMENT REQUIRED WHEN THE UNIT IS REPAIRED OR REPLACED

■ When any of the following assemblies is replaced

POWER SUPPLY Unit	→ (No adjustment required
MAIN BLOCK Assy (*)	• (Execute section [5-5] AUTO ADJUSTMENT of 6.2 [5] INITIALIZE.
Other assemblies	→ (No adjustment required

(*): When replacing the MAIN BLOCK Assy, be sure to perform the FINAL SETUP.

■ Replacement of the whole Assy is required when one of the following part on the corresponding Assy is in failure

PCB Assy No.	Assy Name	Ref No.	Function Name	Part No.	Reason
AXY1204	POWER SUPPLY Unit	U0003	_		The maker forbids Pioneer from repairing the Assy.
		IC6403	DTV Flash	S29GL512P10TFIR1 -K (AGC1089)	Because ID data (MAC address and data on keys) have been stored
		IC6001	SYSTEM IC (BCM7404)	BCM7404XKPB11G-K	
AWV2570	MAIN BLOCK	IC5002	HDCP EEPROM	BR24L02FV-W	
AWV2572	Assy	IC5003	HDCP EEPROM	BR24L02FV-W	
		IC5004	HDCP EEPROM	BR24L02FV-W	
		IC7301	FRONT HDCP EEPROM	BR24L02FV-W	
		IC7004	EMMA2 EEPROM	BR24L64F-W	
		IC6701	ARIA FLASH	S29GL016A90TFIR2 -K (AGC1088)	Because adjustments and data writing a the level of production line are required
		IC6811	IF UCOM	AGC1086	after replacement
		IC7202	EMMA2 FLASH	S29GL032N90TFIO4 -K (AGC1087)	
		IC6201	BCM DDR SDRAM	HY5DU121622DTP-D43-K	
		IC6202	BCM DDR SDRAM	HY5DU121622DTP-D43-K	
		IC6203	BCM DDR SDRAM	HY5DU121622DTP-D43-K	1
		IC6204	BCM DDR SDRAM	HY5DU121622DTP-D43-K	
AWV2571 (AWW1443)	FRONT_IO Assy	IC8501	PC EEPROM	BR24L01AFJ-W	Because adjustments and data writing at the level of production line are required after replacement

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■ Part whose replacement is difficult

PCB Assy No.	Assy Name	Ref No.	Function Name	Part No.	Reason	
		IC7003	SYSTEM IC (EMMA2)	UPD61123F1-100KA3A-K	Because these ICs are packaged in BGA	
AWV2570	MAIN BLOCK	IC6501	ASIC (ARIA)	PD6568A-K		
AWV2572	Assy	IC6702	DDR SDRAM (ARIA)	EDD1232ABBH-5C-E-K		
		IC6703	DDR SDRAM (ARIA)	EDD1232ABBH-5C-E-K		
		IC6704	DDR SDRAM (ARIA)	EDD1232ABBH-5C-E-K		
		IC4801	ADC	AD9985KSTZ	Because these ICs require readjustment	
		IC5101	AV SW	R2S11006FT	after replacement	
		IC5501	RGB SW	R2S11001FT		
		IC4702	VDEC	CM0048BF		
		U5301	DVB-T	AXF1191	Because the part has many pins (from	
		U5201	DVB-S2	AXF1195	G9, through-hole print will be adopted	
		JA5601	CI connector	AKP1341	Because the part has many pins	
		JA7502	Scart connector	AKP1265		
		JA8801	Scart connector	AKP1266		
		IC4901	HDMI	SII9135CTU-K	Because a radiation pad is provided	
		IC5201	S2 demodulation IC	STV-0903		
		IC4601	Regulator	LTC3407EMSE-2		
		IC4501	Regulator	BD8624EFV		
		IC4503	LNB Regulator	LNBH23PP-TBB		

■ Adjustment Procedures After a Part that Requires Readjustment is Replaced

Execute section "[5-5] AUTO ADJUST. <=>" of "6.2 [5] INITIALIZE."

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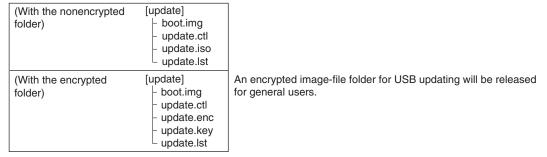
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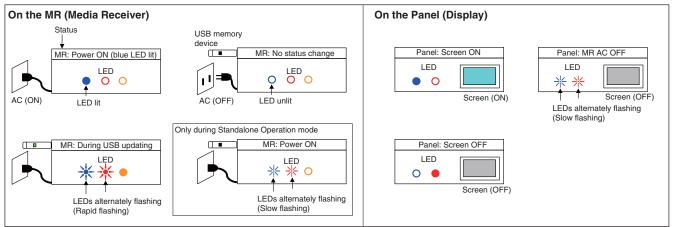
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■ Preparation Expand the image-file folder for USB updating in the root directory of the USB memory device.

Example: Folder construction after expansion in the root directory of the USB memory device

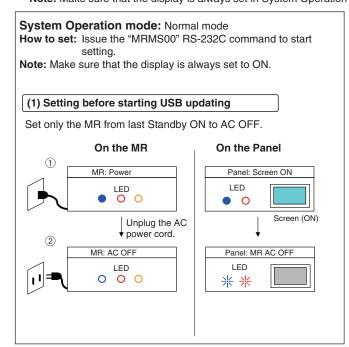


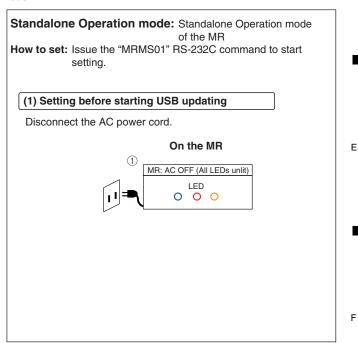
■ Description of the figures



■ Procedures

The methods for USB updating in System Operation mode and Standalone Operation mode of the MR are described below. Note: Make sure that the display is always set in System Operation mode.





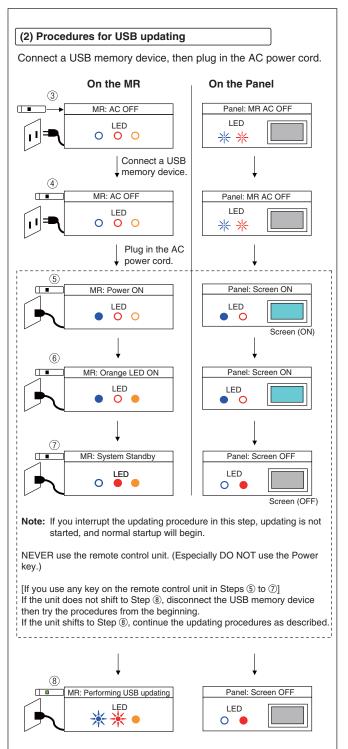
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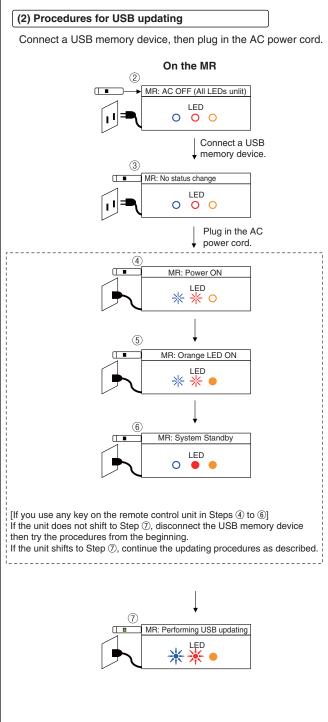


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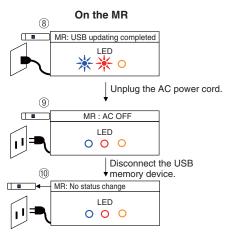


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(3) Completion procedures for USB updating

After USB updating is completed, perform the following steps (unplug the AC power cord, disconnect the USB memory device, then plug the AC power cord back in).



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Return the MR to System Operation mode, by sending a command via the RS-232C connector.

How to set: Send the "MRMS00" command via the RS-232C connector.

In Standalone Operation mode of the MR, the GUI indicating that updating is completed is not displayed.

■ List of frequency of LED (orange) flashing when updating fails

is completed is displayed.

If updating is interrupted, the orange LED flashes to warn you of the error.



Frequency of Orange LED Flashing	Error Content	Details
1	(Not used)	
2	Version error	The same version or a newer version of software has already been loaded.
3	USB update startup error	Startup of USB updating failed.
4	DTV Update Error	Updating of the DTV software failed.
5	MAIN Download Error	Updating of the MAIN microcomputer software failed.
6	ARIA Download Error	Updating of the ASIC software in the previous stage failed.
7	ZEUS Download Error	Updating of the ASIC software in the later stage failed.
8	Module Download Error	Updating of the module microcomputer software failed.
9	IF Download Error	Updating of the IF microcomputer software failed.
10	USB disconnection	Abnormality in the USB memory device
11 to 13	Reserved	-
14	Destination error	The software for a different destination (Europe/North America/Australia) was used for updating.

Example: In a case where the orange LED flashes twice (version error)

Repetition of 1-sec flashing twice followed by a 2.5-sec pause (OFF)

Under the following conditions, USB updating procedures will be interrupted at Step 5 above, and normal startup will begin, but the LED does not flash for error indication.

Conditions under which the LED will not flash for error indication

- Any USB updating file is damaged
- Not all USB updating files are stored in the USB memory device
- The USB updating files are modified
- The USB memory device is defective

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8.3 HOW TO UPDATE DISPLAY PORT FIRMWARE

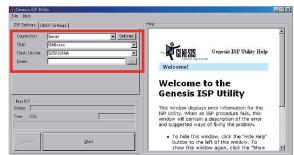
1. Preparation of Tools

- Activate the "ISPUtility xxxxxxxx.exe" file to install the ISP Utility.
 - On each screen, select "Next" until the installation wizard is finished.
- Activate the "CGProbe Redistributable xxxx.exe" file.On each screen, select "Next" until the wizard is finished.
- 3. Place the following files in the designated paths: chip.xml
 - C:\forage Program Files\forage Genesis Microchip\forage ISP Utility\forage SAFELite-ISP_S25FL016A.hex
 - C:\Program Files\Genesis Microchip\ISP Utility\Isp\safe-lite

Note: If you changed the program installation path, the above-mentioned paths may be different.

2. Updating

- 1. Connect the PC with the Media Receiver (MR) or Panel (Display), using an RS-232C straight cable.
- 2. Set the connected MR or Panel to Standby mode.
- Disconnect the DP cable.
 - 4. Start up the program for sending RS-232C commands: Baud rate: 9600
 - COM port: Select, according to the environment of the PC.
 - 5. Send the "UFW" command. Check that the red and blue LEDs flash.
- 6. Issue a command corresponding to the firmware to be updated.
 - [In a case where the DP firmware on the MR is updated] Issue the "DPT" command.
 - [In a case where the DP firmware on the display is updated] Issue the "DPR" command.
- With the program for sending RS-232C commands, terminate the connection.
 - 8. Start up the ISP Utility program and set up the ISP Settings screen.

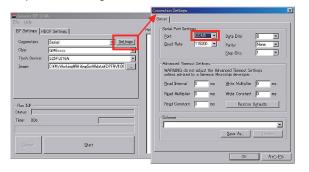


Connection: Serial Chip: GM6xxxx

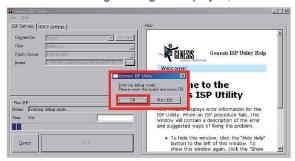
Flash Device: S25FL016A

Image: Select the ".hex" file to write to.

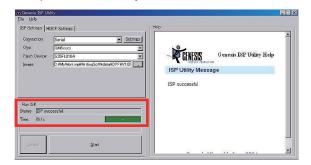
When "Serial" is selected in the "Connection" box, selection of Com ports is enabled. Click on "Settings" then select a Com port, according to the environment of the PC.



- After all necessary settings are completed, click on Start to start updating.
- 10. When the following message is displayed, click on OK.



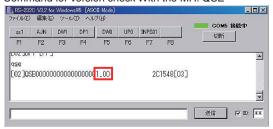
 The current status is displayed in the "Status" box. When "ISP Successful" is displayed, updating is completed successfully.



12. Terminate the utility program and turn the MR or display off then back on again.

With the program for sending RS-232C commands, reestablish the connection. Then send the command for version check.

Command for version check With the MR: QSE



Command for version check with the display: QSB



13. Check that the version has been properly updated. This completes the updating procedures.

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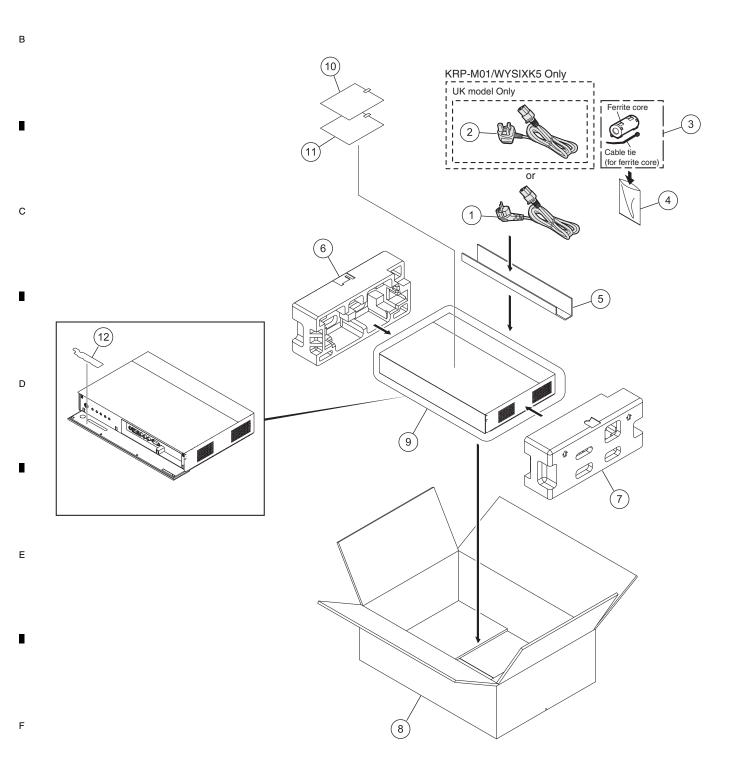
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9. EXPLODED VIEWS AND PARTS LIST

 $NOTES: \bullet \textit{Parts marked by "NSP" are generally unavailable because they are not in our \textit{Master Spare Parts List}.$

- The extstyle - Screws adjacent to ▼ mark on product are used for disassembly.
- For the applying amount of lubricants or glue, follow the instructions in this manual. (In the case of no amount instructions, apply as you think it appropriate.)

■ 9.1 PACKING SECTION



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(1) PACKING SECTION PARTS LIST

<u> Mark</u>	<u>No.</u>	<u>Description</u>	Part No.
<u> </u>	1	Power Cable	ADG1214
<u> </u>	2	Power Cable	See Contrast table (2)
<u> </u>	3	Ferrite Core (L5208)	ATX1039
	4	Vinyl Bag	AHG1337
	5	ACC Carton	See Contrast table (2)
	6	Pad L	See Contrast table (2)
	7	Pad R	See Contrast table (2)
	8	Carton	See Contrast table (2)
	9	Mirror Mat	AHG1420
	10	Caution Card	See Contrast table (2)
	11	Film Caution Card	See Contrast table (2)
	12	Protect Film	GGP1121

(2) CONTRAST TABLE KRP-M01/WYSIXK5 and WYSXJ5 are constructed the same except for the following:

Mark	No.	Symbol and Description	KRP-M01/WYSIXK5	KRP-M01/WYSXJ5
Ŀ	2	Power Cable	ADG1223	Not used
	5	ACC Carton (E)	AHD3677	Not used
	5	ACC Carton (G)	Not used	AHD3679
	6	Pad L (E)	AHA2735	Not used
	6	Pad L (G)	Not used	AHA2739
	7	Pad R (E)	AHA2736	Not used
	7	Pad R (G)	Not used	AHA2740
	8	Carton (E)	AHD3674	AHD3725
	10	Caution Card	ARM1439	ARM1440
	11	Film Caution Card	ARM1448	ARM1449

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(1) EXTERIOR SECTION PARTS LIST

Mark No.	<u>Description</u>	Part No.	Mark 1	<u> No.</u>	<u>Description</u>	Part No.
1	FRONT_HDM_USB Assy	AWW1412		21	Center Frame	AMR3844
2	FRONT IO Assy	AWW1443	<u> </u>	22	Gasket HP	ANK1994
3	CI CARD Assy	AWW1444		23	Rubber Foot	VEB1349
<u> </u>	Ferrite Core (F1001)	ATX1034		24	Top Panel F	AAK2940
5	••••			25	Side Panel L	AAK2941
<u>^</u> 6	Ferrite Core (F1)	ATX1073		26	Side Panel R	AAK2942
7	Flexible Cable (J201)	ADD1564		27	Top Panel R	AAK2946
8	Flexible Cable (J204)	ADD1566		28	Ferrite Stopper	AEC1981
9	Flexible Cable (J205)	ADD1567	<u> </u>	29	Earth Plate MAIN	ANG3219
10	30P Shield FFC (J101)	ADF1042	<u> </u>	30	Gasket UP2	ANK1999
11	USB Cable (J102)	ADX3713	<u> </u>	31	Gasket CI	ANK1996
12	Upper Chassis Assy	See Contrast table (2)		32	Rivet A	BEC1158
13	PCB Holder	See Contrast table (2)		33	••••	
14	Cover Sheet	AAK2850		34	••••	
15	Collar	ABN1095		35	Hexagon Headed Screw	ABA1382
16	Upper Cushion	AEB1504		36	Screw	ABA1383
17	Top Cushion	AEB1505		37	Screw	ABA1391
18	Scrivet	AEC1657		38	Screw	ABZ30P060FTC
19	Ferrite Core Holder	AEC1818		39	Screw	BBZ30P060FTB
20	Edge Saddle	AEC1946		40	Screw	BPZ30P080FTB
				41	Screw (FE)	VBA1088

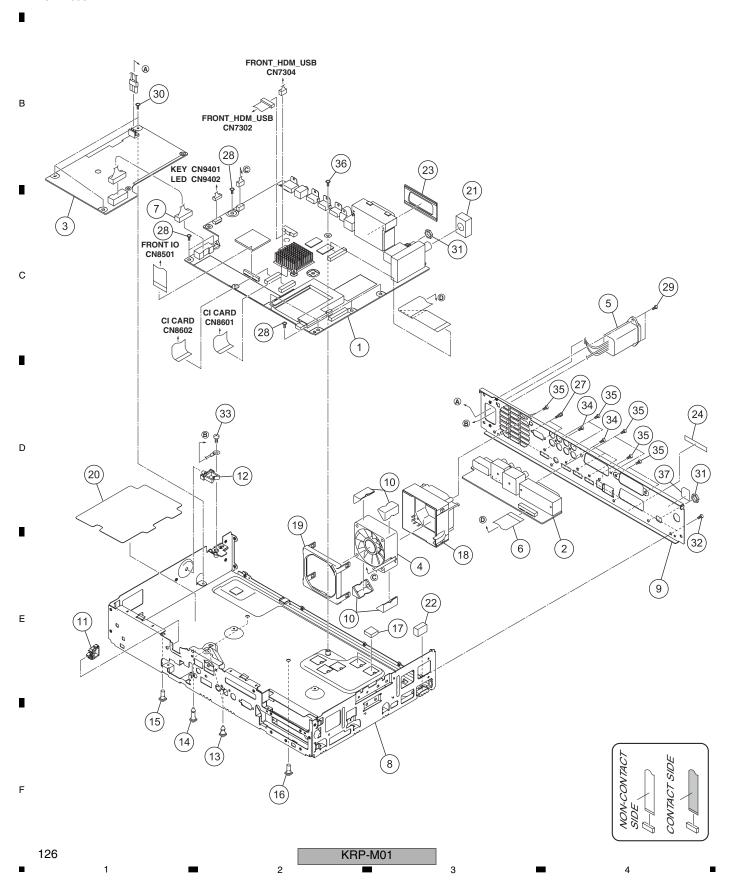
(2) CONTRAST TABLE KRP-M01/WYSIXK5 and WYSXJ5 are constructed the same except for the following:

Mark	No.	Symbol and Description	KRP-M01/WYSIXK5	KRP-M01/WYSXJ5
	12	Upper Chassis Assy	ANA2187	ANA2224
	13	PCB Holder	ANG3186	ANG3217

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9.3 BOTTOM SECTION

Cleaning paper : GED-008



(1) BOTTOM SECTION PARTS LIST

<u>Mark</u>	No.	<u>Description</u>	Part No.	Mark I	<u>No.</u>	<u>Description</u>	Part No.
	1	MAIN BLOCK Assy	AWW1413	<u> </u>	21	Gasket EU	ANK1972
	2	REAR IO Assy	AWW1441		22	Gasket MA	ANK1985
<u> </u>	3	POWER SUPPLY Unit	AXY1204	\triangle	23	Gasket SC	ANK1989
<u> </u>	4	DC FAN Motor 60 x 25L	AXM1068	NSP	24	Serial Label	ARW1100
\triangle	5	AC Inlet (CN1)	AKP1339		25	••••	
	6	Flexible Cable (J203)	ADD1565		26	••••	
	7	26P Housing Wire (J111)	ADX3674		27	Hexagon Headed Screw	ABA1382
	8	Base Chassis Assy	See Contrast table (2)		28	Screw	ABA1383
	9	Terminal Panel (E)	See Contrast table (2)		29	Screw	ABZ30P080FTB
	10	Floating Rubber 60	AEB1410		30	Screw	BBB30P080FSN
	11	Reuse Clamp	AEC2129		31	Washer Faced Nut	BBN1005
	12	Reuse Wire Saddle	AEC2134		32	Screw	BBZ30P060FTB
	13	Circuit Board Spacer	AEC2150		33	Screw	BMP40P080FSN
	14	Circuit Board Spacer	AEC2151		34	Screw	BMZ30P060FTB
	15	Circuit Board Spacer	AEC2152		35	Screw	BPZ30P080FTB
	16	Circuit Board Spacer	AEC2163		36	Screw	AMZ30P060FTB
	17	Silicon Sheet	AEH1182	NSP	37	Gost-R Label	ARW1126
	18	FAN Holder 60 A	See Contrast table (2)				
	19	FAN Holder 60 B	See Contrast table (2)				
	20	Insulation Sheet	AMR3891				

(2) CONTRAST TABLE KRP-M01/WYSIXK5 and WYSXJ5 are constructed the same except for the following:

			· · · · · · · · · · · · · · · · · · ·	
Mark	No.	Symbol and Description	KRP-M01/WYSIXK5	KRP-M01/WYSXJ5
	8	Base Chassis Assy	ANA2186	ANA2225
	9	Terminal Panel (E)	ANC2474	ANC2480
	18	FAN Holder 60 A	AMR3845	AMR3918
	19	FAN Holder 60 B	AMR3846	AMR3919

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MAIN BLOCK CN4204

A disassembly direction of the door section

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FRONT PANEL SECTION PARTS LIST

Mark No.		<u>Description</u>	Part No.
	1	LED Assy	AWW1442
	2	KEY Assy	AWW1445
	3	20P Housing Wire (J112)	ADX3714
<u> </u>	4	Ferrite Core (F1002)	ATX1069
	5	F Panel Assy (EU)	AMB3114
NSP	6	Damper Holder	ANG3198
	7	Magnet Holder Assy	AEC1077
	8	Damper	AXA1022
NSP	9	Shading Sheet	AMR3903
NSP	10	Front Panel	AMB3083
NSP	11	Control Button	AAD4160
	12	Input Sheet (E)	AAL3037
NSP	13	Front LED Lens L	AMR3841
NSP	14	Front LED Lens C	AMR3904
NSP	15	Front LED Lens R	AMR3905
	16	Screw	BPZ30P080FTB
	17	Door Panel Service Kit	GXX1283
	18	Door Catcher	••••
	19	Door Base	••••
	20	Door Panel	••••
	21	Door Cushion	AED1337
	22	Ferrite Holder	AMR3925
	23	••••	
	24	••••	
	25	Screw	BPZ30P080FTB

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■ When Replacing the F PANEL Assy (E)

When replacing the F PANEL Assy (E), discard the following parts of the new Assy kit for service and use the parts from the original door panel:

No.18 Door catcher No.19 Door base No.21 Door cushion

Reassembly Procedures for the Door Panel Service Kit

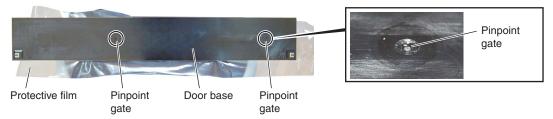
• Component parts of the GXX1283 Door Panel Service Kit

No.18 Door catcher (x2)
No.19 Door base (x1)
No.20 Door panel (x1)
No.21 Door cushion (x2)

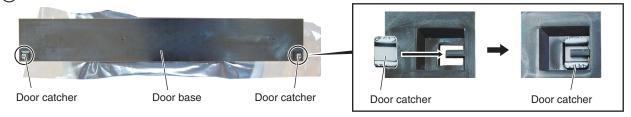
Check that two marks of pinpoint gates do not protrude from the surface of the door base to which the door panel is to be attached.

Do NOT peel off the protective film of the door base in this step.

Peel it off after all the reassembly procedures are completed.



2 Attach the two door catchers.



Peel off the separator of double-back tape on the door panel.

Do NOT peel off the protective film on the exterior surface of the door panel in this step.

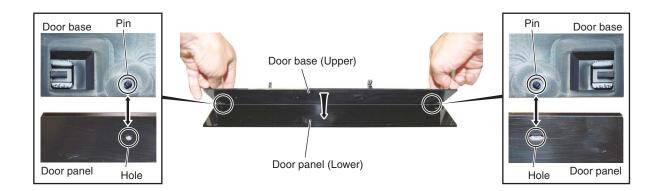
Peel it off after all the reassembly procedures are completed.

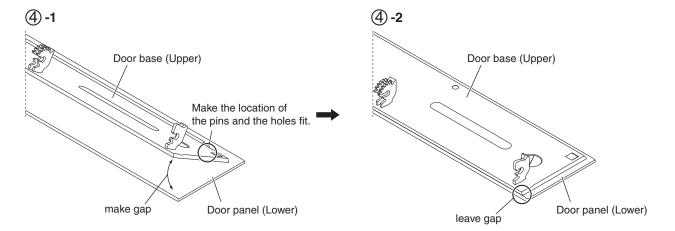
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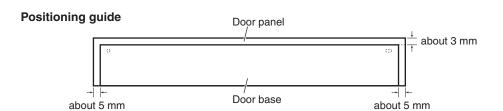
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Align the two positioning pins of the door base with the holes in the door panel.

When positioning, leave gaps between the door panel and door base, as shown in the figure below:







- (5) Stick the door base and door panel together, by pressing them all over.
- (6) Attach the two door cushions.

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